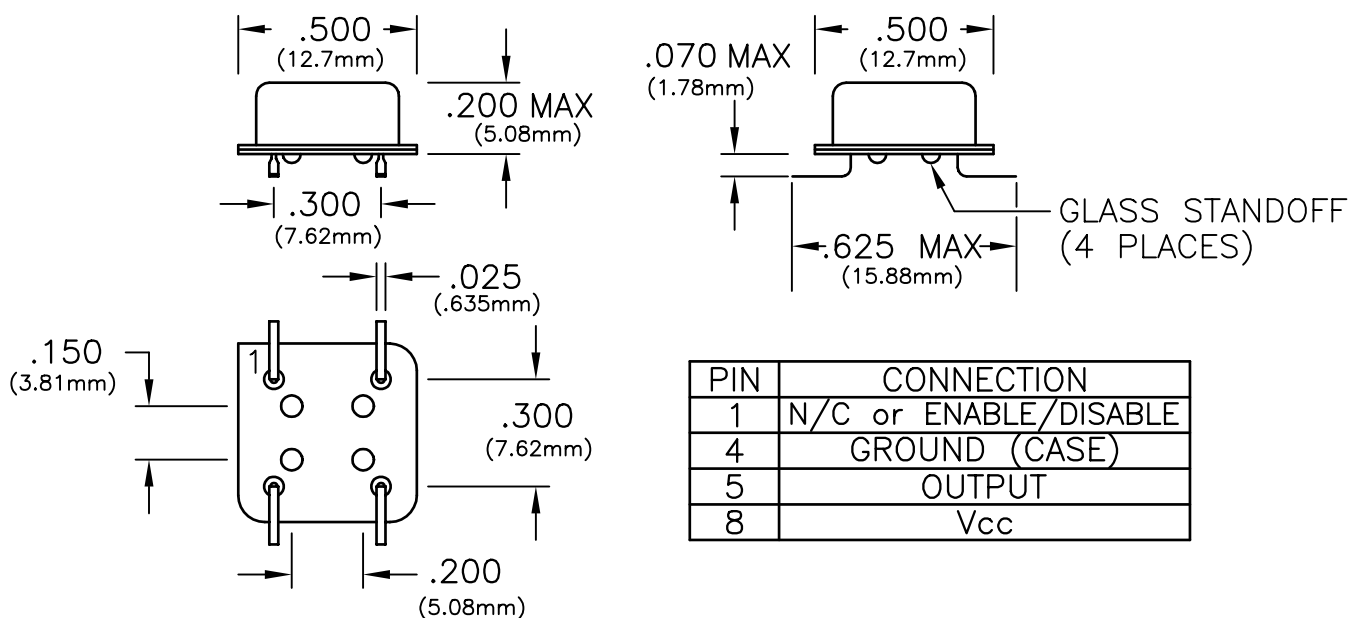


**3 VOLT LOGIC CLOCK OSCILLATORS**

SPECIFICATIONS	JA53AA	JA53RA	
Frequency Range	13MHz to 75MHz		
Frequency Stability	±100ppm		
Temperature Range	0°C to +70°C		
Supply Voltage	3Vdc Minimum , 6Vdc Maximum		
Supply Current	10mA Typical , 50mA Maximum		
Output	Waveform	CMOS Squarewave , TTL Compatible	
	Load	30pF Maximum	
	Voltage Voh	Vcc - .5V Minimum	
	Vol	.44V Maximum	
	Current Ioh	-24mA	
	Iol	24mA	
	Duty Cycle	45/55 Maximum Measured at 2V	
Rise/Fall Time	1nS Typical , 3nS Maximum		
Input	Enable/Disable	N/A	Pin 1
	Output Enable/Disable Time	N/A	100nS
	Enable Vih	N/A	2.1V Minimum
	Disable	N/A	0.8V Maximum
Oscillator output is enabled with no connection on pin 1			
Start Up Time	10mS		
Package	All metal, hermetically sealed, welded package		



PIN	CONNECTION
1	N/C or ENABLE/DISABLE
4	GROUND (CASE)
5	OUTPUT
8	Vcc