

STW3040

High voltage fast-switching NPN power transistor

Preliminary Data

Features

- High voltage capability
- High DC current gain
- Minimum lot to lot spread for reliable operation
- Wide safe operating areas (forward and reverse biased)

Application

■ Switching mode power supplies

Description

The STW3040 is manufactured using diffused collector in planar technology adopting base island layout. The device is designed for use in SMPS and desktop power supply.

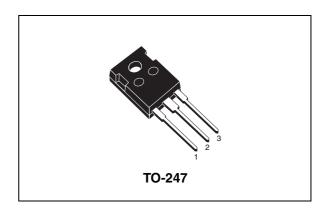


Figure 1. Internal schematic diagram

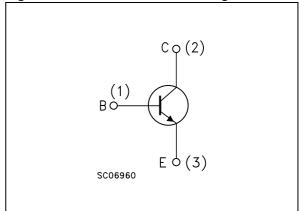


Table 1. Device summary

Order code	Marking	Package	Packaging
STW3040	W3040	TO-247	Tube

Contents STW3040

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1 Absolute maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CES}	Collector-emitter voltage (V _{CE} = 0)	700	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	400	V
V _{EBO}	Emitte-base voltage ($I_C = 0$)	9	V
I _C	Collector current	30	Α
I _{CM}	Collector peak current	50	Α
I _B	Base current	10	Α
I _{BM}	Base peak current	15	Α
P _{TOT}	Total dissipation at T _c = 25°C	160	W
T _{stg}	Storage temperature	-65 to 150	°C
T _J	Max. operating junction temperature	150	°C

Table 3. Thermal data

Symbol	Parameters		Value	Unit
R _{thj-case}	Thermal resistance junction-case m		0.75	°C/W

2 Electrical characteristics

(T_{case} = 25°C; unless otherwise specified)

Table 4. Electrical characteristics

Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
I _{CES}	Collector cut-off current (V _{BE} = 0)	V _{CE} = 700 V	,			1	mA
I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} = 9 V				1	mA
V _{(BR)CEO}	Collector-emitter breakdown voltage (I _B = 0)	I _C = 10 mA		400			V
v (1)	Collector-emitter	$I_C = 6 A$	$I_B = 1.2 A$		0.1		V
V _{CE(sat)} ⁽¹⁾	saturation voltage	I _C = 20 A	$I_B = 4 A$		0.4	0.8	V
v (1)	Base-emitter saturation	I _C = 6 A	I _B = 1.2 A			1.2	V
V _{BE(sat)} ⁽¹⁾	voltage	I _C = 20 A	$I_B = 4 A$			1.5	V
		$I_C = 10 \text{ mA}$	V _{CE} = 5 V	10			
h _{FE} ⁽¹⁾	DC current gain	$I_C = 6 A$	$V_{CE} = 5 V$	18	30	40	
		I _C = 20 A	$V_{CE} = 5 V$	10			
	Resistive load						
t _{on}	Turn-on time		$V_{CC} = 200 \text{ V}$			0.5	μs
t _s	Storage time	$I_{b(on)} = 4 A$	$I_{b(off)} = -8 A$			2.5	μs
t _f	Fall time					0.3	μs

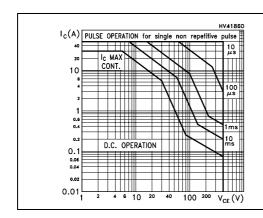
^{1.} Pulsed duration = 300 ms, duty cycle $\leq 1.5\%$

STW3040 Electrical characteristics

2.1 Electrical characteristics (curves)

Figure 2. Safe operating area

Figure 3. Derating curve



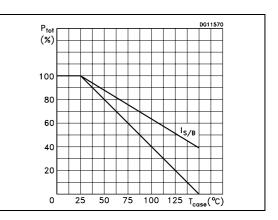
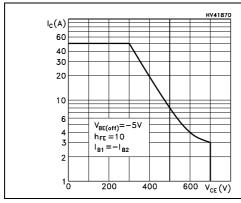


Figure 4. Reverse biased safe operating area

Figure 5. Output characteristics



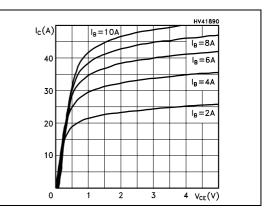
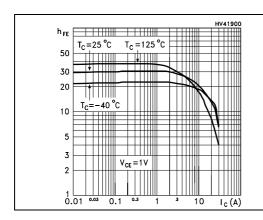
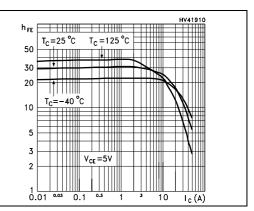


Figure 6. DC current gain

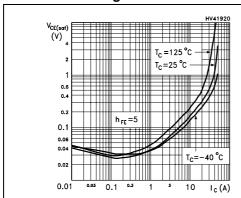
Figure 7. DC current gain





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Figure 8. Collector-emitter saturation Figure 9. E voltage



gure 9. Base-emitter saturation voltage

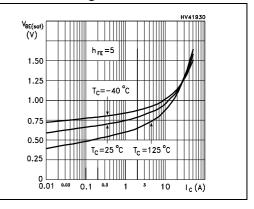
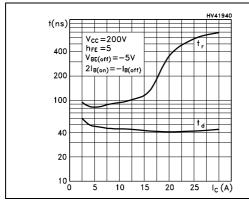
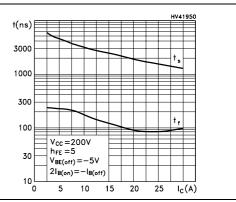


Figure 10. Resistive load switching on time

Figure 11. Resistive load switching off time

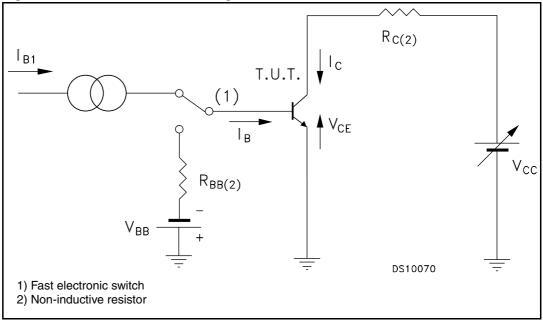




STW3040 Test circuit

3 Test circuit

Figure 12. Resistive load switching test circuit



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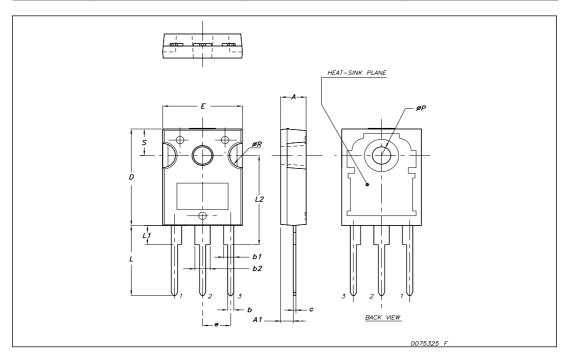
4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com



TO-247 Mechanical data

Dim.	mm.				
5	Min.	Тур	Max.		
Α	4.85		5.15		
A1	2.20		2.60		
b	1.0		1.40		
b1	2.0		2.40		
b2	3.0		3.40		
С	0.40		0.80		
D	19.85		20.15		
Е	15.45		15.75		
е		5.45			
L	14.20		14.80		
L1	3.70		4.30		
L2		18.50			
øΡ	3.55		3.65		
øR	4.50		5.50		
S		5.50			



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Revision history STW3040

5 Revision history

Table 5. Document revision history

Date	Revision	Changes
12-Dec-2007	1	Initial release

STW3040

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