

2SJ543 Silicon P Channel MOS FET

REJ03G0890-0400 (Previous: ADE-208-652B) Rev.4.00 Sep 07, 2005

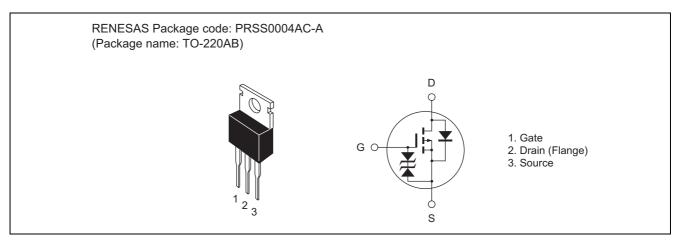
Description

High speed power switching

Features

- Low on-resistance $R_{DS (on)} = 0.042 \Omega$ typ.
- Low drive current.
- 4 V gate drive devices.
- High speed switching.

Outline





Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Value	Unit
Drain to source voltage	V _{DSS}	-60	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	ID	-20	A
Drain peak current	I _{D (pulse)} Note 1	-80	A
Body to drain diode reverse drain current	I _{DR}	-20	A
Avalanche current	I _{AP} Note 3	-20	A
Avalanche energy	E _{AR} Note 3	34	mJ
Channel dissipation	Pch Note 2	75	W
Channel temperature	Tch	150	٥C
Storage temperature	Tstg	-55 to +150	℃

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at Tc = 25°C

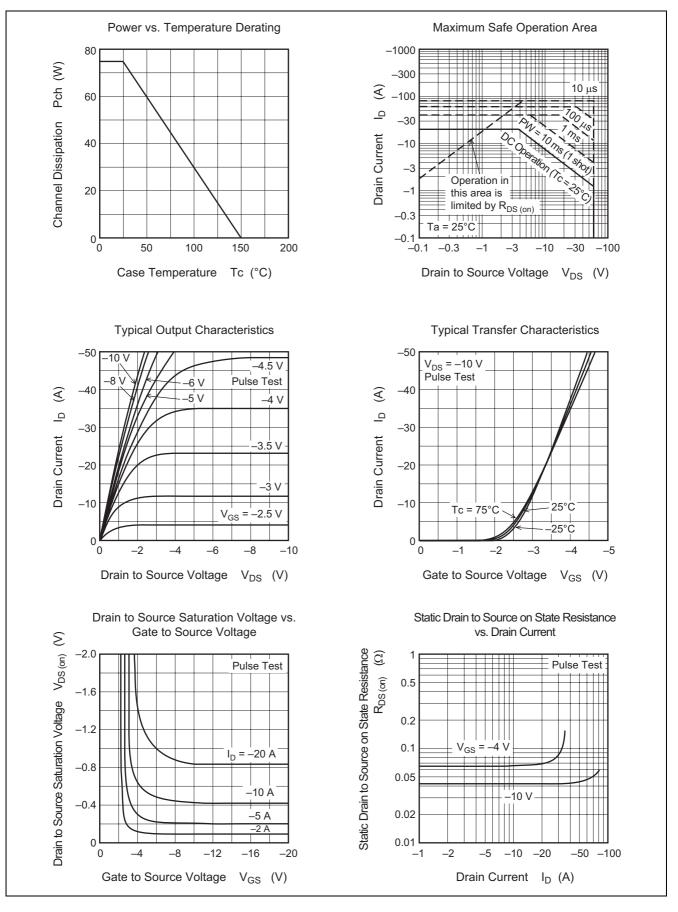
3. Value at Tch = 25° C, Rg $\geq 50 \Omega$

Electrical Characteristics

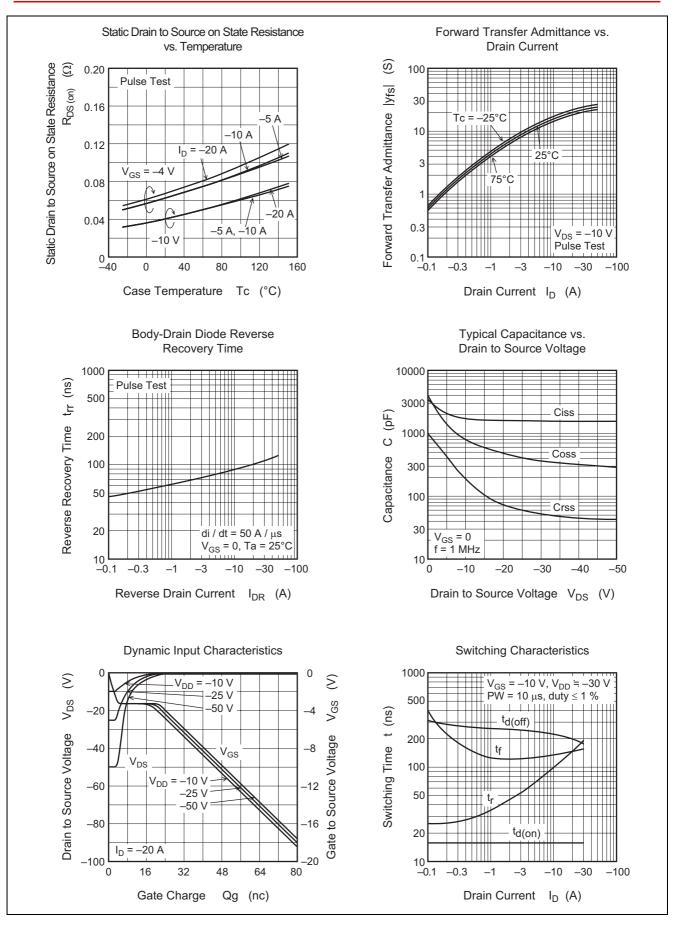
						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V (BR) DSS	-60	—	—	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V (BR) GSS	±20	—	—	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	—	—	-10	μA	$V_{DS} = -60 V, V_{GS} = 0$
Gate to source leak current	I _{GSS}	—	—	±10	μA	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	V _{GS (off)}	-1.0	—	-2.0	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}$
Static drain to source on state resistance	R _{DS (on)}	—	0.042	0.055	Ω	$I_D = -10 \text{ A}, \text{ V}_{GS} = -10 \text{ V}^{Note 4}$
	R _{DS (on)}	_	0.065	0.095	Ω	$I_D = -10 \text{ A}, \text{ V}_{GS} = -4 \text{ V}^{\text{Note 4}}$
Forward transfer admittance	y _{fs}	10	16	—	S	$I_D = -10 \text{ A}, V_{DS} = -10 \text{ V}^{Note 4}$
Input capacitance	Ciss	_	1750	—	pF	$V_{DS} = -10 V$
Output capacitance	Coss	_	800	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	180	—	pF	f = 1 MHz
Turn-on delay time	t _{d (on)}	_	16	—	ns	$V_{GS} = -10 \text{ V}$
Rise time	tr	—	100	_	ns	$I_{\rm D} = -10 \ {\rm A}$
Turn-off delay time	t _{d (off)}	_	230	—	ns	$R_L = 3 \Omega$
Fall time	t _f		140		ns	
Body to drain diode forward voltage	V _{DF}	—	-1.0	—	V	$I_F = -20 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t _{rr}		100		ns	$I_F = -20 \text{ A}, V_{GS} = 0$
						di _F /dt = 50 A/µs

Note: 4. Pulse test

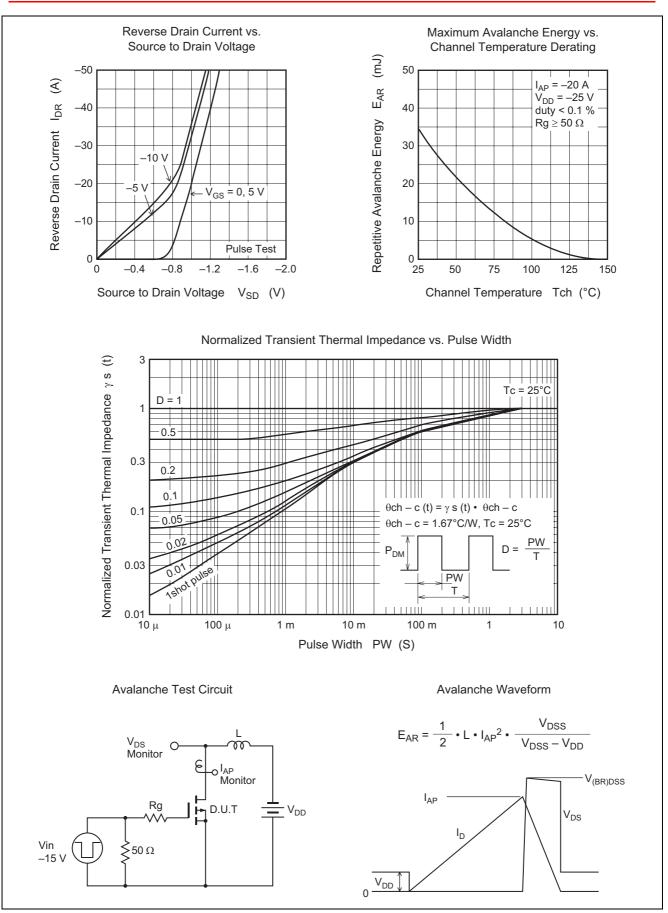
Main Characteristics



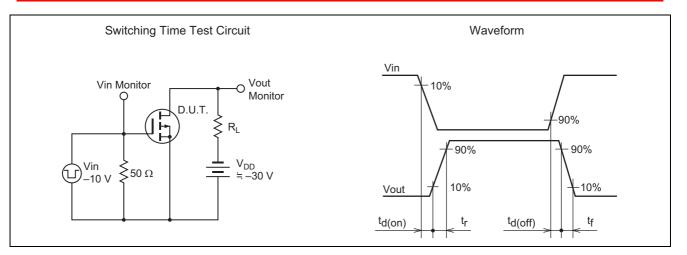






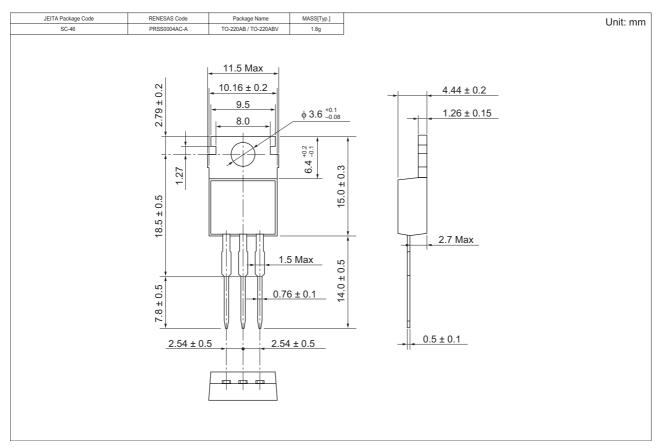








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SJ543-E	500 pcs	Box (Sack)

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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