

FMBL1G100US60

Molding Type Module

General Description

Fairchild's Insulated Gate Bipolar Transistor (IGBT) power modules provide low conduction and switching losses as well as short circuit ruggedness. They are designed for applications such as motor control, uninterrupted power supplies (UPS) and general inverters where short circuit ruggedness is a required feature.

Features

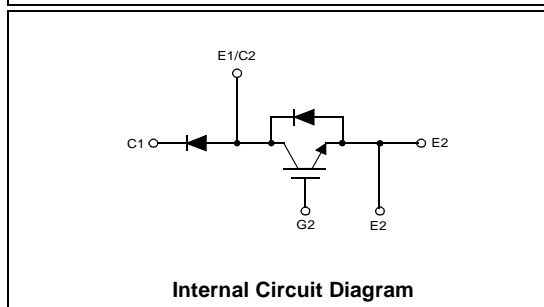
- UL Certified No. E209204
- Short circuit rated 10us @ $T_C = 100^\circ\text{C}$, $V_{GE} = 15\text{V}$
- High speed switching
- Low saturation voltage : $V_{CE(sat)} = 2.2\text{V}$ @ $I_C = 100\text{A}$
- High input impedance
- Fast & soft anti-parallel FWD

Application

- Boost (Step Up) Converter



Package Code : 7PM-AA



Internal Circuit Diagram

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Description	FMBL1G100US60	Units
V_{CES}	Collector-Emitter Voltage	600	V
V_{GES}	Gate-Emitter Voltage	± 20	V
I_C	Collector Current @ $T_C = 25^\circ\text{C}$	100	A
$I_{CM(1)}$	Pulsed Collector Current	200	A
I_F	Diode Continuous Forward Current @ $T_C = 100^\circ\text{C}$	100	A
I_{FM}	Diode Maximum Forward Current	200	A
T_{SC}	Short Circuit Withstand Time @ $T_C = 100^\circ\text{C}$	10	us
P_D	Maximum Power Dissipation @ $T_C = 25^\circ\text{C}$	400	W
T_J	Operating Junction Temperature	-40 to +150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-40 to +125	$^\circ\text{C}$
V_{iso}	Isolation Voltage @ AC 1minute	2500	V
Mounting Torque	Power Terminals Screw : M5	2.0	N.m
	Mounting Screw : M5	2.0	N.m

Notes :

(1) Repetitive rating : Pulse width limited by max. junction temperature

Electrical Characteristics of IGBT $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
Off Characteristics						
BV_{CES}	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_C = 250\mu A$	600	--	--	V
$\Delta BV_{CES}/\Delta T_J$	Temperature Coeff. of Breakdown Voltage	$V_{GE} = 0V, I_C = 1mA$	--	0.6	--	V/ $^\circ\text{C}$
I_{CES}	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$	--	--	250	μA
I_{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$	--	--	± 100	nA
On Characteristics						
$V_{GE(th)}$	G-E Threshold Voltage	$I_C = 0V, I_C = 100mA$	5.0	--	8.5	V
$V_{CE(sat)}$	Collector to Emitter Saturation Voltage	$I_C = 100A, V_{GE} = 15V$	--	2.2	2.8	V
Dynamic Characteristics						
C_{ies}	Input Capacitance	$V_{CE} = 30V, V_{GE} = 0V,$ $f = 1MHz$	--	10840	--	pF
C_{oes}	Output Capacitance		--	963	--	pF
C_{res}	Reverse Transfer Capacitance		--	228	--	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{CC} = 300V, I_C = 100A,$ $R_G = 2.4\Omega, V_{GE} = 15V$ Inductive Load, $T_C = 25^\circ\text{C}$	--	25	--	ns
t_r	Rise Time		--	70	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	120	150	ns
t_f	Fall Time		--	86	200	ns
E_{on}	Turn-On Switching Loss		--	4.0	--	mJ
E_{off}	Turn-Off Switching Loss	--	2.6	--	mJ	
E_{ts}	Total Switching Loss	--	6.6	--	mJ	
$t_{d(on)}$	Turn-On Delay Time	$V_{CC} = 300V, I_C = 100A,$ $R_G = 2.4\Omega, V_{GE} = 15V$ Inductive Load, $T_C = 125^\circ\text{C}$	--	34	--	ns
t_r	Rise Time		--	96	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	175	--	ns
t_f	Fall Time		--	160	--	ns
E_{on}	Turn-On Switching Loss		--	5.0	--	mJ
E_{off}	Turn-Off Switching Loss	--	4.9	--	mJ	
E_{ts}	Total Switching Loss	--	9.9	--	mJ	
T_{sc}	Short Circuit Withstand Time	$V_{CC} = 300V, V_{GE} = 15V$ @ $T_C = 100^\circ\text{C}$	10	--	--	μs
Q_g	Total Gate Charge	$V_{CE} = 300V, I_C = 100A,$ $V_{GE} = 15V$	--	425	500	nC
Q_{ge}	Gate-Emitter Charge		--	80	--	nC
Q_{gc}	Gate-Collector Charge		--	200	--	nC

Electrical Characteristics of DIODE $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units	
V_{FM}	Diode Forward Voltage	$I_F = 100\text{A}$	$T_C = 25^\circ\text{C}$	--	1.9	2.8	V
			$T_C = 100^\circ\text{C}$	--	1.8	--	
t_{rr}	Diode Reverse Recovery Time		$T_C = 25^\circ\text{C}$	--	90	130	ns
			$T_C = 100^\circ\text{C}$	--	130	--	
I_{rr}	Diode Peak Reverse Recovery Current	$I_F = 100\text{A}$ $di / dt = 200 \text{ A/us}$	$T_C = 25^\circ\text{C}$	--	9	12	A
			$T_C = 100^\circ\text{C}$	--	12	--	
Q_{rr}	Diode Reverse Recovery Charge		$T_C = 25^\circ\text{C}$	--	405	790	nC
			$T_C = 100^\circ\text{C}$	--	780	--	

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case (IGBT Part, per 1/2 Module)	--	0.31	$^\circ\text{C/W}$
$R_{\theta JC}$	Junction-to-Case (DIODE Part, per 1/2 Module)	--	0.7	$^\circ\text{C/W}$
$R_{\theta CS}$	Case-to-Sink (Conductive grease applied)	0.05	--	$^\circ\text{C/W}$
Weight	Weight of Module	--	190	g

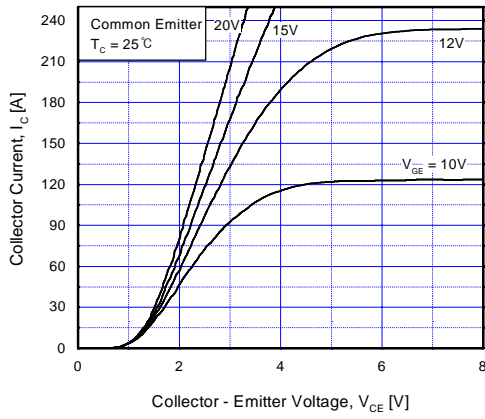


Fig 1. Typical Output Characteristics

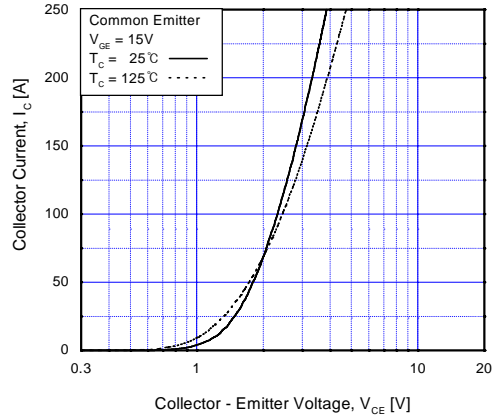


Fig 2. Typical Saturation Voltage Characteristics

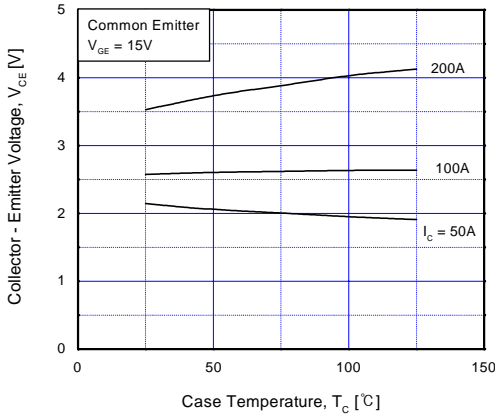


Fig 3. Saturation Voltage vs. Case Temperature at Variant Current Level

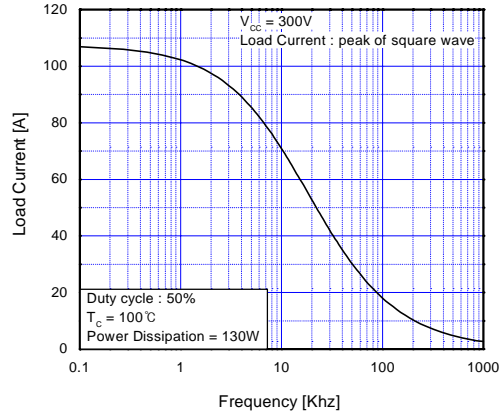


Fig 4. Load Current vs. Frequency

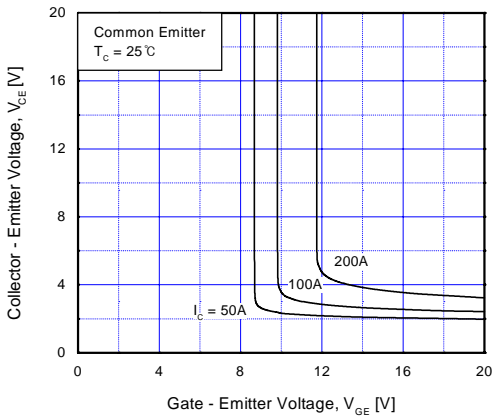


Fig 5. Saturation Voltage vs. V_{GE}

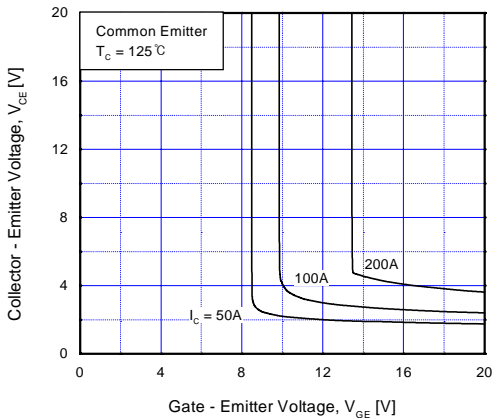


Fig 6. Saturation Voltage vs. V_{GE}

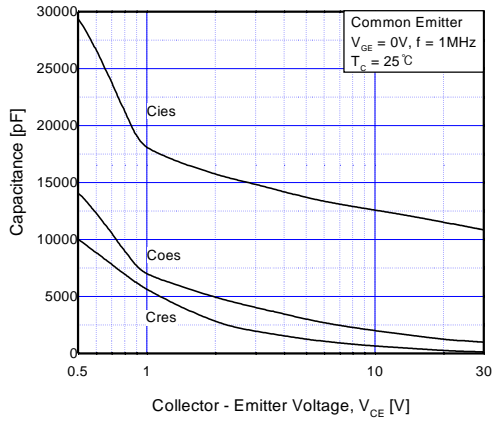


Fig 7. Capacitance Characteristics

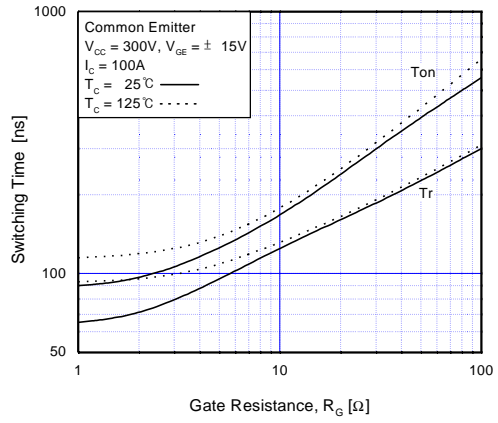


Fig 8. Turn-On Characteristics vs. Gate Resistance

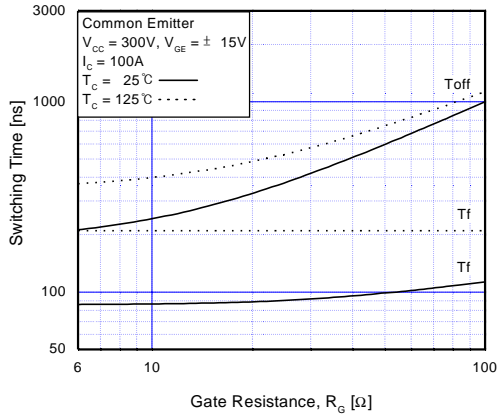


Fig 9. Turn-Off Characteristics vs. Gate Resistance

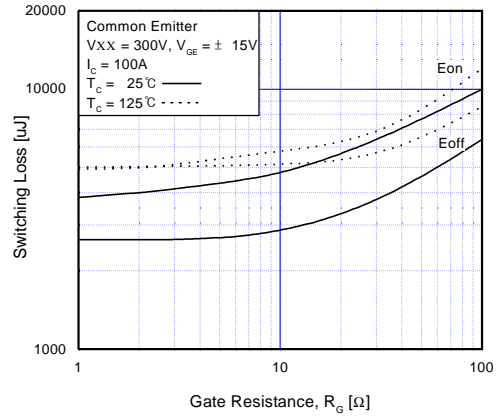


Fig 10. Switching Loss vs. Gate Resistance

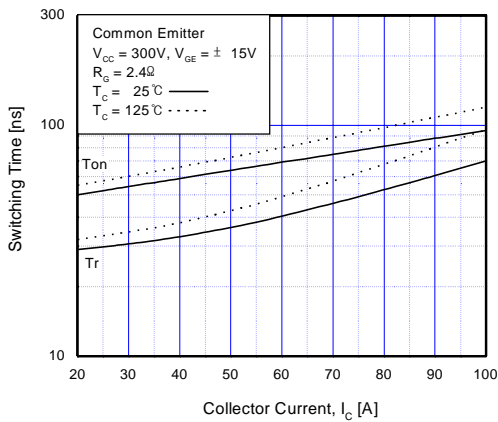


Fig 11. Turn-On Characteristics vs. Collector Current

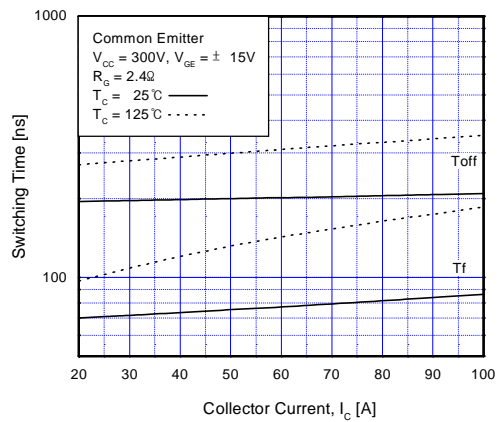


Fig 12. Turn-Off Characteristics vs. Collector Current

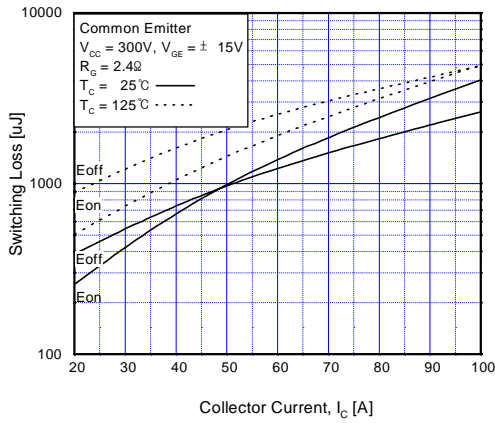


Fig 13. Switching Loss vs. Collector Current

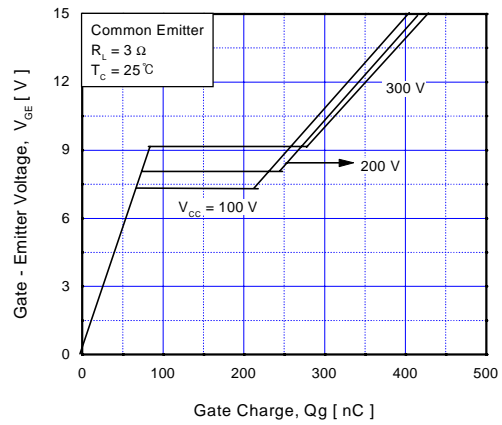


Fig 14. Gate Charge Characteristics

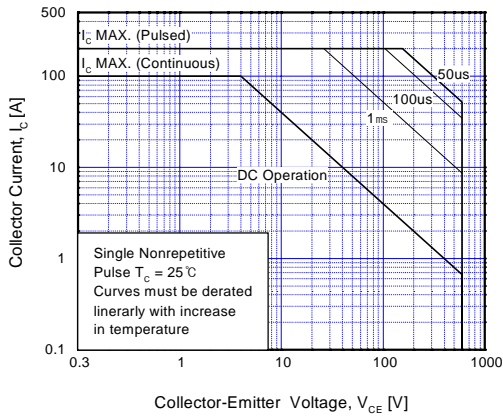


Fig 15. SOA Characteristics

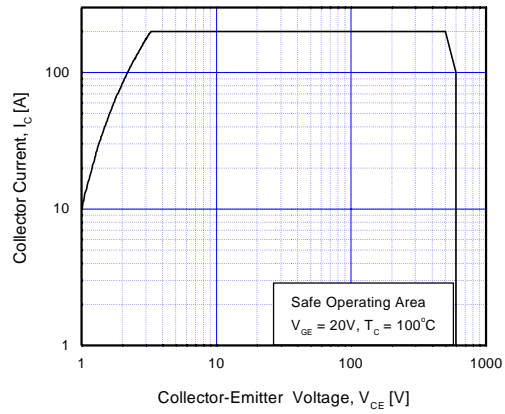


Fig 16. Turn-Off SOA Characteristics

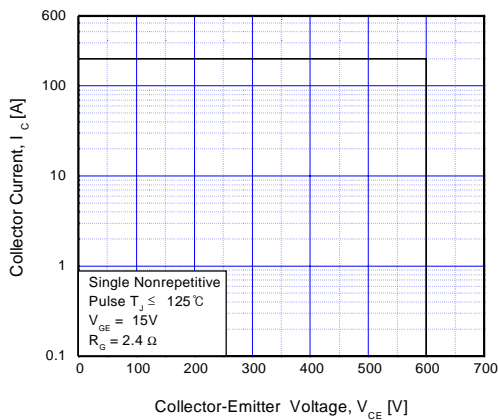


Fig 17. RBSOA Characteristics

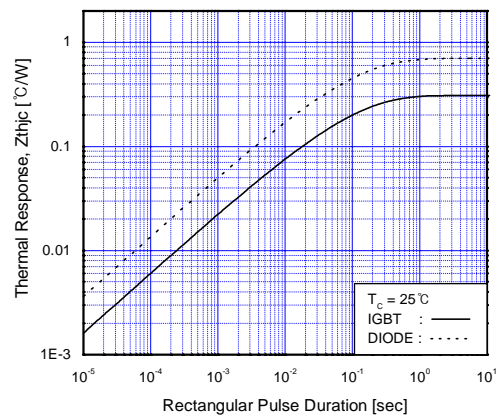


Fig 18. Transient Thermal Impedance

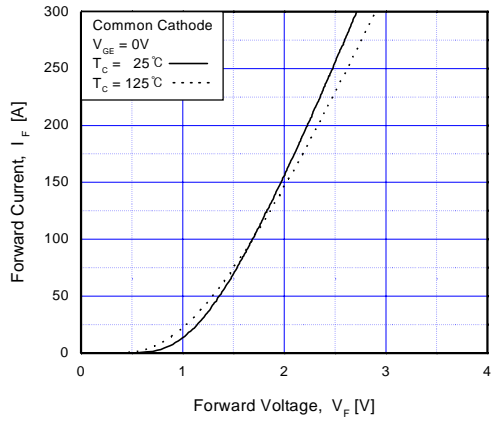


Fig 19. Forward Characteristics

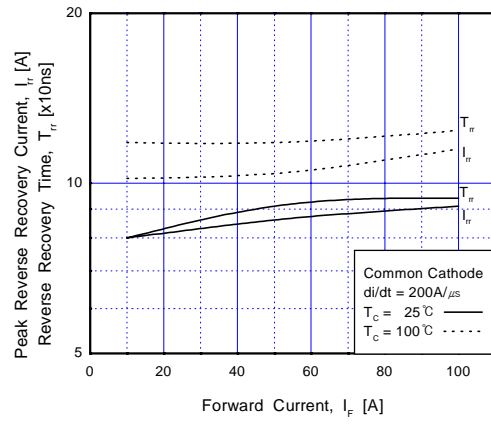
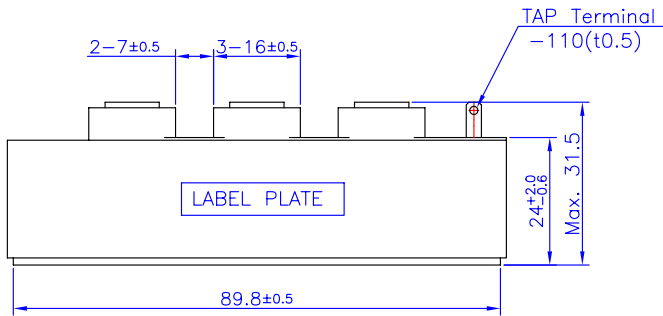
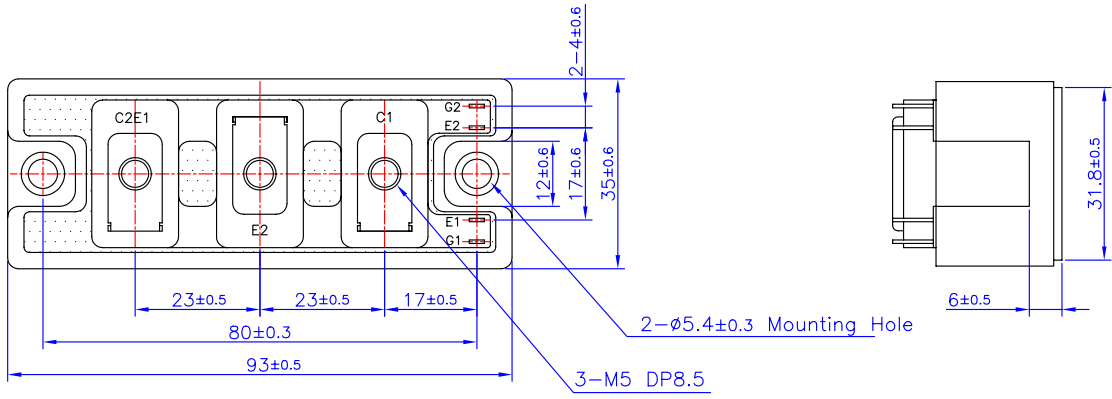


Fig 20. Reverse Recovery Characteristics

Package Dimension

7PM-AA (FS PKG CODE BD)



Dimensions in Millimeters

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