SHARP

LR48252

DESCRIPTION

The LR48252 is a single-chip telephone system incorporating 20 one-toutch or three-touth repertory memories of 16-digit each.

FEATURES

- 32-digit redial memory, and 20 one-toutch or three-toutch repertory memories of 16-digit each.
- Make ratio : 33/39% pin-selectable
- Pulse rate : 10/20 pps pin-selectable
- Key tone output (1 kHz)
- Allows switching from pulse mode to tone mode for mixed-mode dialing by the key entry
- Pulse/Tone dialer operation can be selected by the pin
- Flash function (FLASH key can be stored in memory)
- Pause capability for PABX
- Internal crystal oscillator using external a 3.579 545 MHz crystal resonator for color burst
- CMOS process
- Package: 28-pin DIP (DIP028-P-0600)

Pulse/Tone Dialer LSI

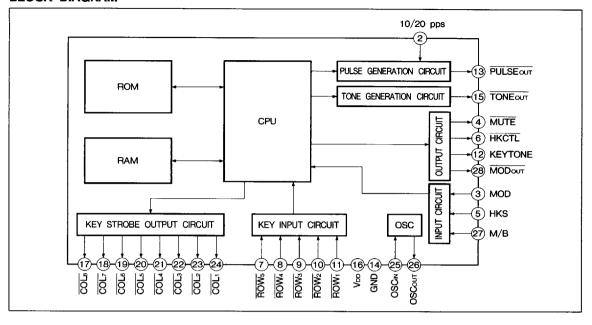
LR48252

PIN CONNECTIONS

28-PIN DIP	TOP VIEW
TEST 1 10/20 pps 2 MOD 3 MUTE 4 HKS 5 HKCTL 6 ROWs 7 ROW4 8 ROW3 9 ROW2 10 ROW1 11 KEYTONE 12 PULSEOUT 13 GND 14	28 MODout 27 M/B 26 OSC OUT 25 OSC IN 24 COL1 23 COL2 22 COL3 21 COL4 20 COL6 19 COL6 18 COL7 17 COL8 16 Voo 15 TONE OUT

8 8180798 0014285 594 **1**

BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATING	UNIT	NOTE
Supply voltage	VDD	6.5	٧	1
Operating temperature	Topr	-30 to +60	°C	
Storage temperature	Tstg	-55 to +150	°C	
Power dissipation	Pd	500	mW	2
Din voltage	VIN1	-0.3	V	3
Pin voltage	Vin2	+0.3	V	4

NOTES:

- 1. Referenced to GND.
- 2. Ta=25°C
- 3. The maximum applicable voltage on any pin with respect to the GND.
- 4. The maximum applicable voltage on any pin with respect to VDD.

DC CHARACTERISTICS

 $(Ta=25^{\circ}C, GND=0 V)$

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT	NOTE
Oleltana	Vpp	Pulse mode	2.0		5.5	V	
Supply voltage	VDD	Tone mode	2.0		5.5	v	
Standby current	Ist	VDD=3.5 V			0.3	μA	1
	lopp	VDD=3.5 V : Pulse mode		0.5	2.0	mA	2
Operating current	ЮРТ	Vpp=3.5 V : Tone mode		1.0	3.0	IIIA	
	VIL		GND		0.2Vpp	v	3
Input voltage	VIH		0.8Vpp		VDD	V	3
Sink current	ICL	VDD=2.0 V, VOL=0.5 V	1.0	2.0		mA	4
KENTONE	lπ∟	VDD=2.0 V, VOL=0.5 V	1.0	2.0		A	
KEYTONE output current	тн	VDD=2.0 V, VOL=1.5 V	1.0	2.0		mA	
Output leakage current	lukg	VDD=5.5 V, VOH=5.5 V			1.0	μA	5
COLUMN	ICL	VDD=3.5 V, VOL=0.5 V		100		μA	
COLUMN output current	Існ	VDD=3.5 V, VOH=3.0 V		5		μΑ	
ROW input current	lp.	VDD=3.5 V, VIL=0 V		35		μА	
HKS input current	lнР	VDD=3.5 V, VIL=0 V		5		μΑ	
TEST input current	lтР	VDD=3.5 V, VIL=0 V		5		μΑ	
Memory retention voltage	V R		1.0			٧	

NOTES:

- 1. Current required to back up memories, with all outputs unloaded in on-hook mode.
- 2. Operating current with all outputs unloaded.
- 3. Applicable to all input pins.
- 4. Applicable to the MUTE, MODout, and HKCTL pins.
- 5. Applicable to the MUTE, MODout, HKCTL, and PULSEout pins.

TONE OUTPUT CHARACTERISTICS

(Ta=25°C, GND=0 V)

PARAME	TER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT	NOTE
Tone output	ROW	Vor	$RL = 10 k\Omega$, $VDD = 4.0 V$	130	175	220	mV _{rms}	
voltage	COLUMN	Voc	RL=10 k Ω , VDD=4.0 V	160	210	270	IIIVms	
Output distortion	· · · · · · · · · · · · · · · · · · ·	Dis	RL=10 kΩ, VDD≥2.0 V			-23	· dB	1
Pre-emphasis		Ренв	RL=10 kΩ, V ^{DD} ≥2.0 V	1.0	2.0	3.0	dB	
Inter-digit pause		tipp		100			ms	2
Tone duration		top		100			ms	2
Tone output rate		ton		200			ms	2

NOTES:

- 1. Distorting frequency components in the range of 20 Hz to 80 kHz are contained in the fundamental ROW and COLUMN tone signals.
- 2. For redialing and repertory memory dialing.

AC CHARACTERISTICS

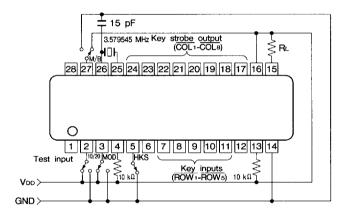
1	Ta	_	2E°	'n	G١	ın	_	Λ	N	n
١	ιa	=	25	υ.	G۱	۱U	=	u	v	0

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNIT	NOTE
Oscillation start-up time	tos				8.0	ms	1
Dulan autout sate	D-	Pin 2=GND		10			
Pulse output rate	PR	Pin 2=Voo		20		pps	
D1. #		Pin 27=GND		67		ms	2
Break time	tв	Pin 27=VDD		61			
I-4 4'-'4	4	Pin 2=GND		850			
Inter-digit pause	tiop	Pin 2=VDD		500		ms	ĺ
Pre-digit pause		Pin 27=GND		33		ms	
	tppp	Pin 27=VDD		39			2

NOTES:

- 1. Crystal resonator parameters : Rs=100 Ω , Lm=96 mH, Cm=0.02 pF, Ch=5 pF, f=3.579 545 MHz.
- 2. Value at 10 pps (value 1/2 at 20 pps).

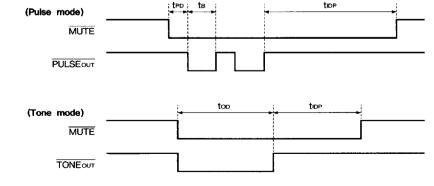
TEST CIRCUIT



NOTES:

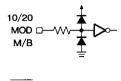
Use a 68 μF capacitor across Voo and GND for supply voltage smoothing and latch-up prevention.
 And connect the TEST pin to Vpb.

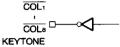
TIMING DIAGRAMS

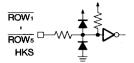


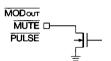
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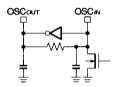
INPUT/OUTPUT CIRCUITS











PIN FUNCTION

SYMBOL	1/0	FUNCTION
COL1 - COL8	0	Key strobe outputs
OSCIN	1	Crystal resonator pin
OSCout	0	Crystal resonator pin
M/B	- 1	Make/break ratio selection pin
MODour	0	Pulse/Tone mode output pin
TEST	ı	Test input pin
10/20 pps	1	Pulse rate selection pin (10/20 pps)
MOD	1	Pulse/Tone mode selection input pin
MUTE	0	Mute signal output pin
HKS	ı	Hook switch input pin
HKCTL	0	Hook control signal output pin
ROW1 - ROW5	1	Key input pins
KEYTONE	0	Key-tone output pin
PULSEOUT	0	Pulse output pin
TONEOUT	0	Tone output pin
Voo	1	Power supply pin
GND	1	Ground pin

PIN DESCRIPTIONS

10/20 ms (Pin 2)

This pin selects the pulse rate in the Pulse mode.

10/20 PIN	PULSE RATE
GND	10 pps
Voo	20 pps

M/B (Pin 27)

This pin selects make/break ratio in the Pulse mode.

M/B PIN	MAKE/BREAK RATIO
GND	33/67
VDD	39/61

MOD (Pulse/Tone mode selection; Pin 3)
Switching from the Pulse to the Tone mode is done as follows:

i) MOD pin

If the mode is switched to the Tone mode during dialing in Pulse mode, the device yields tone singnals for the subsequent dial key operations. In On-Hook mode, the MOD pin also specifies the initial mode in Off-Hook mode:

MOD PIN	INITIAL MODE
GND	Tone mode
Voo	Pulse mode

ii) MOD key

If the MOD or * key is pressed during dialing in Pulse mode, the device outputs tone signals for subsequent dial key operations.

In either case, mode switching information is stored to the buffer memory like other data. If the mode switching key is pressed during dialing, a pause of 4 seconds is automatically inserted before the Tone mode is selected.

STATUS	MODE	MODOUT output
Off-hook	Pulse mode	HZ
OII-NOOK	Tone mode	LOW
On-hook	_	HZ

HZ: High impedance

MUTE (Pin 4)

The $\overline{\text{MUTE}}$ pin is an N-channel open drain output. It is set to a Low level when a pulse or tone dial signal is being output. It is set to high impedance during a pause.

HKS (Pin 5)

This is the hook switch input, and has a pull-up resistor to Vpp.

HKS PIN	MODE
GND	Off-Hook
Voo	On-Hook

KEYTONE (Pin 12)

The KEYTONE is a CMOS output. It yields a 1 kHz square-wave signal while a key is pressed, after the key data becomes valid.

PULSEOUT (Pin 13)

The PULSEOUT is an N-channel open-drain output. During dialing in Pulse mode, it outputs a pulse signal. This pin also yields the flash signal.

TONEOUT (Pin 15)

In the Tone mode, this pin yields the DTMF signal. Fig. 3 shows the output circuit.

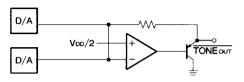


Fig. 3 Tone Output Circuit

HKCTL (Pin 6)

The HKCTL is an N-channel open-drain output. It is controlled by the ON/OFF key to be used for hook control.

PRESENT STATE		INPUT	HKÇTL output	
Hook switch	HKCTL	INPUT	nkere output	
_	HZ	ON/OFF key	LOW	
_	LOW	ON/OFF key	HZ	
On-hook	_	To Off-Hook	HZ	
Off-hook	HZ	To On-Hook	HZ	
Off-hook	LOW	To On-Hook	LOW	

HZ: High impedance

TEST (Pin 1)

This is the reset and test input pin. It is pulled up to Vpp.

TEST PIN	ROW5	MODE
GND	GND	Single tone
GIND	Open or VDD	Reset
Von	_	Normal

A Low input to this pin resets the entire system, upon which all memory contents are cleared. Use a reset switch to prepare for uncertain memory contents resulting from supply voltage drop.

Key Function

KEY	FUNCTION
0 - 9	Numeric keys
*	Pulse mode : Switches from Pulse into Tone mode.
	Tone mode : Data key
#	Pulse mode : Redial key
#	Tone mode : Data key
M1 - M20	One-toutch memory keys
FLASH	Flash key
ON/OFF	Hook control ON/OFF key
STORE	Store to memory key
RED/P	Redial/Pause key
PAUSE	Pause key
MOD	Pulse/Tone mode switching key
RECALL	Repertory memory recall key
CLR	Memory clear key

	COL ₁	COL ₂	COL3	COL4	COL5	COL ₆	COL ₇	COL8
ROW ₁	1	2	3	FLASH	Мı	M6	M11	M16
ROW ₂	4	5	6	STORE	M2	M ₇	M12	M17
ROW 3	7	8	9	CLR	Мз	Мв	М13	M 18
ROW ₄	*	0	#	PAUSE	M4	Мэ	M14	M19
ROW ₅	MOD	RED/P	ON/OFF	RECALL	Мs	M10	M15	M20

Fig. 4 Key Matrix



Fig. 5 Single-Contact Key

Key Signal Specifications

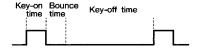
While a key tone is being output, no other key entry is accepted. Also during flash output, no key entry is accepted.

Table 1 Key Signal Specifications

PARAMATER	SPECIFICATION
Double key operation	Only one key code is accepted according to the specified priority order. (NOTE 1)
Bounce time	24 ms
Key on time	30 ms (MIN.) required (NOTE 2)
Key off time	100 ms (MIN.) required (NOTE 2)

NOTES:

- 1. Priority order among keys:
 - Keys in the COL₁ column have the highest priority, while those in the COL₅ have the lowest. Keys in the ROW₅ row have the highest priority, while those in the ROW₁ row have the lowest. Columns have higher priority than rows.
- If the key-on or key-off time is less than these, the key entry may not be accepted.



FUNCTIONAL DESCRIPTION

Output Frequencies in Tone Mode

Table 2 lists the DTMF frequencies. The device yields the DTMF signal while a data key in $\overline{\text{COL}_1}$ - $\overline{\text{COL}_3}$ and $\overline{\text{ROW}_1}$ - $\overline{\text{ROW}_4}$ is pressed.

Table 3 lists the output frequencies in the Single Tone Test mode.

Table 2 DTMF Output Frequencies

		STANDARD DTMF (Hz)	LR48252 (Hz)	DEVIATION (%)
	ROW ₁	697	692.6	-0.63
Lower-	ROW ₂	770	774.1	+0.54
group frequencies	ROW ₃	852	849.0	-0.35
	ROW ₄	941	940.0	-0.11
Higher-	COL ₁	1209	1214.2	+0.43
group	COL ₂	1336	1335.7	-0.03
frequencies	COL3	1477	1484.1	-0.48

NOTE:

The frequencies given in the LR48252 column are when the internal oscillator is oscillating at 3.579545 MHz. Deviation in the oscillation frequency affects tone output frequency.

rabe	3 Output Frequenc	eles in l'est mode
KEY	HIGH FREQUENCY (Hz)	LOW FREQUENCY (Hz)
7	1214.2	_

KEY	HIGH FREQUENCY (Hz)	LOW FREQUENCY (Hz)
7	1214.2	_
2	1335.7	
6	1484.1	_
3	_	692.6
4	_	774.1
8	_	849.0

940.0

Normal Dialing

If data keys (0-9 in Pulse mode; 0-9, *, and # in Tone mode) are used for Off-Hook dialing, the device performs normal dialing operation. In the Tone mode, the device outputs the DTMF signal while a key is pressed and held. (In this case, no key tone is output.) Up to 32 digits of input data are stored in a buffer memory. Key entries in excess of 32 digits are accepted when all of the 32-digit data stored in the buffer memory are dialed. Once the 33rd digit is accepted, the entire buffer memory is cleared, and an additional 32 digits beginning with the 33rd digit can be stored in the buffer.

INPUT	DIAL OUTPUT	BUFFER MEMORY CONTENTS
↓ ⟨P >↑		(R)=last nember dialed
07436 5 1321	0743651321	(R)=0743651321
↓ < T> ↑		(R)=last nember dialed
07436 5 1321 #*	0743651321#*	(R)=0743651321#*
↓ ⟨P> ↑		(R)=last number dialed
12345 · · · 012	12345 · · · 012	(R) = 12345 · · · 012
		32 digits
32 digits	32 digits	
3456789	3456789	(R)=3456789
↓ ↑		(R)=3456789

NOTE:

↓ : On-hook, ↑ : Off-hook, 〈 > : MOD pin status,

: DTMF output, (R) : Buffer memory

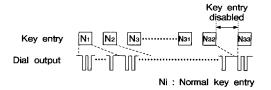


Fig. 6 Normal Mode Dialing Sequence

Redialing Feature

If the RED/P key is pressed following Off-Hook. the contents of the buffer memory are dialed (in Pulse mode, the # key also acts as the REDIAL key). Redialing corresponds to 32 digits of normal dialing.

ENTRY	DIAL OUTPUT	MEMORY CONTENTS
↓ <p> ↑</p>		(R)=last number dialed
1234567890	1234567890	(R) = 1234567890
↓ ↑		
RED/P or #	1234567890	(R) = 1234567890

Repertory Dialing

Twenty memories of 16 digits each can be assigned to one-toutch or three-toutch repertory dial kevs.

Two repertory dial keys may be operarted consecutively in Off-Hook mode. The dialed data are stored in the buffer memory. A third repertory dial key is accepted after all the numbers assigned to the first and second keys have been dialed. When the third key is pressed, the buffer memory is cleared and only the number for the third key is stored in the buffer. Each memory has a length of 16 digits.

ENTRY	DIAL OUTPUT	MEMORY CONTENTS
↓ ⟨ P ⟩		(M ₁)=07436, (M ₂)=51321, (M ₃)=12345
↑		(R)=last number dialed
M ₁	07436	(R)=07436
M2	51321	(R)=0743651321
↓ ↑		
REC		
02	12345	(R) = 12345

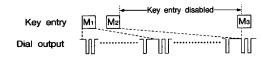


Fig. 7 Repertory Dialing Sequences

Storing Numbers to Memories

Memory contents can be set only in On-Hook mode.

KEY ENTRY	MEMORY CONTENTS	
1	(R)=last number dialed	
STORE M1	(M ₁)=(R)=last number dialed	
STORE 07436 M2	$(M_2) = (R) = 07436$	
STORE 51321 STORE 02	(M ₃)=(R)=51321	

NOTE:

Up to 16 digits can be stored to each memory. For a number exceeding 16 digits, only the first 16 digits are stored and the remaining digits are ignored.

Mixed-Mode Dialing

During dialing in Pulse mode, the user can switch to Tone mode with the MOD or * key. If the mode switching key is pressed during dialing or mode switching data is encountered in memory, a pause of 4 seconds is automatically inserted before the Tone mode is selected. Like other data, the mode switching information is stored in the buffer memory (one-toutch or

three-toutch memory) occupying a single digit.		
KEY ENTRY	DIAL OUTPUT	MEMORY CONTENTS
↓ ⟨ P⟩		(M ₁) = 123MOD456
↑		

↓ ⟨P⟩		$(M_1) = 123MOD456$
1		
07436	07436	(R)=07436
MOD 51321	51321	(R)=07436MOD51321
↓ ⟨P ⟩		
1		
07436 MOD 51321	07436 (PAUSE) 51321	(R)=07436MOD51321
Pressed while dialing 07436		
↓ ⟨ P⟩		
1		
M ₁	123 (PAUSE) 456	(R) = 123MOD456

NOTE:

: DTMF output

Pause Feature

The PAUSE or RED/P* key allows the user to insert a pause of about 4 seconds into a stored dialing sequence. During pause, the MUTE output is set to high impedance. Like other data keys, the PAUSE key data can also be stored in memory. The pause can be cleared by pressing the PAUSE or RED/P key during pause.

	KEY ENTRY	DIALING SEQUENCE	MEMOTRY CONTENTS
	↑		
1	012 PAUSE 3456	012 (PAUSE) 3456	(R)=012PAUSE3456

NOTE:

* Immediately after the off-hook, this key acts as the REDIAL key.

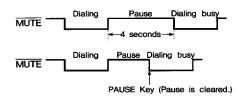


Fig. 8 Pause Feature

Repertory Dialing Plus Normal Dialing

In Off-Hook mode, normal dialing can follow repertory dialing. A number of up to 16 digits can be stored in buffer memory following the repertory number. When the number stored in the buffer has been dialed, the device is ready to accept subsequent key entry. In this case, all previous contents of the buffer are cleared before the 17th digit is stored.

	_	
KEY ENTRYO	DIAL OUTPUT	MEMORY CONTENTS
↑		(M ₁)=07436
M ₁	07436	(R)=07436
1234 · · · 456	1234 · · · 456	(R)=074361234···456
16 digits	16 digits	16 digits
7890	7890	(R)=7890
↓ ↑		(M2)=09876
REC 01	09876	(R)=09876
1234 · · · 456	1234 · · · 456	(R)=098761234···456
16 digits	16 digits	16 digits
7890	7890	(R)=7890

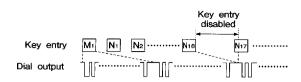


Fig. 9 Repertory Dialing Plus Normal Dialing

Normal Dialing Plus Repertory Dialing

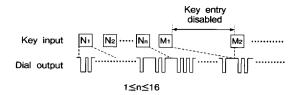
In Off-Hook mode, repertory dialing can follow normal dialing.

After normal dialing up to 16 digits, a repertory dial key can be entered. All numbers dialed with data and repertory keys are stored in a buffer. After all numbers in the buffer have been dialed, the device is ready to accept subsequent data key entry.

When a new data key entry is accepted, the previous contents of the buffer are cleared.

If a number 17 to 32 digits length is dialed with data keys in Off-Hook mode, a repertory dial key can be entered after the entire number is dialed normally. The previous contents of the buffer are cleared when the repertory dial key is entered.

KEY ENTRY	DIAL OUTPUT	MEMORY CONTENTS
1		(M ₁)=51321
07436	07436	(R)=07436
M ₁	51321	(R)=0743651321
2416	2416	(R)=2416
1		(M1)=51321
1234 · · · 4567	1234 · · · 4567	(R) = 1234 ··· 4567
17 digits	17 digits	17 digits
REC 00	51321	(R)=51321
2416	2416	(R)=513212416



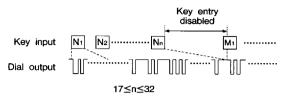


Fig. 10 Normal Dialing Plus Repertory Dialing

Flash Feature

i) If FLASH key is pressed in Off-Hook mode, the device outputs the flash signal pulses shown in Fig. 11 from its PULSEOUT and MUTE pins. The flash signal is output at the exact timing of fLASH key operation even if dialing is busy. Once the flash signal is output, the device is placed in the same state as that in the temporary On-Hook mode. In this case, the flash signal is not stored in buffer.

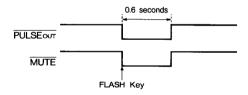


Fig. 11 Flash Signal Pulses

ii) The flash signal can be stored in the onetoutch or three-toutch repertory dial memories. In this case, flash pulses as shown in Fig. 12 are output.

When a repertory memory containing the FLASH key is recalled, the stored number including the FLASH key is transferred to the buffer.

A subsequent REDIAL key operation causes any of the following dialing sequences, depending on the position where the FLASH key is inserted:

- i) When a flash is placed at the last digit of the data in the buffer:
 - The first digit through the digit just before the flash in the buffer are redialed.
- ii) When a flash is placed in the middle of data in the buffer:
 - The digit following the flash through the last digit are redialed.
- iii) When a flash is placed at the first digit of the buffer:
 - The second digit through the last digit are redialed.

In any of the three cases above, the flash signal itself is not redialed.

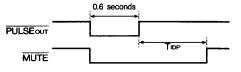


Fig. 12 Flash Signal

Table 4 Flash Function

KEY ENTRY	DIAL OUTPUT	MEMORY CONTENTS
⟨ P ⟩		
↑		(R)=last number dialed
123	123	(R)=123
FLASH	FLASH	(R)=123
456	456	(R)=456
↓ ↑		
RED/P	456	(R)=456
⟨ P ⟩		
↑		(R)=last number dialed
123	123	(R)=123
FLASH	FLASH	(R)=123
↓ ↑		
RED/P	123	(R)=123
		(M1)=123FLASH456
		(M2) = 123FLASH
		(M3)=FLASH789
		(R)=last number dialed
Мı	123FLASH456	(R) = 123FLASH456
↓ ↑		
RED/P	456	(R) = 123FLASH456
↑		(R)=last number dialed
M ₂	123FLASH	(R) = 123FLASH
↓ ↑		
RED/P	123	(R) = 123FLASH
↑		(R) = last number dialed
Мз	FLASH789	(R)=FLASH789
↓ ↑		
RED/P	789	(R)=FLASH789