Solitron DEVICES, INC.
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PRODUCT CATALOG

N-CHANNEL ENHANCEMENT MOS FET

200V, 18A, 0.20Ω

SDF240 JAA
SDF240 JAB
SDF240 JDA

FEATURES

- RUGGED PACKAGE
- HI-REL CONSTRUCTION
- CERAMIC EYELETS: JAA, JAB
- LEAD BENDING OPTIONS
- COPPER CORED 52 ALLOY PINS
- LOW IR LOSSES
- LOW THERMAL RESISTANCE
- OPTIONAL MIL-S-19500 SCREENING

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL		UNITS
Drain-source Volt.(1)	VDSS	200	Vdc
Drain-Gate Voltage (R_{GS}=1.0MΩ) (1)	VDGR	200	Vdc
Gate-Source Voltage Continuous	VGS	±20	Vdc
Drain Current Continuous (T_C = 25°C)	ID	18	Adc
Drain Current Pulsed(3)	IDM	72	A
Total Power Dissipation	PD	100	W
Power Dissipation Derating > 25°C		0.83	W/°C
Operating & Storage Temp.	TJ/Tsig	-55 TO +150	°C
Thermal Resistance	RthJc	1.2	°C/W
Max.Lead temperature	TL	300	°C

ELECTRICAL CHARACTERISTICS T_C = 25°C (UNLESS OTHERWISE SPECIFIED)

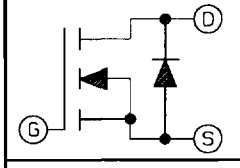
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Drain-source Breakdown Volt.	V(BR)DSS	VGS=0V ID=250 µA	200	-	-	V
Gate Threshold Voltage	VGS(TH)	VDS=VGS ID=250 µA	2.0	-	4.0	V
Gate Source Leakage	IGSS	VGS=±20 V	-	-	100	nA
Zero Gate Voltage Drain Current	IDSS	VDS=MAX.RATING VGS=0	-	-	250	µA
		VDS=0.8 MAX.RATING VGS=0 TJ=125°C	-	-	1000	µA
Static Drain-Source On-State Resistance(1)	RDS(ON)	VGS=10 V ID=10A	-	-	0.20	Ω
Forward Trans-Conductance (2)	gfs	VDS ≥ 50 V IDS=10A	6.7	-	-	S(℧)
Input Capacitance	CISS	VGS=0V VDS=25 V f=1.0 MHz	-	1300	-	pF
Output Capacitance	COSS		-	380	-	pF
Reverse Transfer Capacitance	CRSS		-	93	-	pF
Turn-On Delay	td(on)	VDD=100V RG=9.1Ω ID=18A RD=5.6Ω (MOSFET switching times are essentially independent of operating temp.)	-	-	21	ns
Rise Time	tr		-	-	77	ns
Turn-Off Delay	td(off)		-	-	68	ns
Fall Time	tf		-	-	54	ns
Total Gate Charge (Gate-Source Plus Gate-Drain)	Qg	VGS=10V, ID=18A VDS=0.8 MAX.RATING (Gate charge is essentially independent of the operating temperature)	-	-	60	nC
Gate-Source Charge	Qgs		-	-	10	nC
Gate-Drain ("Miller") Charge	Qgd		-	-	32	nC

SOURCE-DRAIN DIODE RATINGS & CHARACT. T_C = 25°C (UNLESS OTHERWISE SPECIFIED)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Continuous Source Current (Body Diode)	IS	Modified MOSFET symbol showing the integral reverse P-N junction rectifier (See schematic)	-	-	18	A
Pulse Source Current (Body Diode) (1)	ISM		-	-	72	A
Diode Forward Voltage (2)	VSD	IF=18A, VGS=0V TC=+25°C	-	-	2.0	V
Reverse Recovery Time	trr	TC=+25° C IF=18A di/dt=100A/µS	-	-	530	ns
Reverse Recovery Charge	Qrr		-	2.6	-	µC

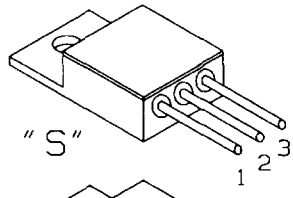
(1) TJ = 25°C to 150°C.
(2) Pulse test: Pulse Width < 300µS, Duty Cycle < 2%.
(3) Repetitive Rating: Pulse Width limited By Max.junction Temperature.

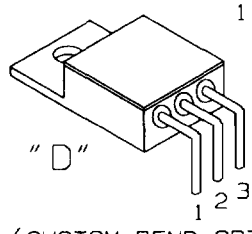
SCHEMATIC

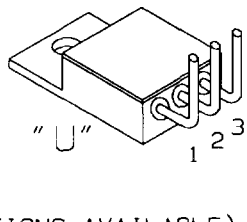


TERMINAL CONNECTIONS			
G		H	
1	GATE	1	DRAIN
2	DRAIN	2	SOURCE
3	SOURCE	3	GATE

STANDARD BEND CONFIGURATIONS — JAA JDA

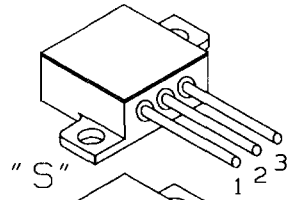


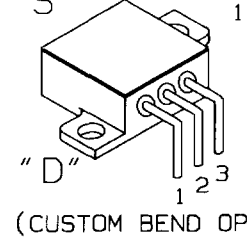


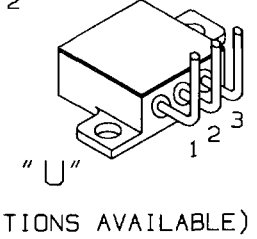


(CUSTOM BEND OPTIONS AVAILABLE)

STANDARD BEND CONFIGURATIONS — JAB







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