Vishay High Power Products

Schottky Rectifier, 5.5 A



- Popular D-PAK outline
- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS directive 2002/95/EC
- AEC-Q101 qualified

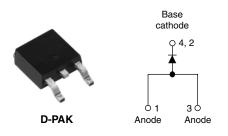
DESCRIPTION

The 50WQ10FNPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	5.5	A	
V _{RRM}		100	V	
I _{FSM}	t _p = 5 μs sine	330	A	
V _F	5 Apk, T _J = 125 °C	0.63	V	
TJ	Range	- 40 to 150	۵°	

VOLTAGE RATINGS					
PARAMETER	SYMBOL	50WQ10FNPbF	UNITS		
Maximum DC reverse voltage	V _R	100	V		
Maximum working peak reverse voltage	V _{RWM}	100	v		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_{C} = 135 °C, rectangular waveform		5.5	
Maximum peak one cycle non-repetitive surge current	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	330	A
See fig. 7		10 ms sine or 6 ms rect. pulse		110	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 0.5 \text{ A}, L = 40 \text{ mH}$		6.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.5	А



5.5 A

100 V

PRODUCT SUMMARY

I_{F(AV)}

 V_{R}







50WQ10FNPbF

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	5 A	T _J = 25 °C	0.77	V
Maximum forward voltage drop		10 A		0.91	
See fig. 1	V FM (1)	5 A	- T _J = 125 °C	0.63	
		10 A		0.74	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	1	mA
See fig. 2	IRM ()	T _J = 125 °C		4	
Threshold voltage	V _{F(TO)}	- T _J =T _J maximum		0.47	V
Forward slope resistance	r _t			21.46	mΩ
Typical junction capacitance	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		183	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		5.0	nH

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

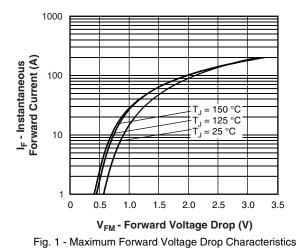
THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 40 to 150	°C
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	3.0	°C/W
Approximate weight			0.3	g
Approximate weight			0.01	oz.
Marking device		Case style D-PAK (similar to TO-252AA)	50WC	10FN

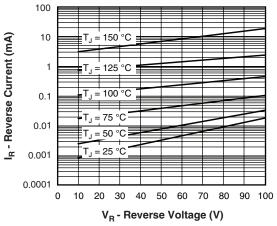
Note

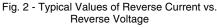
(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink



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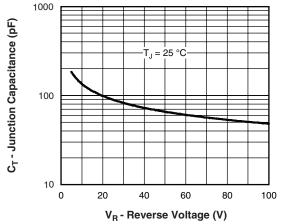


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

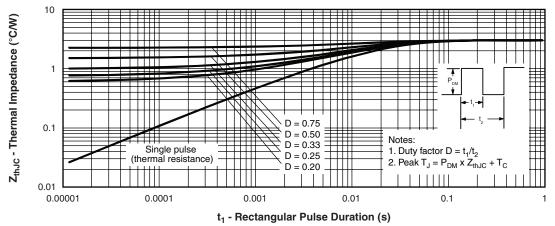
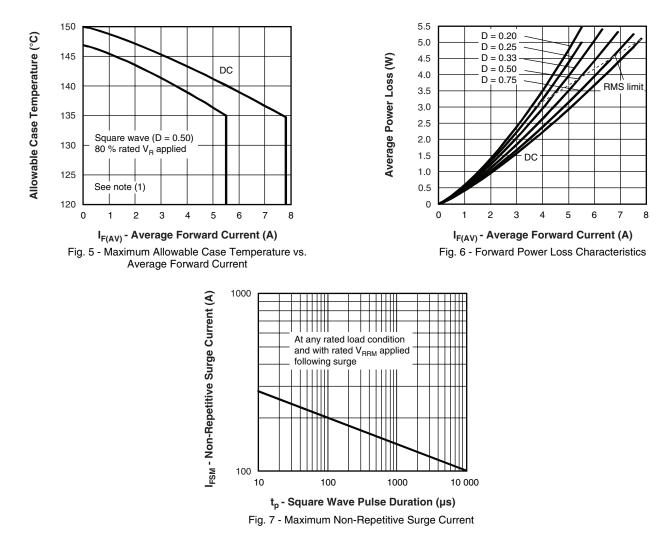


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

50WQ10FNPbF

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Note

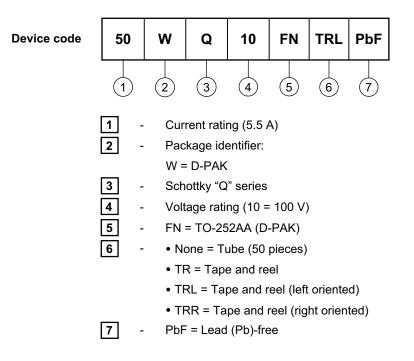
- $\begin{array}{l} \mbox{Formula used: } T_C = T_J \mbox{-} (Pd + Pd_{REV}) \ x \ R_{thJC}; \\ \mbox{Pd} = \mbox{Forward power loss} = I_{F(AV)} \ x \ V_{FM} \ at \ (I_{F(AV)}/D) \ (see \ fig. \ 6); \\ \mbox{Pd}_{REV} = \mbox{Inverse power loss} = V_{R1} \ x \ I_R \ (1 \ D); \ I_R \ at \ V_{R1} = 80 \ \% \ rated \ V_R \end{array}$

⁽¹⁾



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95016		
Part marking information	www.vishay.com/doc?95059		
Packaging information	www.vishay.com/doc?95033		



Vishay

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