RJK0631JPD

Silicon N Channel Power MOS FET
High Speed Power Switching

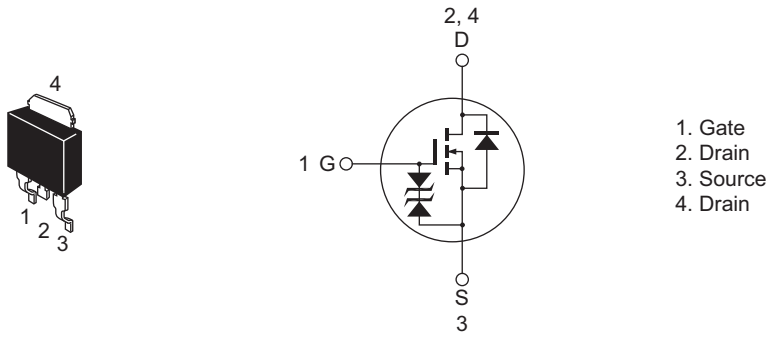
R07DS0252EJ0200
Rev2.00
May 23, 2013

Features

- For Automotive application
- Low on-resistance : $R_{DS(on)}$ = 12 mΩ typ.
- Capable of 4.5 V gate drive
- Low input capacitance: Ciss = 1350 pF typ
- AEC-Q101 compliant

Outline

RENESAS Package code: PRSS0004ZD-C
(Package name: DPAK (S))



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Drain to source voltage	V_{DSS}	60	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I_D	30	A
Drain peak current	I_D (pulse) Note1	120	A
Body-drain diode reverse drain current	I_{DR}	30	A
Body-drain diode reverse drain peak current	$I_{DR (pulse)}$ Note1	120	A
Avalanche current	I_{AP} Note2	27	A
Avalanche energy	E_{AR} Note2	62.5	mJ
Channel dissipation	Pch Note3	45	W
Channel temperature	Tch Note4	175	°C
Strage temperature	Tstg	–55 to +150	°C

Notes: 1. PW ≤ 10µs duty cycle ≤ 1%
2. Tch = 25°C, Rg ≥ 50 Ω
3. Tc = 25°C
4. AEC-Q101 compliant

Thermal Impedance Characteristics

- Channel to case thermal impedance θch-c: 3.33°C/W

Electrical Characteristics

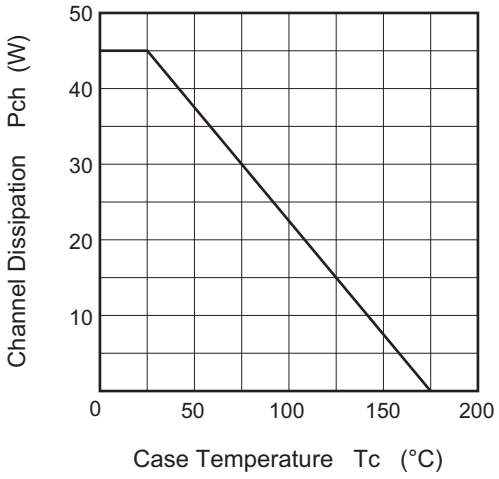
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Gate to source leak current	I_{GSS}	—	—	±10	µA	V_{GS} = ±20 V, V_{DS} = 0
Zero gate voltage drain current	I_{DSS}	—	—	1	µA	V_{DS} = 60 V, V_{GS} = 0
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	—	2.0	V	I_D = 1 mA, V_{DS} = 10 V
Static drain to source on state resistance	$R_{DS(on)}$	—	12	15	mΩ	I_D = 15 A, V_{GS} = 10 V Note5
		—	15	20	mΩ	I_D = 15 A, V_{GS} = 4.5 V Note5
Input capacitance	Ciss	—	1350	—	pF	V_{DS} = 10V, V_{GS} = 0, f = 1 MHz
Output capacitance	Coss	—	360	—	pF	
Reverse transfer capacitance	Crss	—	270	—	pF	
Total gate charge	Qg	—	32	—	nC	V_{DD} = 25 V, V_{GS} = 10 V, I_D = 30 A
Gate to source charge	Qgs	—	3.6	—	nC	
Gate to drain charge	Qgd	—	10	—	nC	
Turn-on delay time	$t_{d(on)}$	—	13	—	ns	I_D = 15 A, R_L = 2 Ω, V_{GS} = 10 V, R_G = 4.7 Ω
Rise time	t_r	—	15	—	ns	
Turn-off delay time	$t_{d(off)}$	—	60	—	ns	
Fall time	t_f	—	15	—	ns	
Body-drain diode forward voltage	V_{DF}	—	0.94	1.17	V	I_F = 30 A, V_{GS} = 0 Note5
Body-drain diode reverse recovery time	t_{rr}	—	40	—	ns	I_F = 30 A, V_{GS} = 0 di_F/dt = 100 A/µs

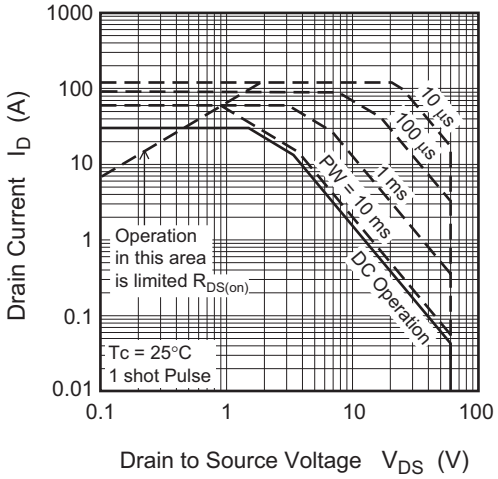
Note: 5. Pulse test

Main Characteristics

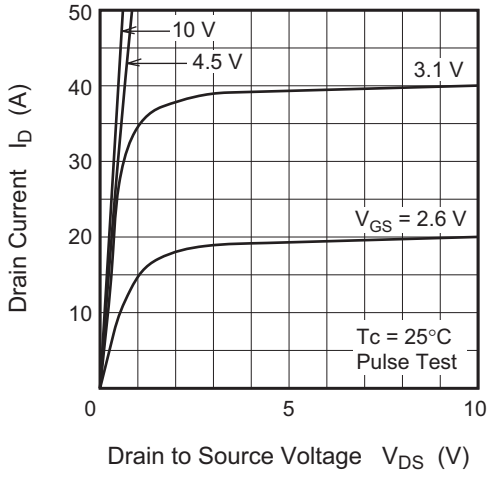
Power vs. Temperature Derating



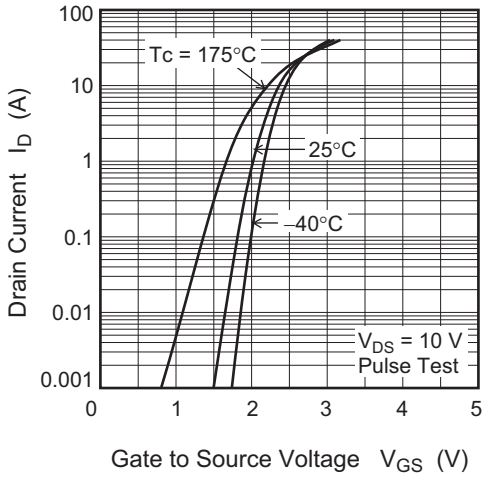
Maximum Safe Operation Area



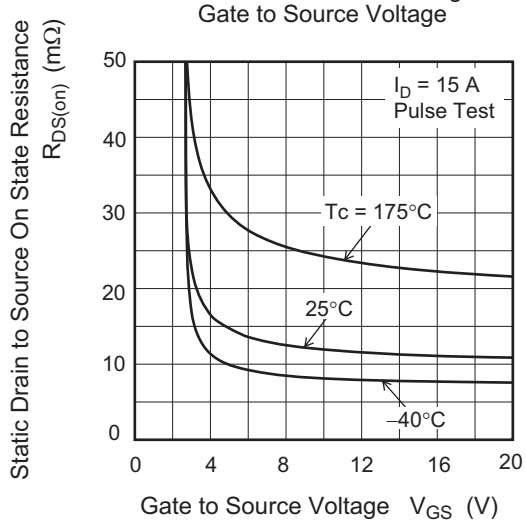
Typical Output Characteristics



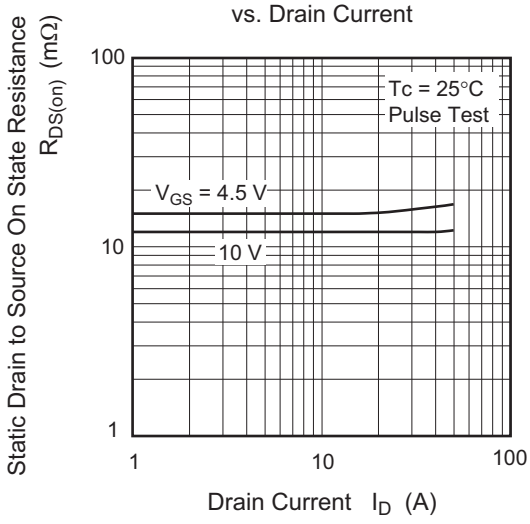
Typical Transfer Characteristics



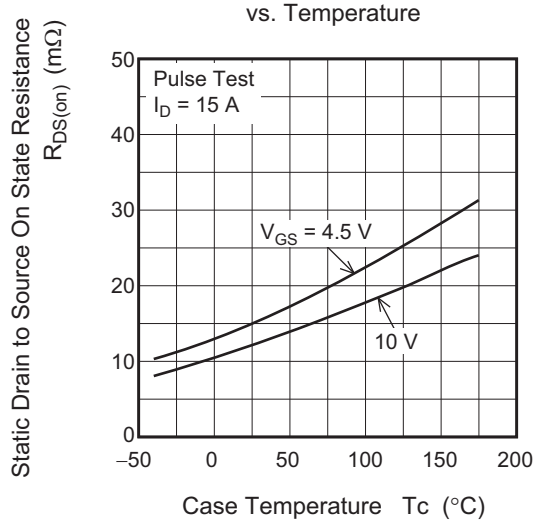
Drain Source Saturation Voltage vs. Gate to Source Voltage



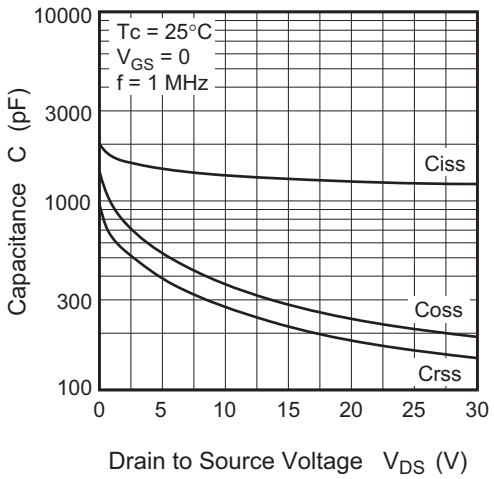
Static Drain to Source State Resistance vs. Drain Current



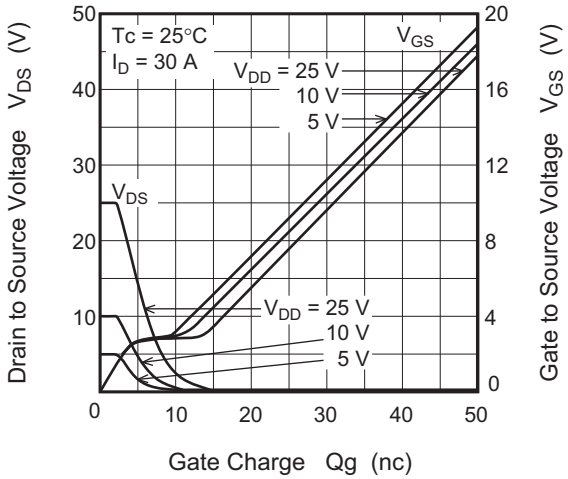
Static Drain to Source on State Resistance vs. Temperature



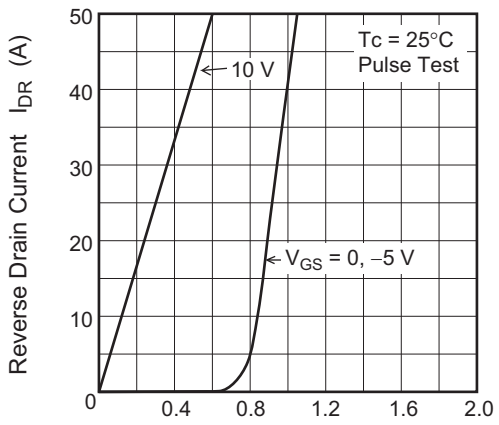
Typical Capacitance vs. Drain to Source Voltage



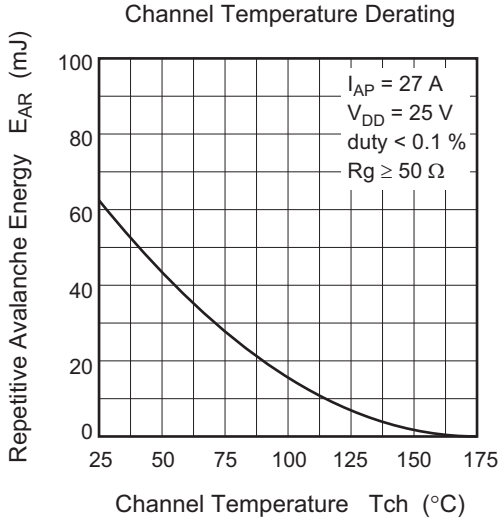
Dynamic Input Characteristics



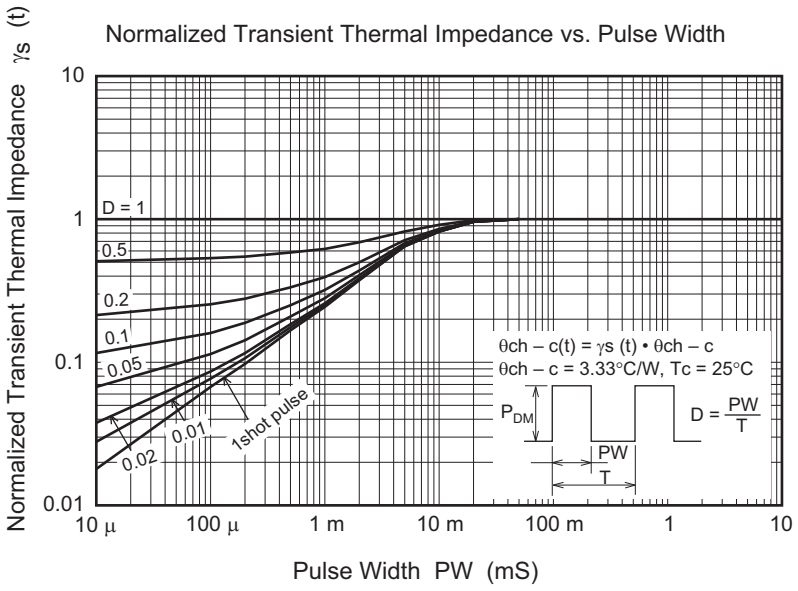
Reverse Drain Current vs. Source to Drain Voltage



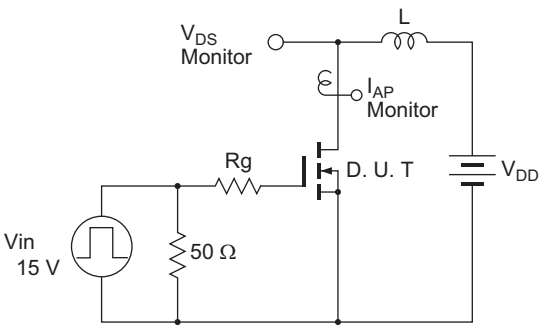
Avalanche Energy vs. Channel Temperature Derating



Normalized Transient Thermal Impedance vs. Pulse Width

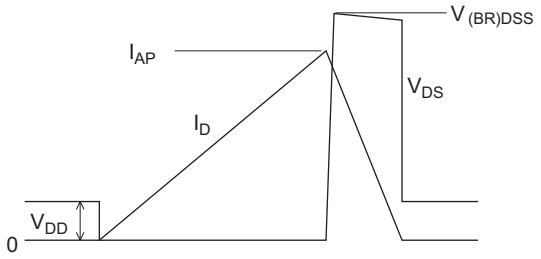


Avalanche Test Circuit

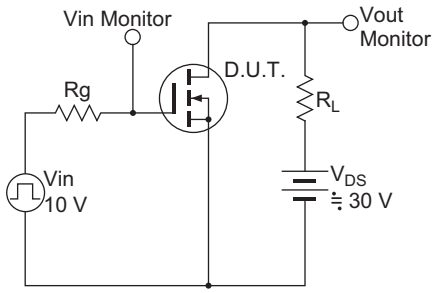


Avalanche Waveform

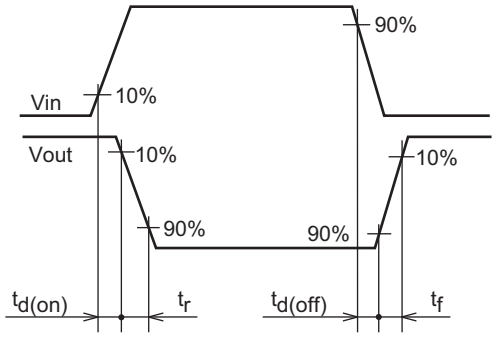
$$E_{AR} = \frac{1}{2} L \bullet I_{AP}^2 \bullet \frac{V_{DSS}}{V_{DSS} - V_{DD}}$$



Switching Time Test Circuit



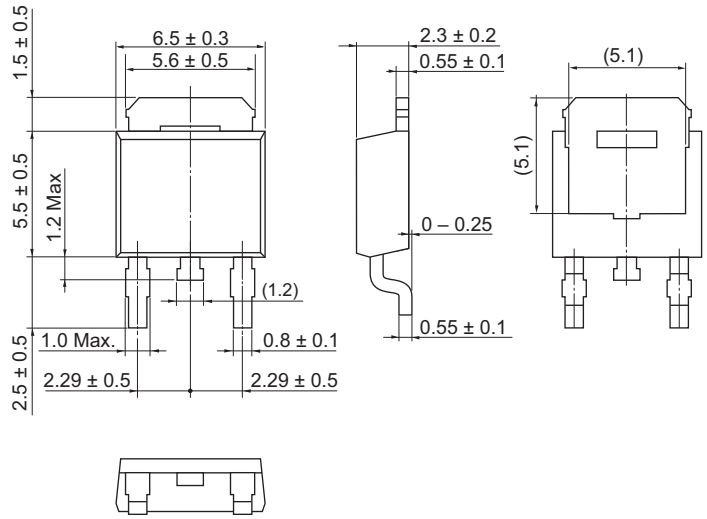
Switching Time Waveform



Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
DPAK(S)	SC-63	PRSS0004ZD-C	DPAK(S) / DPAK(S)V	0.28g

Unit: mm



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJK0631JPD-00-J0	3000 pcs	Taping (Left-winded)

Note: The symbol of 2nd "-" is occasionally presented as "#".

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