TOSHIBA Photocoupler GaAlAs IRED & Photo-IC

TLP250(INV)

Transistor Inverter
Inverters for Air Conditioner
IGBT Gate Drive
Power MOS FET Gate Drive

The TOSHIBA TLP250(INV) consists of a GaAlAs light emitting diode and a integrated photodetector.

This unit is 8-lead DIP.

TLP250(INV) is suitable for gate driving circuit of IGBT or power MOS FET.

Input Threshold Current : I_F=5mA(max)
 Supply Current(ICC) : 11mA(max)
 Supply Voltage(VCC) : 10~35V
 Output Current(IO) : ±2.0A(max)
 Switching Time(tpLH/tpHL) : 0.5µs(max)
 Isolation Voltage : 2500Vrms

UL Recognized : UL1577, File No. E67349

Option(D4)

VDE Approved: DIN EN 60747-5-2 Certificate No.40011913

 $\begin{aligned} & \text{Maximum Operating Insulation Voltage} : 630V_{PK} \\ & \text{Highest Permissible Over Voltage} \end{aligned} : 4000V_{PK}$

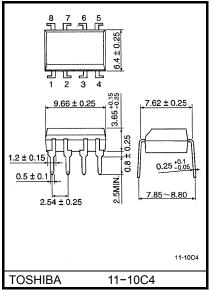
Note: When a EN 60747-5-2 approved type is needed, Please designate the "Option(D4)"

Creepage Distance : 6.4mm(MIN)Clearance : 6.4mm(MIN)

Truth Table

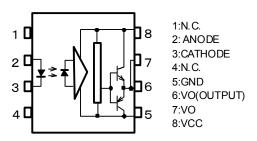
		Tr 1	Tr 2
Input LED	ON	ON	OFF
	OFF	OFF	ON

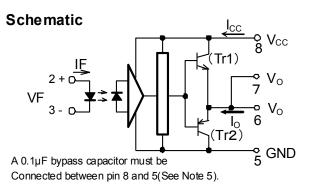
Unit in mm



Weight: 0.54 g (typ.)

Pin Configuration (top view)





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Absolute Maximum Ratings (Ta=25°C)

	Chara	Symbol	Rating	Unit			
Forward Current					20	mA	
	Forward Current Derating (Ta≥7	ΔI _F /ΔTa	-0.36	mA /°C			
Peak Transient Forward Current (Note 1)					1	Α	
	Reverse Voltage			V_R	5	V	
	Junction Temperature			Tj	125	°C	
	"H" Peak	PW ≤2.5µs , f≤15 kH	z		-1.5		
	Output Current	PW≤1.0µs , f≤15 kHz	z (Note 2)	I _{OPH}	-0.36 1 5 125 -1.5 -2.0 +1.5 +2.0 35 24 35 24 -0.73	Α	
	"L" Peak	PW≤2.5µs , f≤15 kHz		1	+1.5	Α	
١.	Output Current	PW ≤1.0µs , f≤15 kH	z	I _{OPL}	+2.0	A	
Detector	Output Voltage		(Ta≤70°C)		35	V	
Dete	ថ្ម Output Voltage		(Ta=85°C)	Vo	24	V	
	Supply Voltage		(Ta≤70°C)	V _{CC}	35	V	
	Supply Voltage		(Ta=85°C)	V CC	24	V	
	Output Voltage Derating (Ta≥70)°C)		ΔV₀ /∆Τα	-0.73	V /°C	
	Supply Voltage Derating (Ta≥70	ΔV _{CC} /ΔTa	-0.73	V /°C			
	Junction Temperature	Tj	125	°C			
Оре	erating Frequency	f	25	kHz			
Оре	erating Temperature Range	T_{opr}	-20~85	°C			
Stor	rage Temperature Range	T_{stg}	-55~125	°C			
Lea	d Soldering Temperature(10s)	T _{sol}	260	°C			
Isola	ation Voltage (AC,1min., R.H. ≤	60%,Ta=25°C)	(Note 4)	BVs	2500	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- (Note 1) : Pulse width PW≤1µs,300pps
- (Note 2): Exporenential Waveform
- (Note 3) : Exporenential Waveform $I_{OPH} \le -1.0A \ (\le 2.5 \mu s)$, $I_{OPL} \le +1.0A \ (\le 2.5 \mu s)$
- (Note 4): Device considerd a two terminal device: pins 1,2,3 and 4 shorted together and pins 5,6,7 and 8 shorted together.
- (Note 5): A ceramic capacitor(0.1µF) should be connected from pin 8 to pin 5 to stabilize the operation of the high gain linear amplifier. Failure to provide the bypassing may impair the switching proparty. The total lead length between capacitor and coupler should not exceed 1cm.

Recommended Operating Conditions

Characteristics	Symbol	Min	Тур.	Max		Unit
Input Current, ON	I _{F (ON)}	7	8	10		mA
Input Voltage, OFF	$V_{F(OFF)}$	0	_	0.8		V
Supply Voltage	V _{CC}	15	_	30	20	V
Peak Output Current	I _{OPH} / I _{OPL}	_	_	±0.5		Α
Operating Temperature	T_{opr}	-20	25	70	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

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Electrical Characteristics (Ta = -20~70°C, Unless otherwise specified)

Characteris	tics	Symbol	Test Circuit	Test Condition		Min	Тур.	Max	Unit	
Input Forward Voltage		V _F	_	I _F = 10 mA, Ta = 25°C		_	1.6	1.8	V	
Temperature Coefficient of Forward Voltage		ΔV _F /ΔTa	_	I _F = 10 mA			-2.0		mV /°C	
Input Reverse Current		I _R	_	V _R = 5 V, Ta = 25°C			_	10	μA	
Input Capacitance		C _T	_	V = 0, f = 1 MHz, Ta = 25°C		_	45	250	pF	
	"H" Level	I _{OPH}	2	V _{CC} = 30 \		I _F = 10 mA V ₈₋₆ = 4 V	-1.0	-1.5		А
Output Current	"L" Level	I _{OPL}	1	(*1)		$I_F = 0$ $V_{6-5} = 2.5 \text{ V}$	1.0	2	1	^
Output Valtage	"H" Level	V _{OH}	3	$V_{CC1} = +15 \text{ V}$ $V_{EE1} = -15 \text{ V}$ $R_L = 200\Omega$, $I_F = 5 \text{ mA}$		11	12.8	-		
Output Voltage	"L" Level	V _{OL}	4	$V_{CC1} = +15 \text{ V}$ $V_{EE1} = -15 \text{ V}$ $R_L = 200\Omega$, $V_F = 0.8 \text{ V}$		_	-14.2	-12.5	V	
	"H" Level	Іссн	_	- V _{CC} = 30 V	I _F =	= 10 mA = 25°C	_	7	_	mA
Supply Current					I _F =	= 10 mA	-	_	11	
Зарргу Сапен	"L" Level	I _{CCL}			I _F = 0 mA Ta = 25°C			7.5	_	mA
					I _F =	= 0 mA		1	11	
Threshold Input Current	L→H	I _{FLH}		$\begin{split} &V_{\text{CC1}} = +15 \text{ V} \\ &V_{\text{EE1}} = -15 \text{ V} \\ &R_{\text{L}} = 200\Omega, V_{\text{O}} > 0 \text{V} \\ \\ &V_{\text{CC1}} = +15 \text{ V} \\ &V_{\text{EE1}} = -15 \text{ V} \\ &R_{\text{L}} = 200\Omega, V_{\text{O}} < 0 \text{V} \end{split}$		I	1.2	5	mA	
Threshold Input Voltage	H→L	V_{FHL}				0.8	_	_	V	
Supply Voltage		V _{CC}	_	_		10	-	35	V	
Capacitance (Input-Out	Capacitance (Input-Output)		_	V _S = 0, f = 1 MHz, Ta = 25°C		_	1.0	2.0	pF	
Resistance (Input-Output)		R _s	_	V _S = 500 V, Ta = 25°C R.H.≤60%		1×10 ¹²	10 ¹⁴	_	Ω	

(*) : All typical values are at Ta=25°C

(*1) : Duration of IO time $\leq 50\mu s$

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Switching Characteristics ($Ta = -20 \sim 70^{\circ}$ C,Unless otherwise specified)

Characteristics		Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Propagation	L→H	t _{pLH}			0.05	0.15	0.5	
Delay Time	H→L	t _{pHL}		I _F = 8 mA,	0.05	0.15	0.5	
Switching Time Dispersion between ON and OFF Output Rise Time		tpHL-tpLH	5	$V_{CC} = 15 \text{ V}$ $R_L = 20\Omega, C_L = 10 \text{nF}$	_	_	0.45	μs
		t _r			_		_	
Output Fall Time		t _f			_		_	
Common Mode Transient Immunity at High Level Output		CM _H	6	V _{CM} = 1000 V, I _F = 8 mA V _{CC} = 30 V, Ta = 25°C	-15000	_	_	V /µs
Common Mode Transient Immunity at Low Level Output		CML		V _{CM} = 1000 V, I _F = 0 mA V _{CC} = 30 V, Ta = 25°C	15000	ı	_	V /µs

Fig.1 I_{OPL} Test Circuit

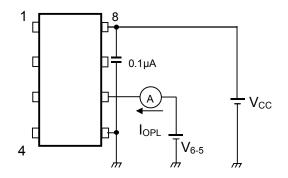


Fig.2 I_{OPH} Test Circuit

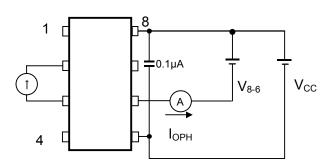


Fig.3 V_{OH} Test Circuit

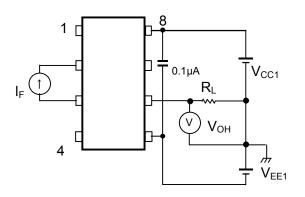


Fig.4 V_{OL} Test Circuit

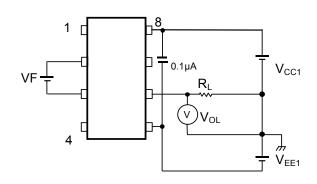


Fig.5 tpLH, tpHL, tr, tf Test Circuit

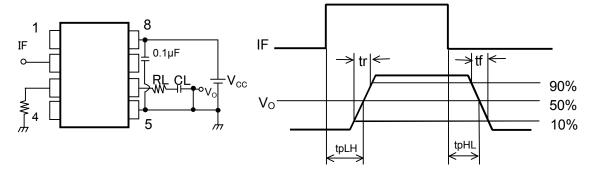
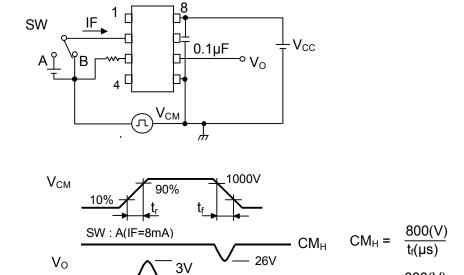


Fig.6 CM_H, CM_L Test Circuit



CML(CMH) is the maximum rate of rise(fall) of the common mode voltage that can be sustained with the output voltage in the low(high)state.

SW: B(IF=0mA)

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