

TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOSVI-H)

TPCA8053-H

Switching Regulator Applications Motor Drive Applications DC-DC Converter Applications

- Small footprint due to a small and thin package
- High-speed switching
- Small gate charge: Q_{SW} = 6.9 nC (typ.)
- Low drain-source ON-resistance: R_{DS (ON)} = 13.9 mΩ (typ.)
- High forward transfer admittance: |Y_{fs}| = 46 S (typ.)
- Low leakage current: I_{DSS} = 10 μ A (max) (V_{DS} = 60 V)
- Enhancement mode: V_{th} = 1.3 to 2.3 V (V_{DS} = 10 V, I_D = 0.2 mA)

Absolute Maximum Ratings (Ta = 25°C)

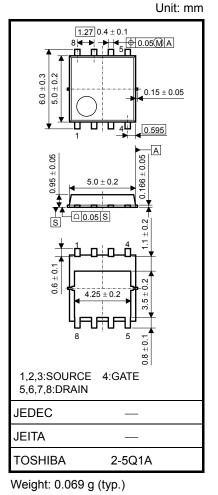
Characte	ristic	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	60	V	
Drain-gate voltage (R	t _{GS} = 20 kΩ)	V _{DGR}	60	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	۱ _D	15	А	
Drain current	Pulsed (Note 1)	I _{DP}	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	τ.	
Drain power dissipati	on (Tc = 25°C)	PD	30	W	
Drain power dissipati	on (t = 10 s) (Note 2a)	PD	2.8	W	
Drain power dissipati	on (t = 10 s) (Note 2b)	PD	1.6	W	
Single-pulse avalance	ne energy (Note 3)	E _{AS}	16	mJ	
Avalanche current		I _{AR}	15	А	
Repetitive avalanche (To	energy c = 25°C) (Note 4)	E _{AR}	1.53	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature	range	T _{stg}	–55 to 150	°C	

Note: For Notes 1 to 4, refer to the next page.

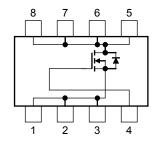
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.

operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.



Circuit Configuration



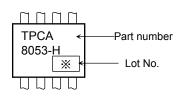
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Thermal Characteristics

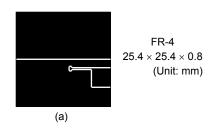
Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case (Tc = 25°C)	R _{th (ch-c)}	4.17	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	44.6	°C/W
Thermal resistance, channel to ambient $(t = 10 \text{ s})$ (Note 2b)	R _{th (ch-a)}	78.1	°C/W

Marking (Note 5)

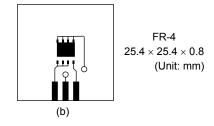


Note 1: Ensure that the channel temperature does not exceed 150°C.

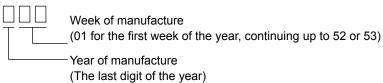
Note 2: (a) Device mounted on a glass-epoxy board (a)



(b) Device mounted on a glass-epoxy board (b)



- Note 3: V_DD = 24 V, T_{ch} = 25 ^{\circ}C (initial), L = 100 μ H, R_G = 25 Ω , I_{AR} = 15 A
- Note 4: Repetitive rating: pulse width limited by maximum channel temperature
- Note 5: * Weekly code: (Three digits)



Electrical Characteristics (Ta = 25°C)

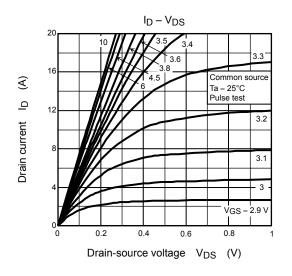
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS}=\pm 20~V,~V_{DS}=0~V$	_		±100	nA
Drain cutoff curre	ent	I _{DSS}	$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_	_	10	μA
Drain source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	60	—	—	v
Drain-source brea	in-source breakdown voltage		$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	43	_	_	
Gate threshold vo	oltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 0.2 \text{ mA}$	1.3	—	2.3	V
Desis anno ON assistante		R _{DS (ON)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 7.5 \text{ A}$	_	15.6	24.0	mΩ
Drain-source ON	ain-source ON-resistance		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 7.5 \text{ A}$	_	13.9	22.3	
Forward transfer	admittance	Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 7.5 \text{ A}$	23	46	_	S
Input capacitance	Input capacitance			_	1620	2110	
Reverse transfer	capacitance	C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	60	90	pF
Output capacitance		C _{oss}		_	200	_	
Gate resistance		rg	V_{DS} = 10 V, V_{GS} = 0 V, f = 5 MHz		2.3	3.5	Ω
Switching time	Rise time	tr	$V_{GS} \stackrel{10}{}_{0}V \qquad I_{D} = 7.5 \text{ A}$	_	2.4	_	ns
	Turn-on time	t _{on}			9.1	_	
	Fall time	t _f			7.0	_	
	Turn-off time	t _{off}	$V_{DD}\approx 30~V$ Duty \leq 1%, $t_W=10~\mu s$		33	_	
Total gate charge		Qg	$V_{DD} \approx 48$ V, $V_{GS} = 10$ V, $I_D = 15$ A	_	25	_	
(gate-source plus	-source plus gate-drain)		$V_{DD}\approx 48~V,~V_{GS}=5~V,~I_{D}=15~A$		13		
Gate-source charge 1		Q _{gs1}	$V_{DD} \approx 48$ V, $V_{GS} = 10$ V, $I_D = 15$ A		5.5		nC
Gate-drain ("Miller") charge		Q _{gd}			4.4		
Gate switch char	ge	Q _{SW}]	_	6.9	—	

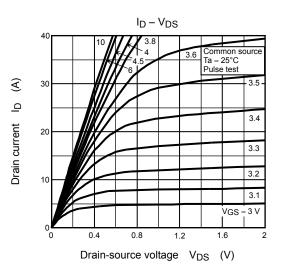
Source-Drain Ratings and Characteristics (Ta = 25°C)

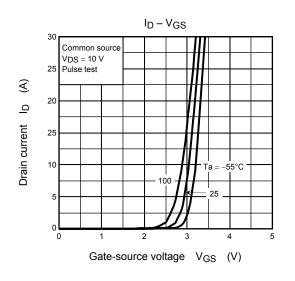
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I _{DRP}	—	_	_	45	А
Forward voltage (diode)			V _{DSF}	I _{DR} = 15 A, V _{GS} = 0 V	_		-1.2	V

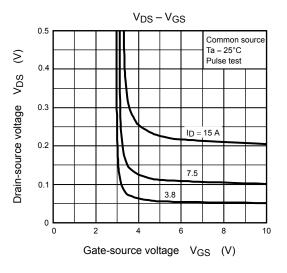
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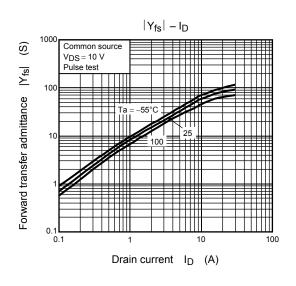
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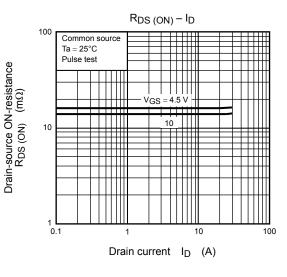




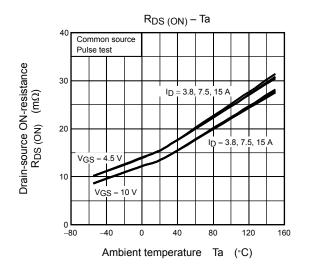


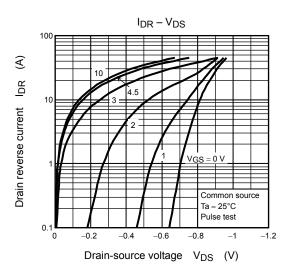


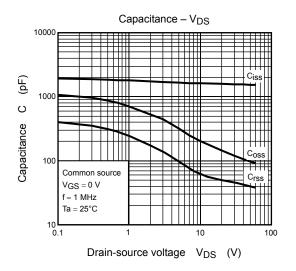


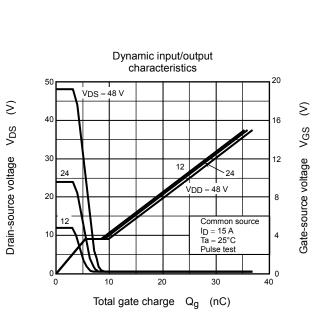


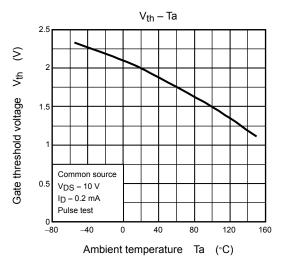
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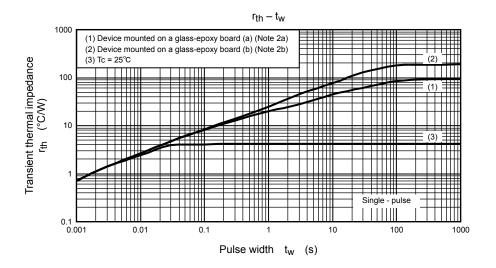


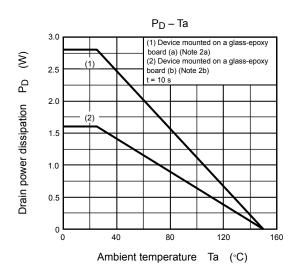


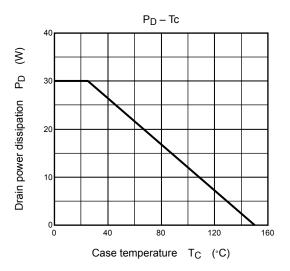


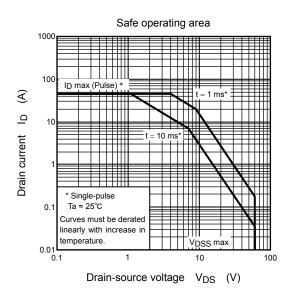












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