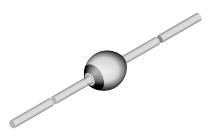


Vishay Semiconductors

Standard Avalanche Sinterglass Diode



949539

FEATURES

- Glass passivated junction
- · Hermetically sealed package
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition



(e2)

ROHS COMPLIANT HALOGEN FREE

MECHANICAL DATA

Case: SOD-57

Terminals: plated axial leads, solderable per MIL-STD-750,

method 2026

Polarity: color band denotes cathode end

Mounting position: any **Weight:** approx. 369 mg

APPLICATIONS

- High voltage rectification
- Efficiency diode in horizontal deflection circuits

PARTS TABLE				
PART	TYPE DIFFERENTIATION	PACKAGE		
BY448	V _R = 1500 V; I _{FAV} = 2 A	SOD-57		
BY458	V _R = 1200 V; I _{FAV} = 2 A	SOD-57		

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
Deverse velters	See electrical characteristics	BY448	$V_R = V_{RRM}$	1500	V
Reverse voltage		BY458	$V_R = V_{RRM}$	1200	V
Peak forward surge current	t _p = 10 ms, half sine wave		I _{FSM}	30	Α
Average forward current			I _{FAV}	2	Α
Junction temperature			Tj	140	°C
Storage temperature range			T _{stg}	- 55 to + 175	°C
Non repetitive reverse avalanche energy	I _{(BR)R} = 0.4 A		E _R	10	mJ

MAXIMUM THERMAL RESISTANCE (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION SYMBOL VALUE			UNIT
Junction ambient	I = 10 mm, T _L = constant	R_{thJA}	45	K/W
	On PC board with spacing 25 mm	R_{thJA}	100	K/W

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX	UNIT
Forward voltage	I _F = 3 A	V _F	-	-	1.6	V
Reverse current	$V_R = V_{RRM}$	I _R	-	-	3	μA
	$V_R = V_{RRM}$, $T_j = 140$ °C	I _R	-	-	140	μA
Reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_R = 0.25 \text{ A}$	t _{rr}	-	-	2000	ns
Total reverse recovery time	$I_F = 1 A$, - $dI_F/dt = 0.05 A/\mu s$	t _{rr}	-	-	20	μs

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TYPICAL CHARACTERISTICS ($T_{amb} = 25$ °C, unless otherwise specified)

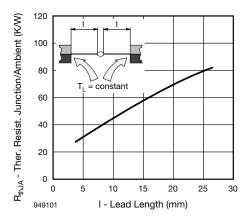


Fig. 1 - Typ. Thermal Resistance vs. Lead Length

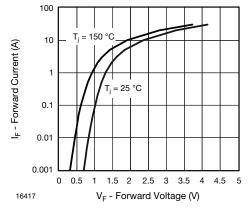


Fig. 2 - Forward Current vs. Forward Voltage

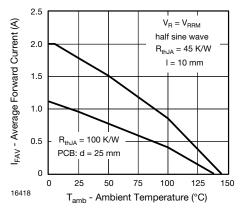


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

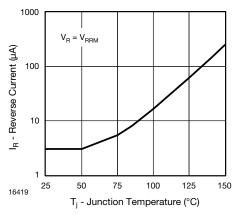


Fig. 4 - Reverse Current vs. Junction Temperature

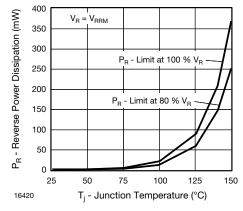


Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

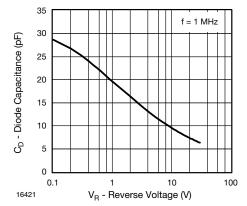


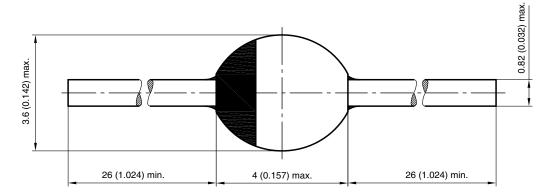
Fig. 6 - Diode Capacitance vs. Reverse Voltage



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PACKAGE DIMENSIONS in millimeters (inches): SOD-57



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