LFXO TF OSCILLATORS

Low Power Coinsumption SMD Crystal Oscillator

32.768kHz

5 7000 ₹32.7K

FEATURES

- Very low power consumption
- **Tight frequency tolerance**
- **CMOS Output**
- Optional output Eanable/Disable with Tri-state
- Low EMI emission
- Full military testing available

DESCRIPTION

The 32.7368kHz LFXO oscillator has been designed especially for applications requiring low power consumption, as low as 500nA. The oscillator consists of a miniature tuning fork quartz crystal and a CMOS/TTL compatible hybrid circuit. Each crystal is pre-qualified before assembly by electrical tests and temperature characterization.

SPECIFICATION

Specifications are typical at 25°C unless otherwise indicated.

Specifications are liable to change without notice. Frequency:

1.8V to +5.0 Volts ±10% Supply Voltage: Calibration Tolerance: ± 10 , ± 50 or ± 100 ppm

Frequency Stability

over Operating Temperature Range

0° ~ +50°C: ±25ppm -40° ~ +85°C: ±100ppm -55° ~ +125°C: ±100ppm

Ageing: ±1ppm year typical

±3ppm per year maximum. 5000g, 0.3ms, 1/2 sine Shock, Survival: Vibration Survival: 20g, 10~2000Hz swept sine

Frequency Change vs.

10% Output Load Change: ±1ppm maximum.

Operating Temperature Range

-10° to +70°C Commercial: -40° to +85°C Industrial: -55° to +125°C Military:

Typical Current Consumption

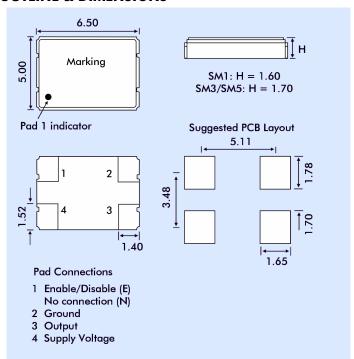
Vdd = 1.8V: 0.5μΑ Vdd = 2.5V: 0.6μΑ Vdd = 3.3V: $0.7\mu A$ Vdd = 5.0V: $1.3\mu A$ Output: **CMOS**

12ns typical, 25ns maximum Rise/Fall Times:

Duty Cycle: 45/55%

Process Temperature: 260°C, 20 seconds -55° to +125°C Storage Temperature:

OUTLINE & DIMENSIONS



ENABLE/DISABLE OPTIONS

There are two Enable/Disable options available, 'E' and 'N'. The 'E' option stops oscillating when the output is put into the High Z state. the 'N' version does not have Pad 1 connected internally. The table below describes the 'E' Enable/Disable option.

	Enable (Pad 1 High)	Disable (Pad 1 Low)
Output	Frequency Output	High Z state
	Oscillates	Stops
Current	Normal	Very Low

When Pad 1 is allowed to float it is held high (output enabled) by an internal pull-up resistor.

HOW TO ORDER LFXO SMD CRYSTAL OSCILLATORS

