

GS2N7002K**N-CHANNEL ENHANCEMENT MODE POWER MOSFET**

BV _{DSS}	60V
R _{DS(ON)}	2Ω
I _D	640mA

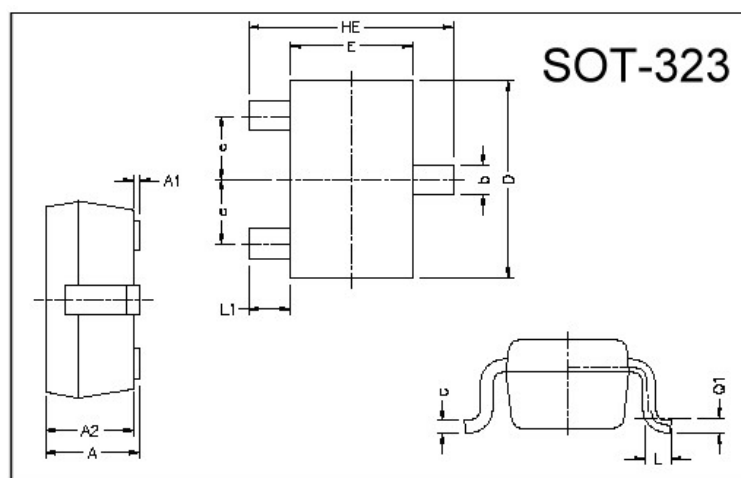
Description

The GS2N7002K utilized advanced processing techniques to achieve the lowest possible on-resistance, extremely efficient and cost-effectiveness device.

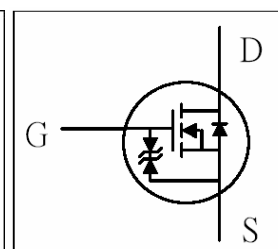
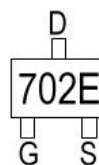
The GS2N7002K is universally used for all commercial-industrial applications.

Features

- *Simple Drive Requirement
- *Small Package Outline
- *RoHS Compliant

Package Dimensions

Marking :



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	0.80	1.10	L1	0.42 REF.	
A1	0	0.10	L	0.15	0.35
A2	0.80	1.00	b	0.25	0.40
D	1.80	2.20	c	0.10	0.25
E	1.15	1.35	e	0.65 REF.	
HE	1.80	2.40	Q1	0.15 BSC.	

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current ³ , V _{GS} @10V	I _D @TA=25°C	640	mA
Continuous Drain Current ³ , V _{GS} @10V	I _D @TA=70°C	500	mA
Pulsed Drain Current ^{1,2}	I _{DM}	950	mA
Power Dissipation	P _D @TA=25°C	0.35	W
Linear Derating Factor		0.003	W/°C
Operating Junction and Storage Temperature Range	T _j , T _{stg}	-55 ~ +150	°C

Thermal Data

Parameter	Symbol	Ratings	Unit
Thermal Resistance Junction-ambient ³ Max.	R _{thj-a}	360	°C/W

Electrical Characteristics(T_j = 25°C Unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV _{DSS}	60	-	-	V	V _{GS} =0, I _D =250uA
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS} / \Delta T_j$	-	0.06	-	V/°C	Reference to 25°C, I _D =1mA
Gate Threshold Voltage	V _{GS(th)}	1.0	-	3.0	V	V _{DS} =V _{GS} , I _D =250uA
Forward Transconductance	g _{fs}	-	600	-	mS	V _{DS} =10V, I _D =600mA
Gate-Source Leakage Current	I _{GSS}	-	-	±10	uA	V _{GS} = ±20V
Drain-Source Leakage Current(T _j =25°C)	I _{DSS}	-	-	1	uA	V _{DS} =60V, V _{GS} =0
Drain-Source Leakage Current(T _j =70°C)		-	-	100	uA	V _{DS} =48V, V _{GS} =0
Static Drain-Source On-Resistance	R _{DS(ON)}	-	-	2	Ω	V _{GS} =10V, I _D =500mA
		-	-	4		V _{GS} =4.5V, I _D =200mA
Total Gate Charge ²	Q _g	-	1	1.6	nC	I _D =600mA V _{DS} =50V V _{GS} =4.5V
Gate-Source Charge	Q _{gs}	-	0.5	-		
Gate-Drain ("Miller") Charge	Q _{gd}	-	0.5	-		
Turn-on Delay Time ²	T _{d(on)}	-	12	-	ns	V _{DS} =30V I _D =600mA V _{GS} =10V R _G =3.3Ω R _D =52Ω
Rise Time	T _r	-	10	-		
Turn-off Delay Time	T _{d(off)}	-	56	-		
Fall Time	T _f	-	29	-		
Input Capacitance	C _{iss}	-	32	50	pF	V _{GS} =0V V _{DS} =25V f=1.0MHz
Output Capacitance	C _{oss}	-	8	-		
Reverse Transfer Capacitance	C _{rss}	-	6	-		

Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward On Voltage ²	V _{SD}	-	-	1.2	V	I _S =200mA, V _{GS} =0V

Notes: 1. Pulse width limited by Max. junction temperature.

2. Pulse width ≤ 300us, duty cycle ≤ 2%.

3. Surface mounted on FR4 board, t ≤ 10sec.

Characteristics Curve

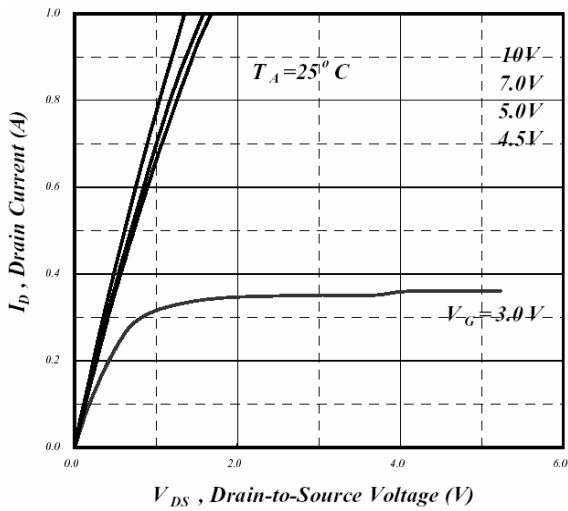


Fig 1. Typical Output Characteristics

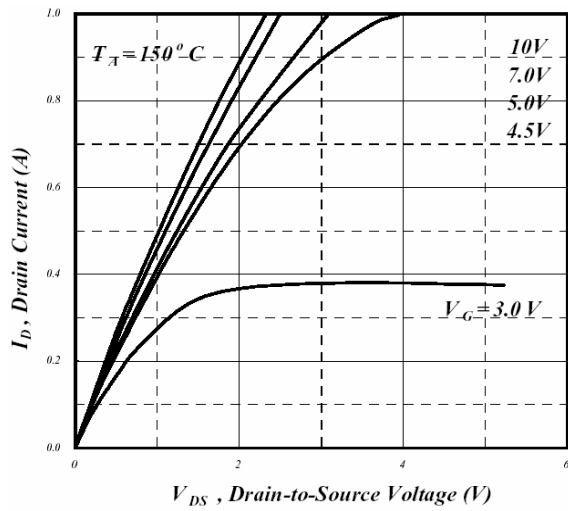


Fig 2. Typical Output Characteristics

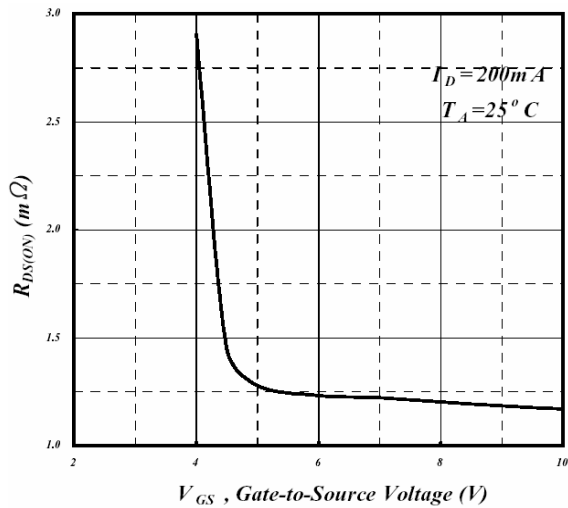


Fig 3. On-Resistance v.s. Gate Voltage

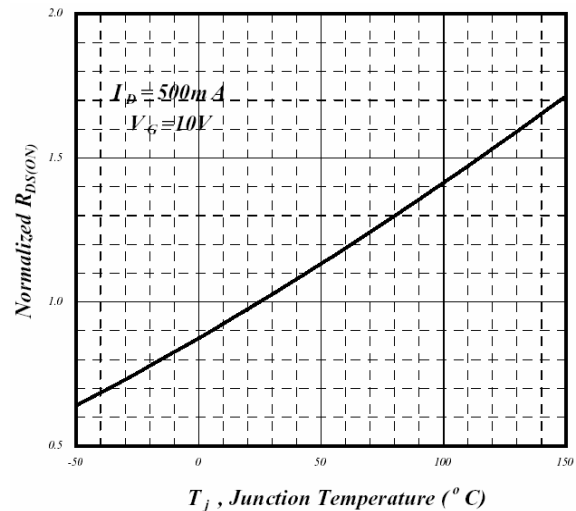


Fig 4. Normalized On-Resistance v.s. Junction Temperature

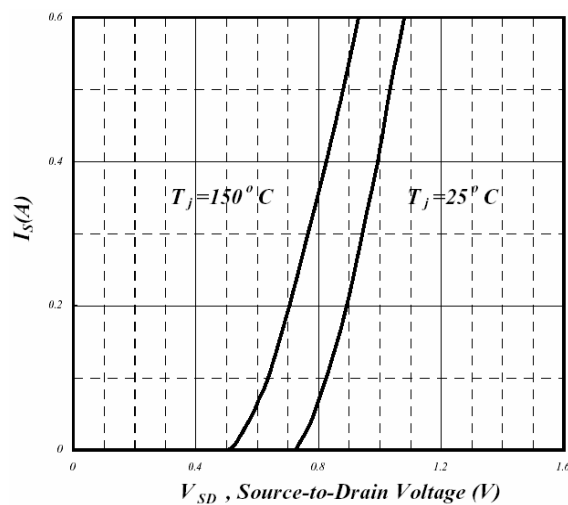


Fig 5. Forward Characteristics of Reverse Diode

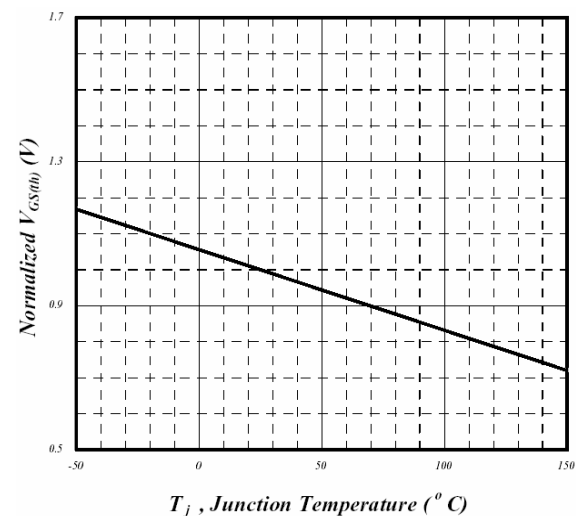


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

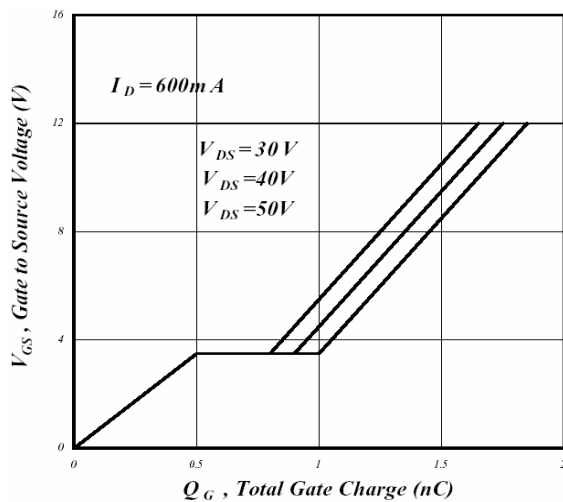


Fig 7. Gate Charge Characteristics

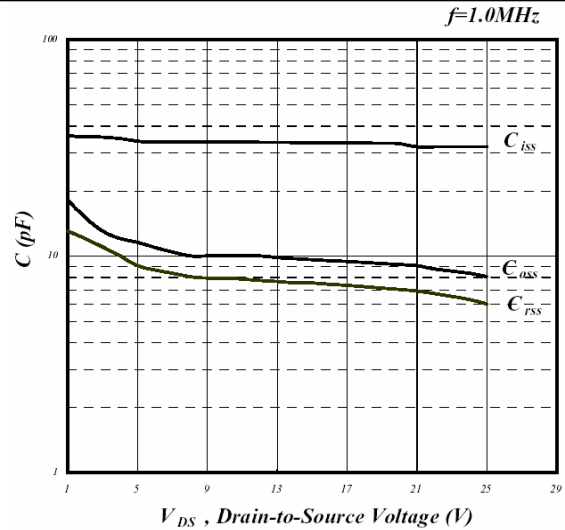


Fig 8. Typical Capacitance Characteristics

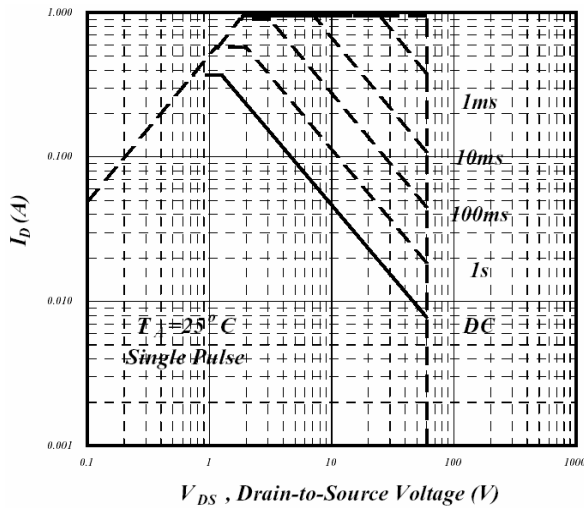


Fig 9. Maximum Safe Operating Area

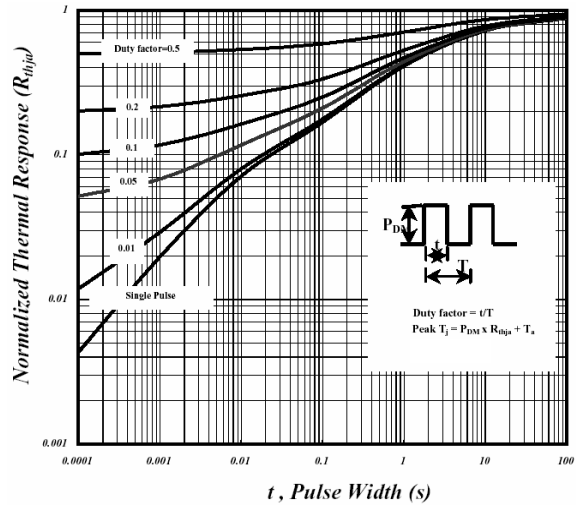


Fig 10. Effective Transient Thermal Impedance

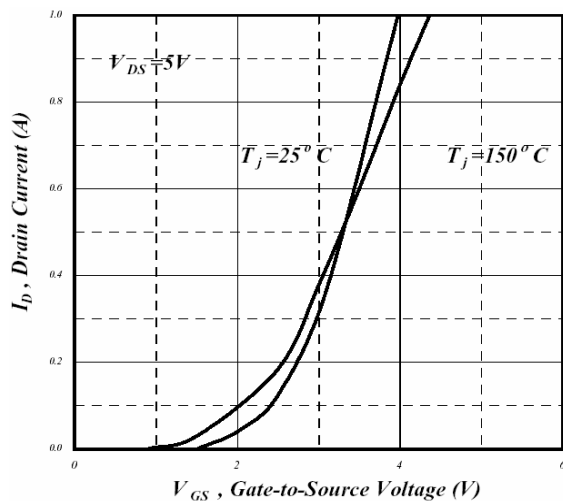


Fig 11. Transfer Characteristics

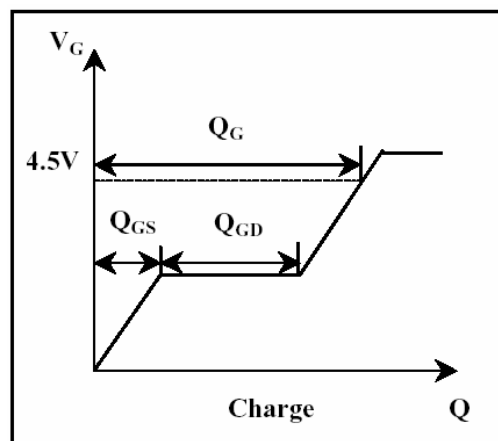


Fig 12. Gate Charge Waveform

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Head Office And Factory:

- Taiwan: No. 17-1 Tatung Rd. Fu Kou Hsin-Chu Industrial Park, Hsin-Chu, Taiwan, R. O. C.
- TEL : 886-3-597-7061 FAX : 886-3-597-9220, 597-0785
- China: (201203) No.255, Jang-Jiang Tsai-Lueng RD. , Pu-Dung-Hsin District, Shang-Hai City, China
- TEL : 86-21-5895-7671 ~ 4 FAX : 86-21-38950165