

WNMD6003

Dual N-Channel, 60V, 0.30A, Power MOSFET

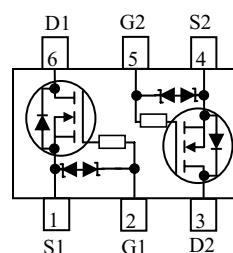
V _{DS} (V)	R _{ds(on)} (Ω)
60	1.4 @ V _{GS} =10V
	1.7 @ V _{GS} =4.5V
ESD Rating:2000V HBM	



SOT-563

Descriptions

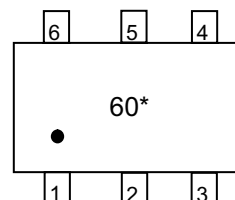
The WNMD6003 is Dual N-Channel enhancement MOS Field Effect Transistor. Uses advanced trench technology and design to provide excellent R_{DS(ON)} with low gate charge. This device is suitable for use in DC-DC conversion, power switch and charging circuit. Standard Product WNMD6003 is Pb-free and Halogen-free.



Pin configuration (Top view)

Features

- Trench Technology
- Supper high density cell design
- Excellent ON resistance for higher DC current
- Extremely Low Threshold Voltage
- Small package SOT-563



60 = Device Code
* = Month (A~Z)

Marking

Applications

- Driver for Relay, Solenoid, Motor, LED etc.
- DC-DC converter circuit
- Power Switch
- Load Switch
- Charging

Order information

Device	Package	Shipping
WNMD6003-6/TR	SOT-563	3000/Reel&Tape

Absolute Maximum ratings

Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V_{DS}	60		V
Gate-Source Voltage		V_{GS}	± 20		
Continuous Drain Current ^{ad}	$T_A=25^\circ\text{C}$	I_D	0.30	0.27	A
	$T_A=70^\circ\text{C}$		0.24	0.21	
Maximum Power Dissipation ^{ad}	$T_A=25^\circ\text{C}$	P_D	0.37	0.29	W
	$T_A=70^\circ\text{C}$		0.23	0.18	
Continuous Drain Current ^{bd}	$T_A=25^\circ\text{C}$	I_D	0.26	0.23	A
	$T_A=70^\circ\text{C}$		0.20	0.19	
Maximum Power Dissipation ^{bd}	$T_A=25^\circ\text{C}$	P_D	0.27	0.22	W
	$T_A=70^\circ\text{C}$		0.17	0.14	
Pulsed Drain Current ^c		I_{DM}	1.0		A
Operating Junction Temperature		T_J	-55 to 150		$^\circ\text{C}$
Lead Temperature		T_L	260		$^\circ\text{C}$
Storage Temperature Range		T_{stg}	-55 to 150		$^\circ\text{C}$

Thermal resistance ratings

Parameter		Symbol	Typical	Maximum	Unit
Junction-to-Ambient Thermal Resistance ^a	$t \leq 10$ s	$R_{\theta JA}$	285	335	$^\circ\text{C/W}$
	Steady State		340	430	
Junction-to-Ambient Thermal Resistance ^b	$t \leq 10$ s	$R_{\theta JA}$	380	460	
	Steady State		465	555	
Junction-to-Case Thermal Resistance		$R_{\theta JC}$	280	320	

a Surface mounted on FR-4 Board using 1 square inch pad size, 1oz copper.

b Surface mounted on FR-4 board using minimum pad size, 1oz copper.

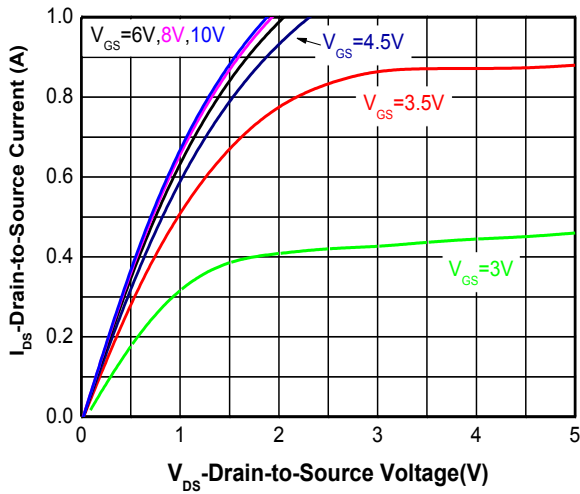
c Pulse width < 380 μs .

d Maximum junction temperature $T_J=150^\circ\text{C}$.

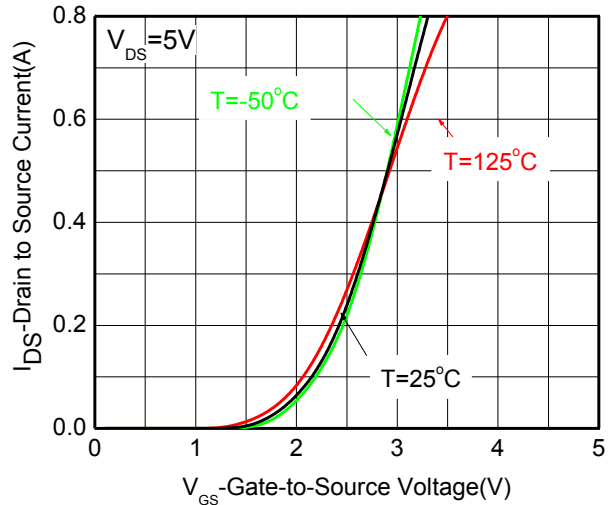
Electronics Characteristics (Ta=25°C, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0\text{ V}, I_D = 250\mu\text{A}$	60			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 60\text{ V}, V_{GS} = 0\text{ V}$			1	μA
Gate-to-source Leakage Current	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 5	μA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = 250\mu\text{A}$	0.8	1.3	2	V
Drain-to-source On-resistance ^{b, c}	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 0.3\text{ A}$		1.4	2.0	Ω
		$V_{GS} = 4.5\text{ V}, I_D = 0.2\text{ A}$		1.7	2.6	
Forward Transconductance	g_{FS}	$V_{DS} = 15\text{ V}, I_D = 0.25\text{ A}$		0.42		S
CAPACITANCES, CHARGES						
Input Capacitance	C_{ISS}	$V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz},$ $V_{DS} = 25\text{ V}$		23.37		pF
Output Capacitance	C_{OSS}			7.33		
Reverse Transfer Capacitance	C_{RSS}			5.2		
Total Gate Charge	$Q_{G(TOT)}$	$V_{GS} = 10\text{ V},$ $V_{DD} = 30\text{ V},$ $I_D = 0.37\text{ A}$		1.2		nC
Threshold Gate Charge	$Q_{G(TH)}$			0.15		
Gate-to-Source Charge	Q_{GS}			0.21		
Gate-to-Drain Charge	Q_{GD}			0.12		
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	$t_d(ON)$	$V_{DD} = 30\text{ V}, R_L = 150\Omega$ $I_D = 0.2\text{ A}, V_{GEN} = 10\text{ V},$ $R_G = 10\Omega$		7.6		ns
Rise Time	t_r			5.1		
Turn-Off Delay Time	$t_d(OFF)$			24.6		
Fall Time	t_f			10		
BODY DIODE CHARACTERISTICS						
Forward Voltage	V_{SD}	$V_{GS} = 0\text{ V}, I_S = 0.3\text{ A}$		0.9	1.5	V

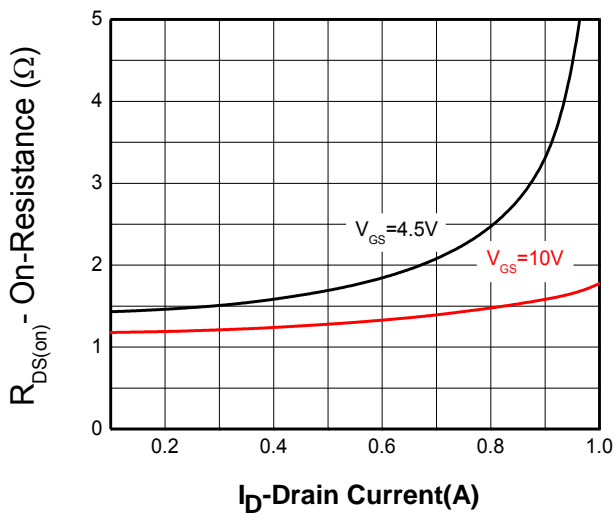
Typical Characteristics (Ta=25°C, unless otherwise noted)



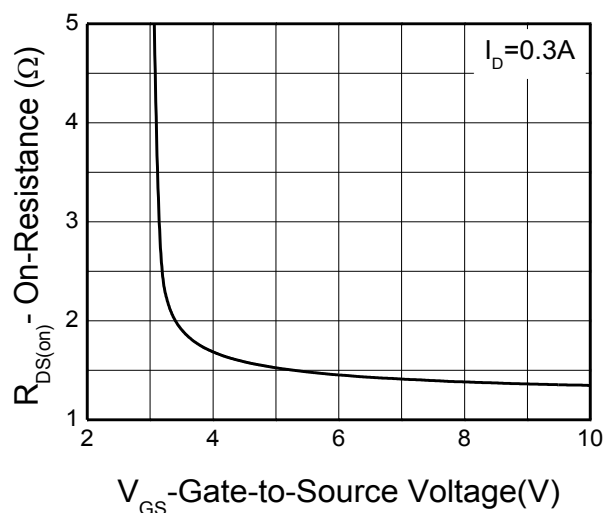
Output characteristics



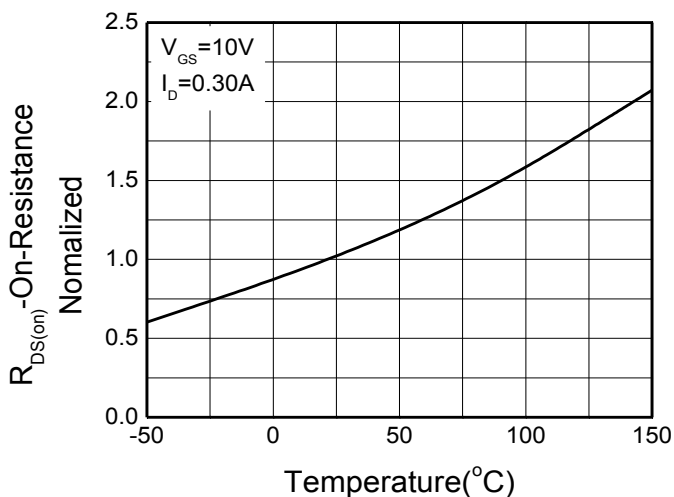
Transfer characteristics



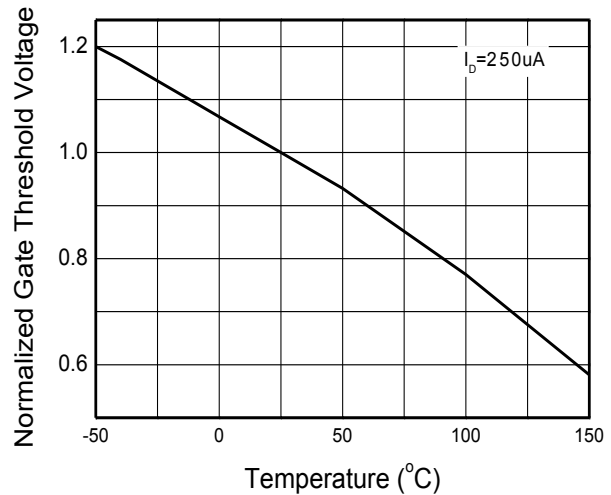
On-Resistance vs. Drain current



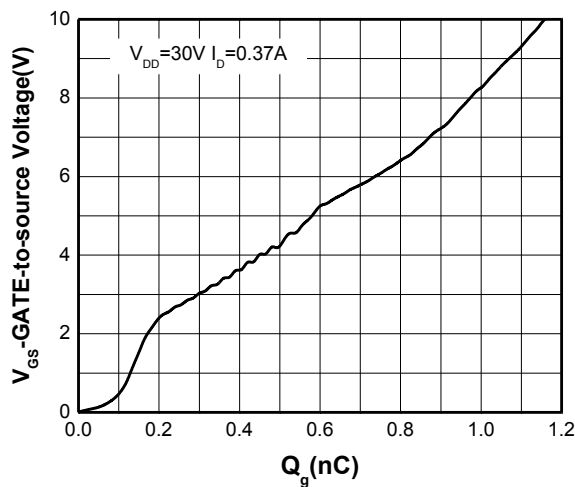
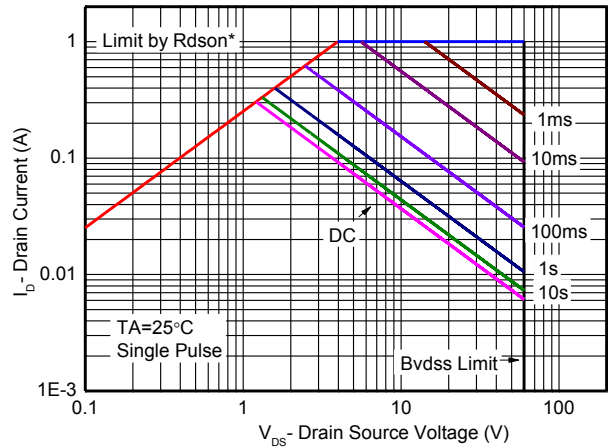
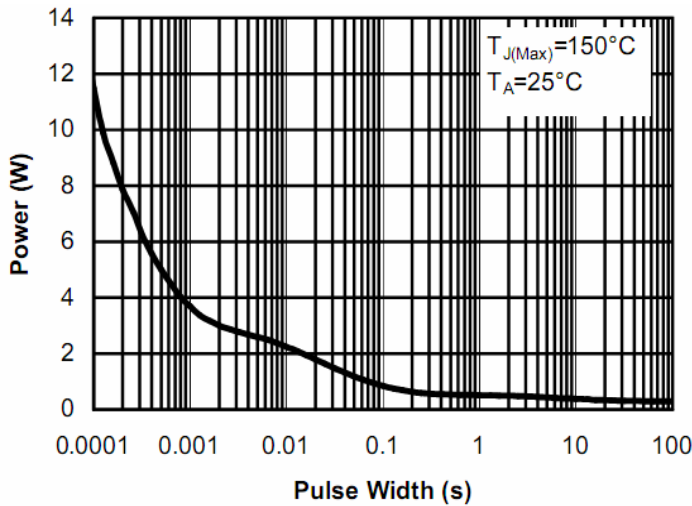
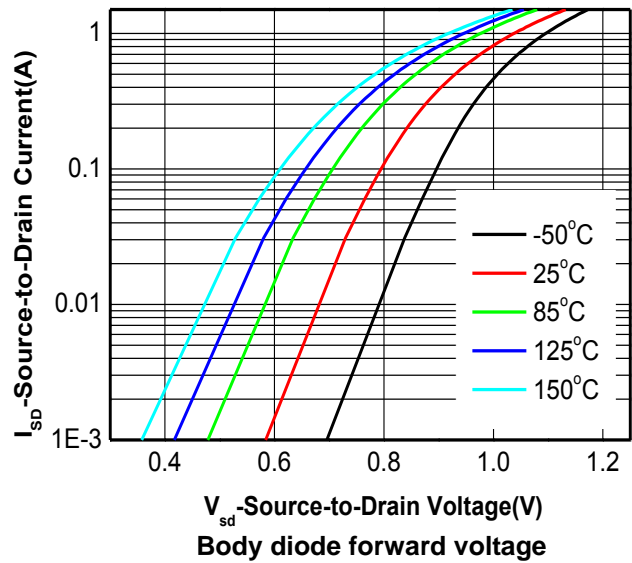
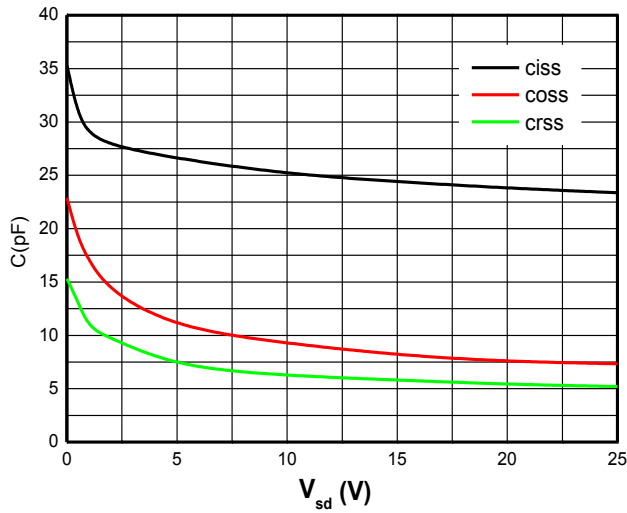
On-Resistance vs. Gate-to-Source voltage

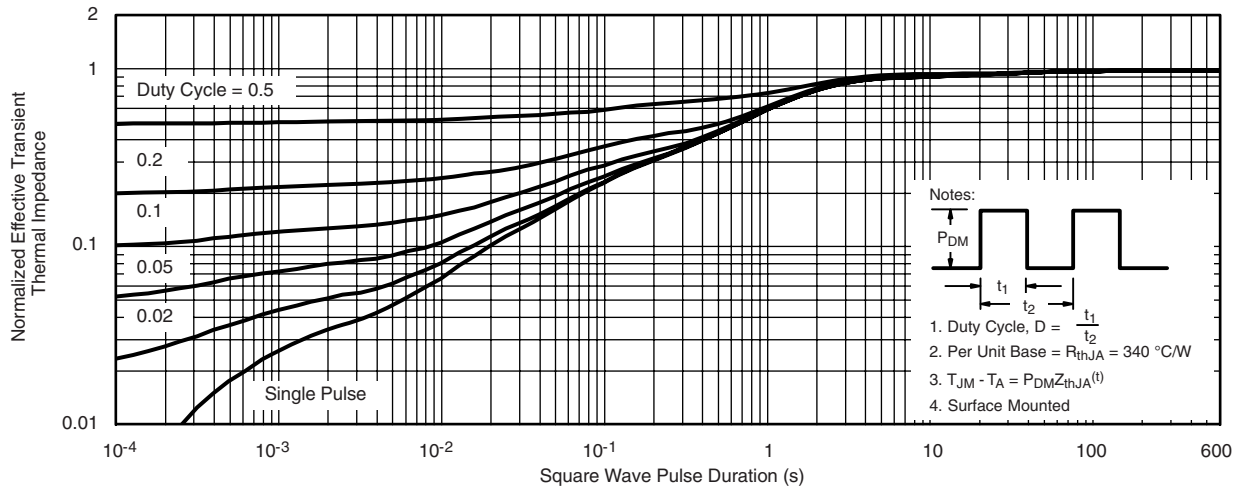


On-Resistance vs. Junction temperature



Threshold voltage vs. Temperature

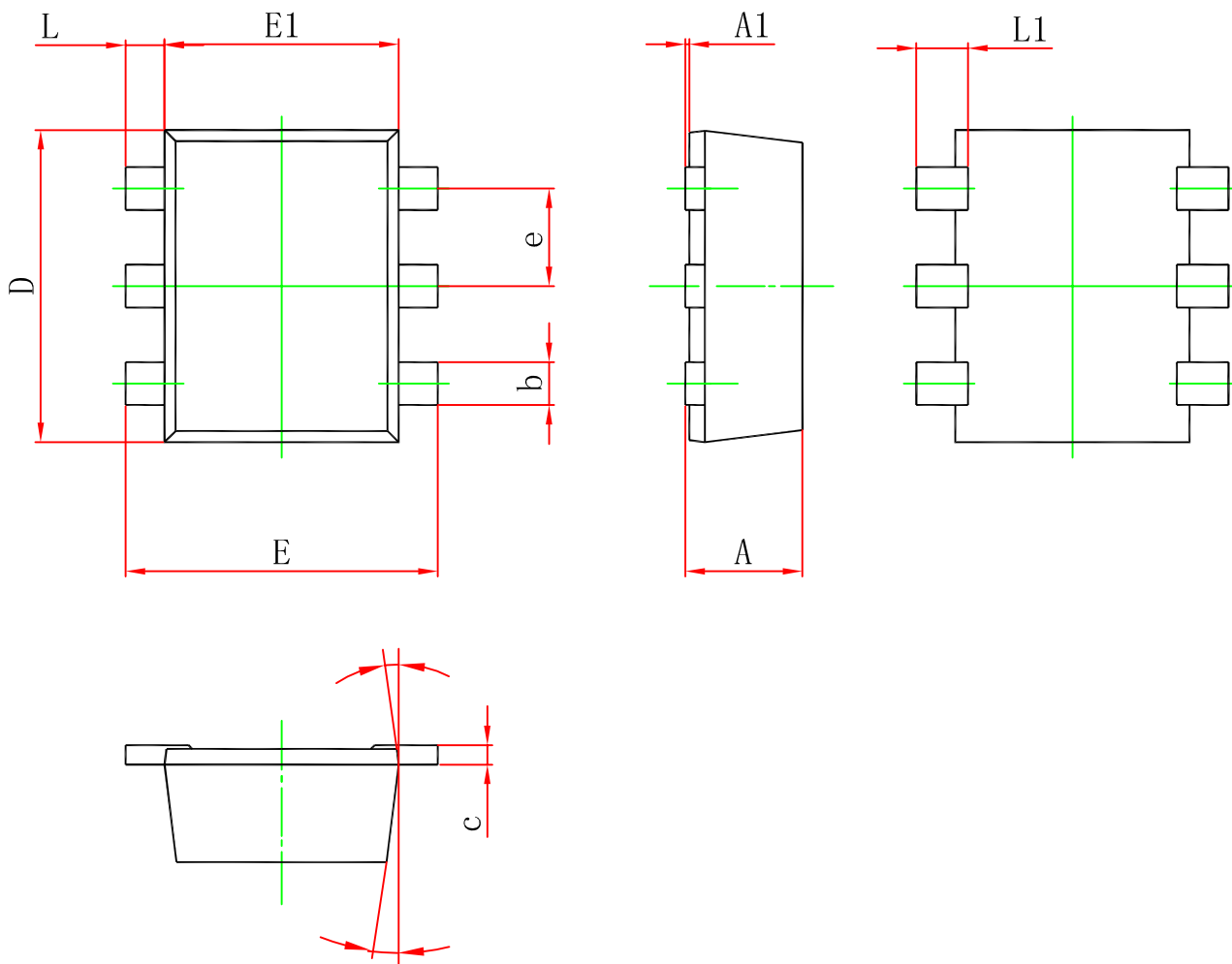




Transient thermal response (Junction-to-Ambient)

Package outline dimensions

SOT-563



Symbol	Dimensions In Millimeters		Dimensions in inches	
	Min.	Max.	Min.	Max.
A	0.525	0.600	0.021	0.024
A1	0.000	0.050	0.000	0.002
e	0.450	0.550	0.018	0.022
c	0.090	0.160	0.004	0.006
D	1.500	1.700	0.059	0.067
b	0.170	0.270	0.007	0.011
E1	1.100	1.300	0.043	0.051
E	1.500	1.700	0.059	0.067
L	0.100	0.300	0.004	0.012
L1	0.200	0.400	0.008	0.016
θ	7° REF.		7° REF.	