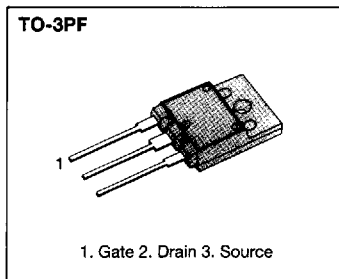


FEATURES

- Lower $R_{DS(ON)}$
- Improved inductive ruggedness
- Fast switching times
- Rugged polysilicon gate cell structure
- Lower input capacitance
- Extended safe operating area
- Improved high temperature reliability



PRODUCT SUMMARY

Part Number	V_{DSS}	$R_{DS(ON)}$	I_D
IRFS140	100V	0.077 Ω	19.4A
IRFS141	60V	0.077 Ω	19.4A

ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	IRFS140	IRFS141	Unit
Drain-Source Voltage (1)	V_{DSS}	100	60	Vdc
Drain-Gate Voltage ($R_{GS}=1.0M\Omega$)(1)	V_{DGR}	100	60	Vdc
Gate-Source Voltage	V_{GS}	± 20		Vdc
Continuous Drain Current $T_C=25^\circ C$	I_D	19.4		A
Continuous Drain Current $T_C=100^\circ C$	I_D	13.8		A
Drain Current - Pulsed (3)	I_{DM}	112		A
Gate Current - Pulsed	I_{GM}	± 1.5		A
Single Pulsed Avalanche Energy (4)	E_{AS}	108		mJ
Avalanche Current	I_{AS}	19.4		A
Total Power Dissipation at $T_C=25^\circ C$	P_D	83		Watts
Derate Above $25^\circ C$		0.67		W/ $^\circ C$
Operating and Storage Junction Temperature Range	T_J, T_{STG}	-55 to +150		$^\circ C$
Maximum Lead Temp. for Soldering Purposes, 1/8" from case for 5 seconds	T_L	300		$^\circ C$

Notes : (1) $T_J=25^\circ C$ to $150^\circ C$

(2) Pulse test : Pulse width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

(3) Repetitive rating : Pulse width limited by junction temperature

(4) $L=0.53mH$, $V_{DD}=25V$, $R_G=25\Omega$, Starting $T_J=25^\circ C$

ELECTRICAL CHARACTERISTICS (T_C=25°C unless otherwise specified)

Symbol	Characteristic	Min	Typ	Max	Units	Test Conditions
BV _{DSS}	Drain-Source Breakdown Voltage					
	IRFS140	100	-	-	V	V _{GS} =0V, I _D =250μA
	IRFS141	60	-	-	V	
V _{GS(th)}	Gate Threshold Voltage	2.0	-	4.0	V	V _{DS} =V _{GS} , I _D =250μA
I _{GSS}	Gate-Source Leakage Forward	-	-	100	nA	V _{GS} =20V
I _{GSS}	Gate-Source Leakage Reverse	-	-	-100	nA	V _{GS} =-20V
I _{DSS}	Zero Gate Voltage Drain Current	-	-	250	μA	V _{DS} =Max. Rating, V _{GS} =0V
		-	-	1000	μA	V _{DS} =0.8 Max. Rating, V _{GS} =0V, T _C =150°C
R _{DS(on)}	Static Drain-Source On-Resistance(2)	-	-	0.077	Ω	V _{GS} =10V, I _D =14A
g _{fs}	Forward Transconductance (2)	8.7	10.8	-	∅	V _{DS} ≥50V, I _D =14A
C _{iss}	Input Capacitance	-	1500	-	pF	V _{GS} =0V, V _{DS} =25V, f=1MHz
C _{oss}	Output Capacitance	-	500	-	pF	
C _{rss}	Reverse Transfer Capacitance	-	90	-	pF	
t _{d(on)}	Turn-On Delay Time	-	-	23	ns	V _{DD} =0.5 BV _{DSS} , I _D =28A, Z _o =9.1Ω (MOSFET switching times are essentially independent of operating temperature)
t _r	Rise Time	-	-	10	ns	
t _{d(off)}	Turn-Off Delay Time	-	-	60	ns	
t _f	Fall Time	-	-	75	ns	
Q _g	Total Gate Charge (Gate-Source Plus Gate-Drain)	-	-	68	nC	
Q _{gs}	Gate-Source Charge	-	13.3	-	nC	V _{GS} =10V, I _D =28.0A, V _{DS} =0.8 Max. Rating (Gate charge is essentially independent of operating temperature)
Q _{gd}	Gate-Drain ("Miller") Charge	-	29.3	-	nC	

THERMAL RESISTANCE

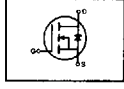
Symbol	Characteristics		All	Units	Remark
R _{thJC}	Junction-to-Case	MAX	1.5	K/W	
R _{thCS}	Case-to-Sink	TYP	0.24	K/W	Mounting surface flat
R _{thJA}	Junction-to-Ambient	MAX	40	K/W	Free Air Operation

Notes : (1) T_J=25°C to 150°C

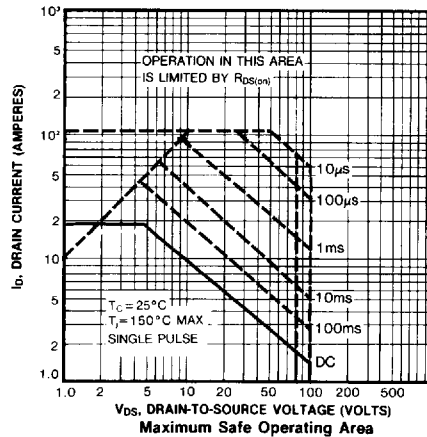
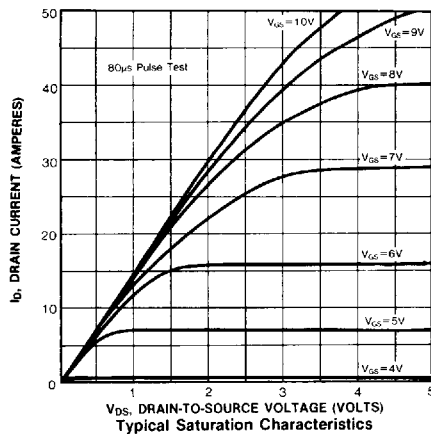
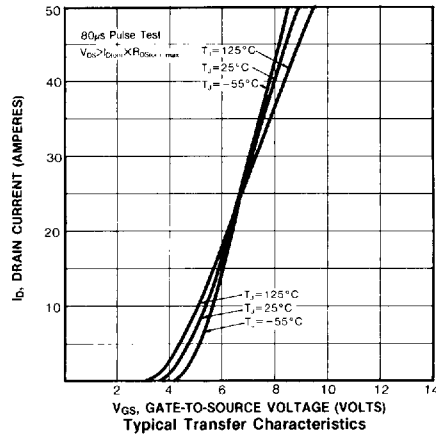
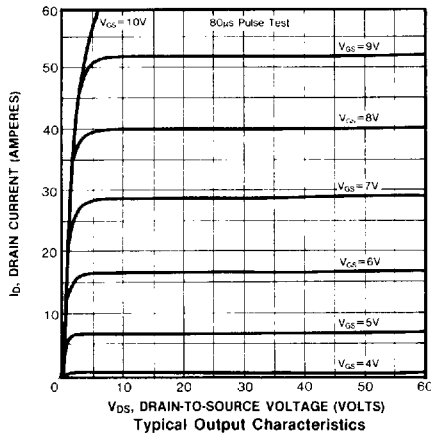
(2) Pulse test : Pulse width ≤ 300μs, Duty Cycle ≤ 2%

(3) Repetitive rating : Pulse width limited by junction temperature

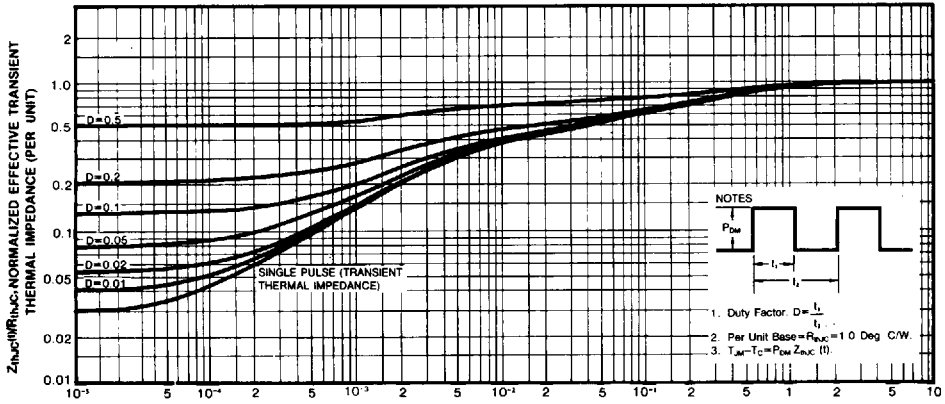
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Symbol	Characteristic	Min	Typ	Max	Units	Test Conditions
I _S	Continuous Source Current (Body Diode)	-	-	28	A	Modified MOSFET symbol showing the integral reverse P-N junction rectifier 
I _{SM}	Pulse Source Current (Body Diode) (3)	-	-	112	A	
V _{SD}	Diode Forward Voltage (2)	-	-	2.5	V	T _J =25°C, I _S =28A, V _{GS} =0V
t _r	Reverse Recovery Time	-	150	-	ns	T _J =25°C, I _F =28A, dI _F /dt=100A/μS

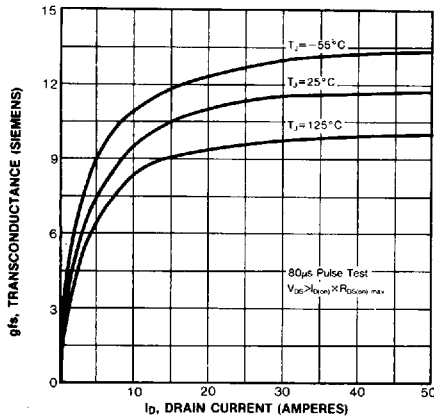
- Notes : (1) T_J=25°C to 150°C
 (2) Pulse test : Pulse width ≤ 300μs, Duty Cycle ≤ 2%
 (3) Repetitive rating : Pulse width limited by junction temperature



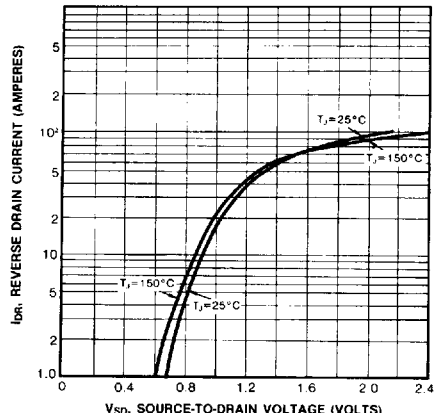
4



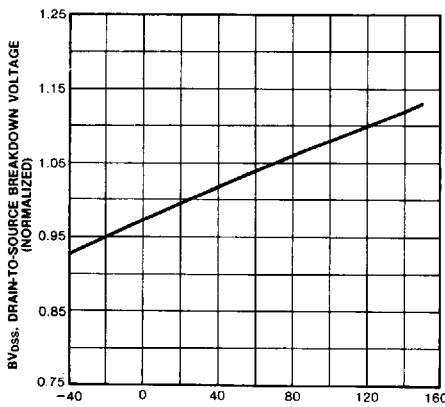
11. SQUARE WAVE PULSE DURATION (SECONDS)
Maximum Effective Transient Thermal Impedance Junction-to-Case Vs. Pulse Duration



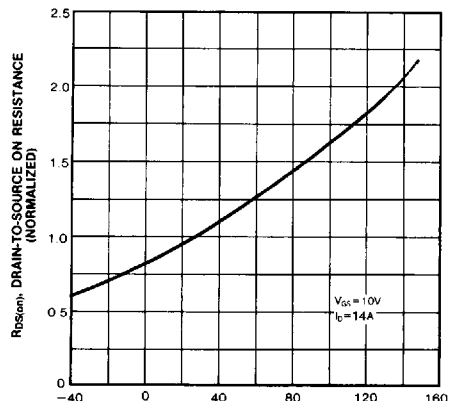
Typical Transconductance Vs. Drain Current



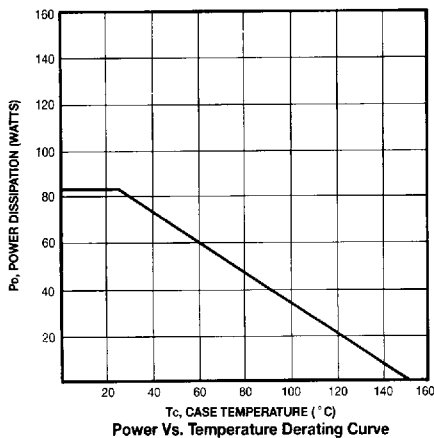
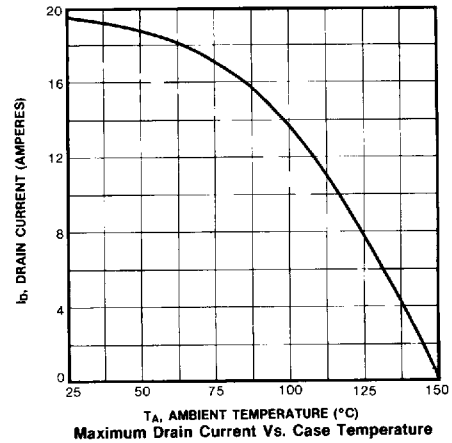
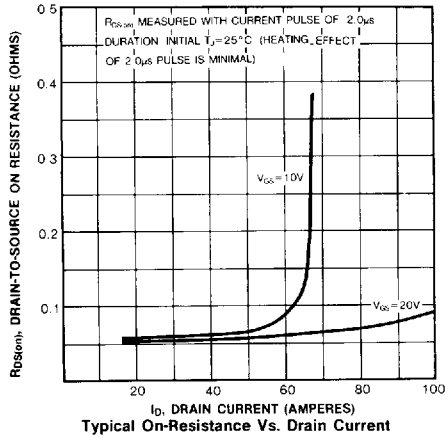
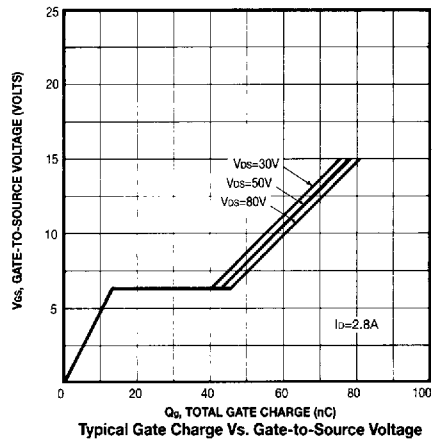
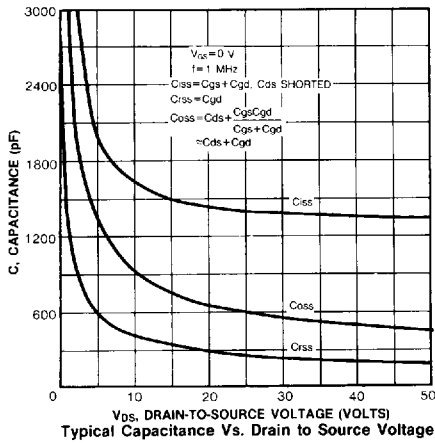
Typical Source-Drain Diode Forward Voltage



Breakdown Voltage Vs. Temperature



Normalized On-Resistance Vs. Temperature



4