



MB105F~MB110F

Miniature Glass Passivated Single-Phase Surface Mount Flat Bridge Rectifier

Major Ratings and Characteristics

$I_{F(AV)}$	1.0A
V_{RRM}	50-1000V
I_{FSM}	35 A
I_R	5.0 μ A
V_F	1.1V
$T_J \text{ max.}$	150 °C

Features

- Low profile space
- Ideal for automated placement
- Glass passivated chip junction
- Low forward voltage drop
- Low leakage current
- High forward surge capability
- High temperature soldering:
260°C/10 seconds at terminals
- Component in accordance to
RoHS 2002/95/1 and WEEE 2002/96/EC

Mechanical Data

- Case: MBF Molded plastic
over glass passivated chip
- Terminals: Solder plated, solderable per
J-STD-002B and JESD22-B102D
- Polarity: Polarity symbols marked on body

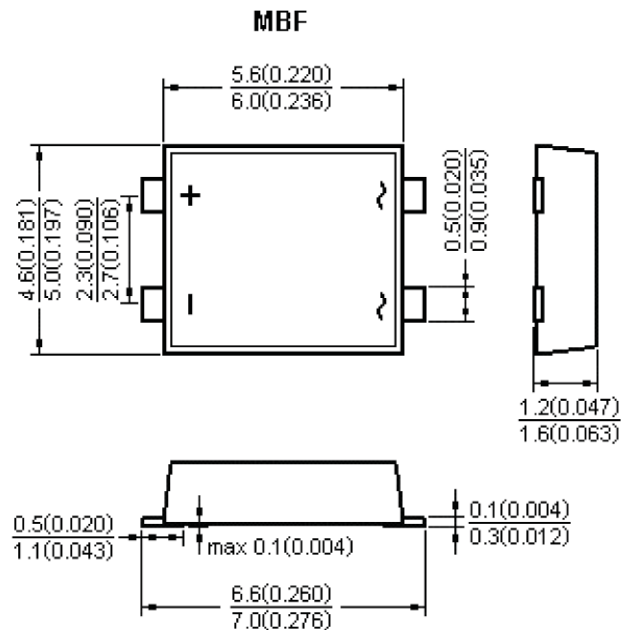
Maximum Ratings & Thermal Characteristics & Electrical Characteristics

($T_A = 25^\circ\text{C}$ unless otherwise noted)

	Symbol	MB105F	MB11F	MB12F	MB14F	MB16F	MB18F	MB110F	UNIT
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V _{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V _{DC}	50	100	200	400	600	800	1000	V
Maximum average forward output rectified current at T _A =30℃	I _{F(AV)}	1.0							A
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load(JEDEC Method)	I _{FSM}	35							A
Maximum instantaneous forwad voltage drop per leg at 1.0A	V _F	1.1							V
Maximum DC reverse current at T _A = 25 ℃ rated DC blocking voltage per leg T _A = 125℃	I _R	5.0 100							μ A
Typical junction capacitance per leg at 4.0 V ,1MHz	C _J	13							p F
Thermal resistance per leg (NOTE 1)	R _{θJA}	70							℃/ W
	R _{θJL}	20							
Operating junction and storage temperature range	T _J , T _{STG}	-55 to +150							℃

NOTE1: Units mounted on P.C.B. with 0.05×0.05" (1.3×1.3mm) pads

Patent Pending



Dimensions in millimeters and (inches)



Characteristic Curves ($T_A=25^\circ\text{C}$ unless otherwise noted)

Fig.1 Derating Curve For Output Rectified Current

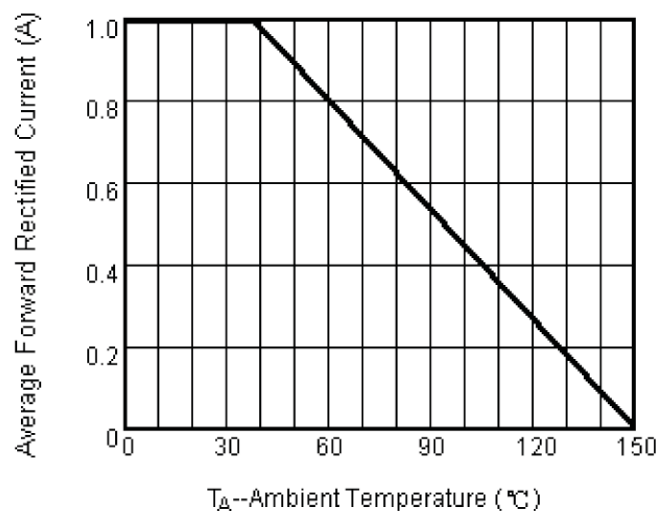


Fig.2 Maximum Non-Repetitive Peak Forward Surge Current Per Leg

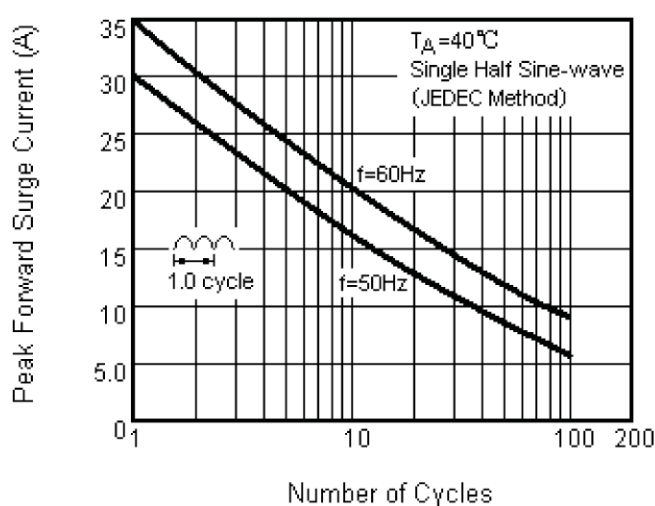


Fig.3 Typical Forward Voltage Characteristics Per Leg

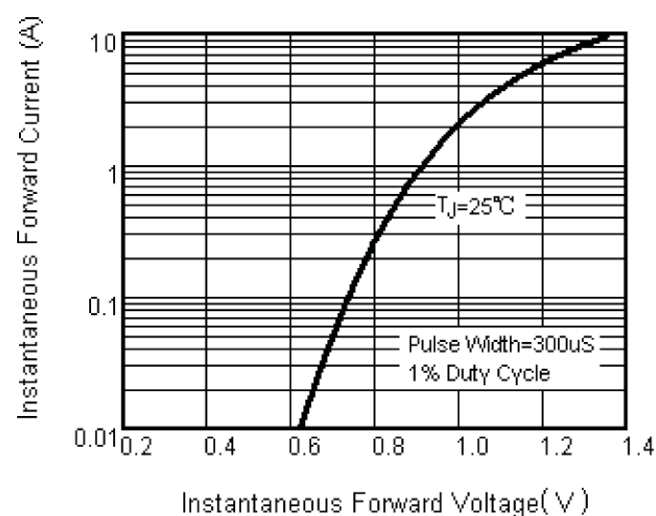


Fig.4 Typical Reverse Leakage Characteristics Per Leg

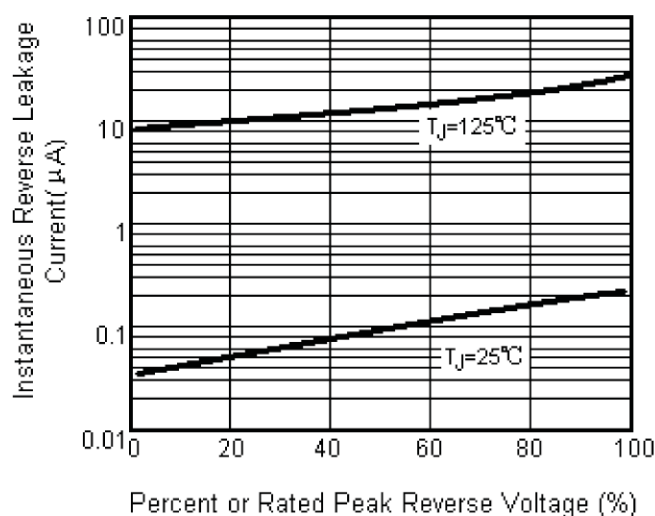


Fig.5 Typical Junction Capacitance Per Leg

