

SUR526H

Epitaxial planar NPN silicon transistor

Description

• Dual chip digital transistor

Features

- Two SRC1206 chips in SOT-353 package
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process

Package: SOT-353

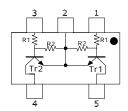
Ordering Information

SUR526H $\frac{26H}{2}$ SOT-353	Type NO.	Marking	Package Code
	SUR526H	<u>26H</u> □ ① ②	SOT-353

①Device Code ②Year&Week Code

Equivalent circuit & PIN Connections

• Equivalent Circuit



	\mathbf{R}_{1}	\mathbf{R}_2
Tr1	4.7ΚΩ	47ΚΩ
Tr2	4.7ΚΩ	47ΚΩ

PIN Connections

- 1. IN 1
- 2. COMMON 1,2
- 3. IN 2
- 4. OUT 2
- 5. OUT 1

Absolute Maximum Ratings [Tr1,Tr2]

(Ta=25°C)

Characteristic	Symbol	Rating	Unit	
Output voltage	Vo	50	V	
Input voltage	Vı	20,-5	V	
Output current	I _O	100	mA	
Power dissipation	P _D *	200	mW	
Junction temperature	TJ	150	°C	
Storage temperature range	T _{stg}	-55 ~ 150	°C	

*****: Total rating

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Electrical Characteristics [Tr1,Tr2]

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I _{O(OFF)}	V ₀ =50V, V ₁ =0	-	-	500	nA
DC current gain	G _I	$V_0 = 5V$, $I_0 = 10mA$	80	200	-	-
Output voltage	V _{O(ON)}	I _O =10mA, I _I =0.5mA	-	0.1	0.3	V
Input voltage (ON)	V _{I(ON)}	V ₀ =0.2V, I ₀ =5mA	-	0.9	1.3	V
Input voltage (OFF)	V _{I(OFF)}	V _O =5V, I _O =0.1mA	0.5	0.65	-	V
Transition frequency	f _T *	$V_0 = 10V, I_0 = 5mA, f = 1MHz$	-	200	-	MHz
Input current	I ₁	$V_1 = 5V, I_0 = 0$	-	-	1.8	mA
Input resistor (Input to base)	R ₁	-	3.3	4.7	6.1	ΚΩ
Input resistor (Base to common)	R ₂	-	33	47	61	ΚΩ

^{* :} Characteristic of transistor only

Electrical Characteristic Curves

[Tr1,Tr2]

Fig. 1 I_O - V_{I(ON)}

100

Vo=0.2V

100

Ta=25°C

Ta=25°C

100

Input on voltage VI(ON) [V]

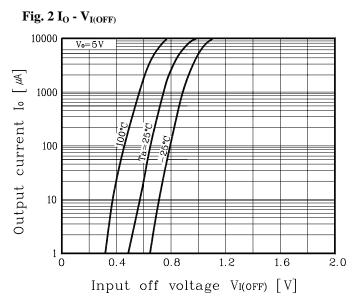
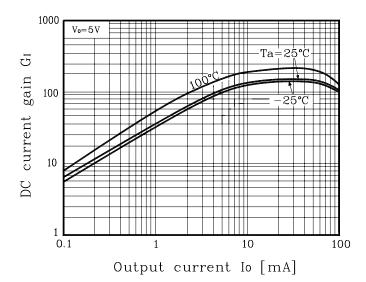


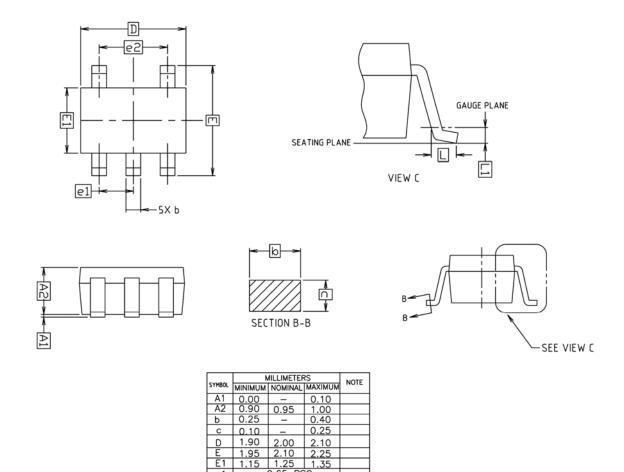
Fig. 3 G_I - I_O



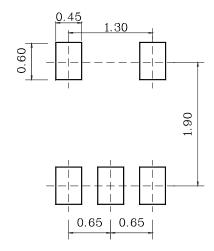
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3

Outline Dimension



* Recommend PCB solder land [Unit: mm]



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