

RoHS Compliant Product
A suffix of "-C" specifies halogen & lead-free

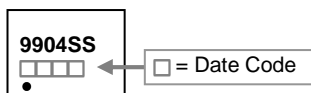
DESCRIPTION

The SSG9904 provide the designer with the best Combination of fast switching, ruggedized device design, Ultra low on-resistance and cost-effectiveness.

FEATURES

- Low on-resistance
- Simple Drive Requirement
- Double-N MosFET Package

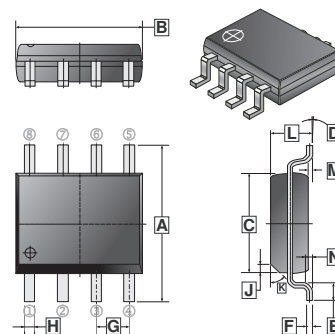
MARKING CODE



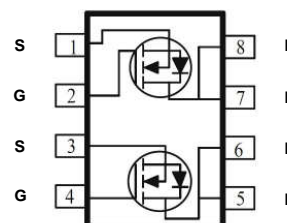
PACKAGE INFORMATION

Package	MPQ	Leader Size
SOP-8	3K	13' inch

SOP-8



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	5.80	6.20	H	0.35	0.49
B	4.80	5.00	J	0.375 REF.	
C	3.80	4.00	K	45°	
D	0°	8°	L	1.35	1.75
E	0.40	0.90	M	0.10	0.25
F	0.19	0.25	N	0.25 REF.	
G	1.27 TYP.				



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Rated	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current @ $V_{GS} = 10V$ ¹	I_D	$T_A = 25^\circ\text{C}$	7.2
		$T_A = 70^\circ\text{C}$	5.6
Pulsed Drain Current ²	I_{DM}	14.5	A
Single Pulse Avalanche Energy ³	EAS	28	mJ
Avalanche Current	I_{AS}	17.8	A
Power Dissipation @ $T_C = 25^\circ\text{C}$ ⁴	P_D	2.5	W
Operating Junction & Storage Temperature Range	T_J, T_{STG}	-55~150	$^\circ\text{C}$
Thermal Resistance Ratings			
Thermal Resistance Junction-ambient (Max.) ¹	$R_{\theta JA}$	85	$^\circ\text{C} / \text{W}$
Thermal Resistance Junction-Case (Max.) ¹	$R_{\theta JC}$	50	$^\circ\text{C} / \text{W}$

ELECTRICAL CHARACTERISTICS (T_j = 25°C unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	40	-	-	V	V _{GS} =0V, I _D =250μA
Breakdown Voltage Temp. Coefficient	ΔBV _{DS} /ΔT _j	-	0.034	-	V / °C	Reference to 25°C, I _D =1mA
Gate-Threshold Voltage	V _{GS(th)}	1	-	2.5	V	V _{DS} =V _{GS} , I _D =250μA
Forward Transconductance	G _{fs}	-	14	-	S	V _{DS} =5V, I _D =6A
Gate-Body Leakage	I _{GSS}	-	-	±100	nA	V _{GS} =±20V
Zero Gate Voltage Drain Current	I _{DSS}	-	-	1	μA	V _{DS} =32V, V _{GS} =0, T _J =25°C
		-	-	5		V _{DS} =32V, V _{GS} =0, T _J =55°C
Drain-Source On-Resistance ²	R _{DS(ON)}	-	-	26	mΩ	V _{GS} =10V, I _D =6A
		-	-	35		V _{GS} =4.5V, I _D =4A
Total Gate Charge	Q _g	-	5.5	-	nC	I _D = 6A V _{DS} = 20V V _{GS} = 4.5V
Gate-Source Charge	Q _{gs}	-	1.25	-		
Gate-Drain ("Miller") Charge	Q _{gd}	-	2.5	-		
Turn-On Delay Time	T _{d(on)}	-	8.9	-	nS	V _{DS} = 20V I _D = 1A V _{GS} = 10V R _G = 3.3Ω R _D = 20Ω
Rise Time	T _r	-	2.2	-		
Turn-Off Delay Time	T _{d(off)}	-	41	-		
Fall Time	T _f	-	2.7	-		
Input Capacitance	C _{iss}	-	593	-	pF	V _{GS} =0V V _{DS} =15V f=1.0MHz
Output Capacitance	C _{oss}	-	76	-		
Reverse Transfer Capacitance	C _{rss}	-	56	-		
Guaranteed Avalanche Characteristics						
Single Pulse Avalanche Energy ⁵	EAS	9	-	-	mJ	V _{DD} = 25V, L=0.1mH, I _{AS} = 10A
Source-Drain Diode						
Forward On Voltage ²	V _{DS}	-	-	1.2	V	I _S =1A, V _{GS} =0V
Continuous Source Current ^{1,6}	I _S	-	7.2	-	A	V _{GS} = V _{DS} =0V, Force Current
Pulsed Source Current ^{2,6}	I _{SM}	-	14.5	-	A	

Notes:

1. Surface mounted on a 1 inch² FR-4 board with 2OZ copper. 135°C/W when mounted on Min. copper pad.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
3. The EAS data shows Max. rating. The test condition is V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=17.8A.
4. The power dissipation is limited by 150°C junction temperature.
5. The Min. value is 100% EAS tested guarantee.
6. The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

CHARACTERISTICS CURVE

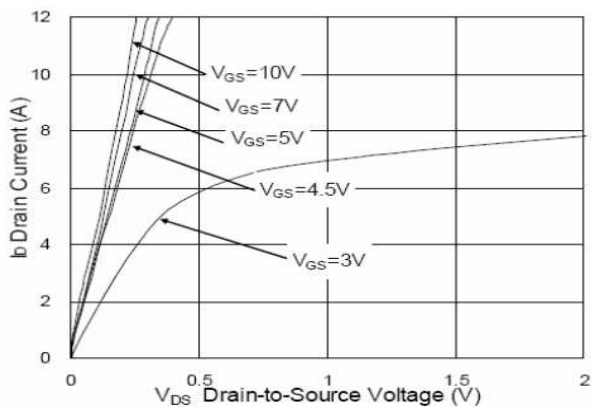


Fig.1 Typical Output Characteristics

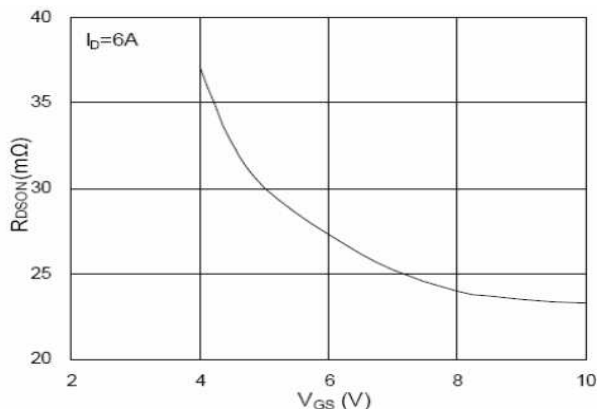


Fig.2 On-Resistance vs. G-S Voltage

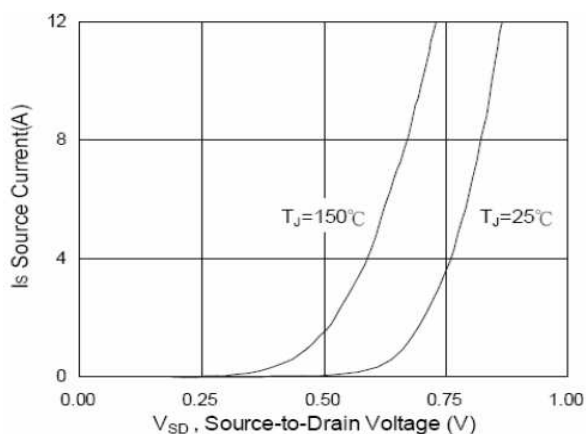


Fig.3 Forward Characteristics of Reverse

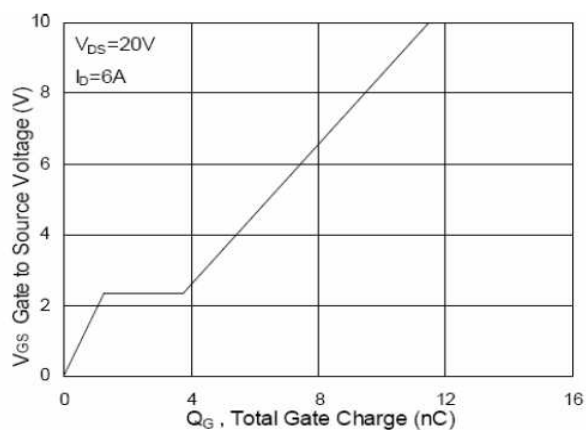


Fig.4 Gate-Charge Characteristics

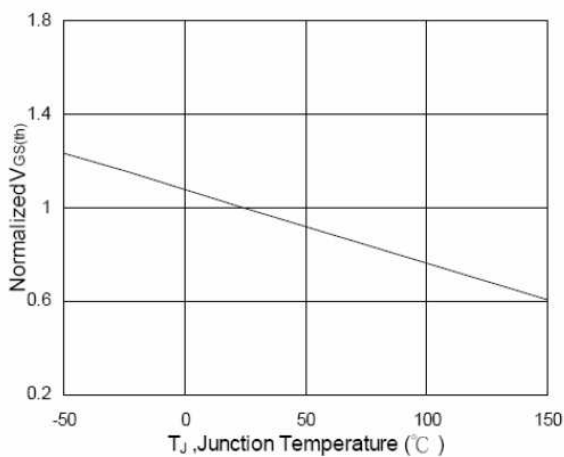


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

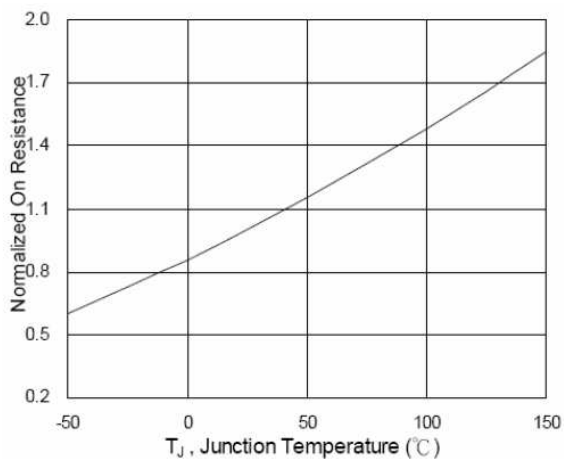


Fig.6 Normalized $R_{DS(ON)}$ vs. T_J

CHARACTERISTICS CURVE

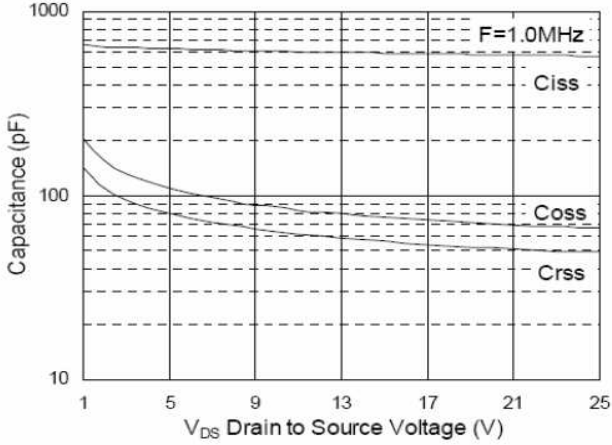


Fig.7 Capacitance

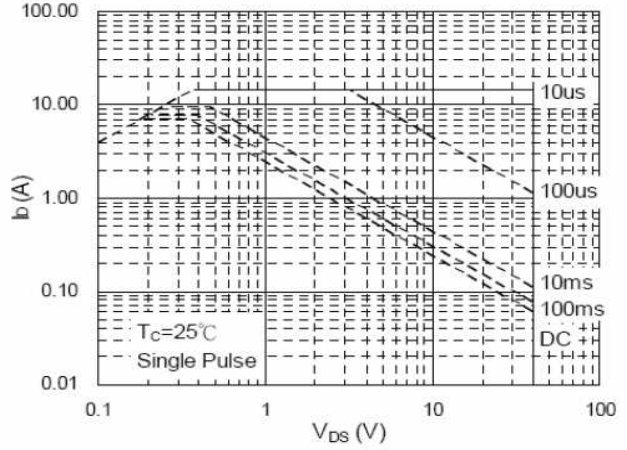


Fig.8 Safe Operating Area

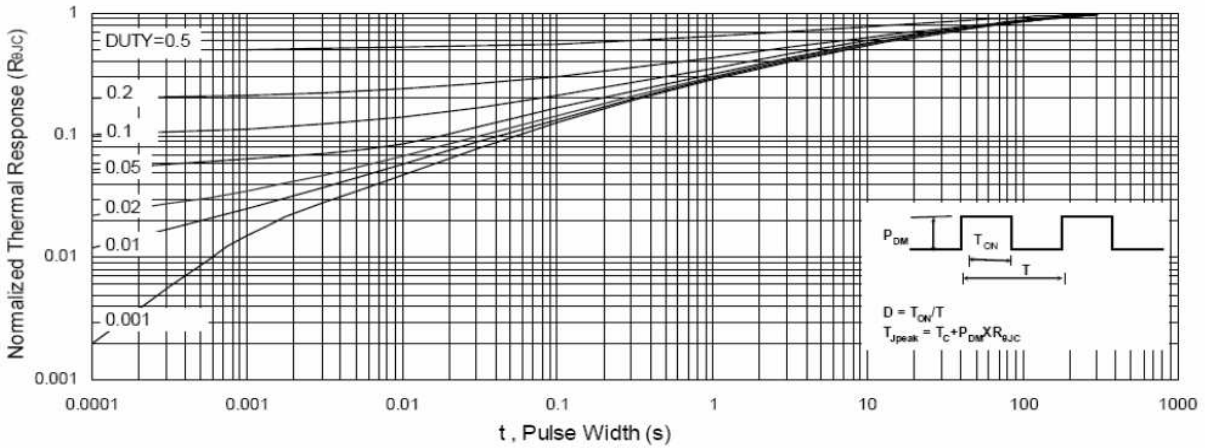


Fig.9 Normalized Maximum Transient Thermal Impedance

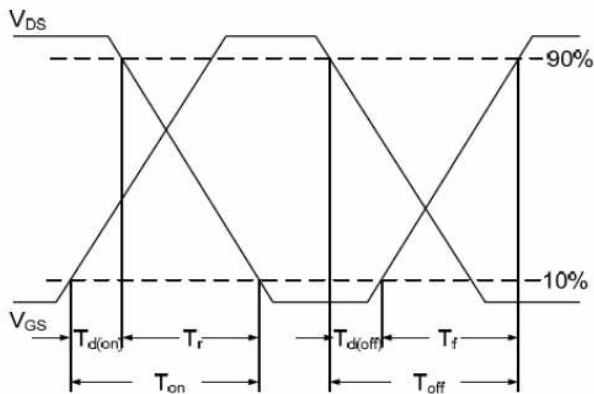


Fig.10 Switching Time Waveform

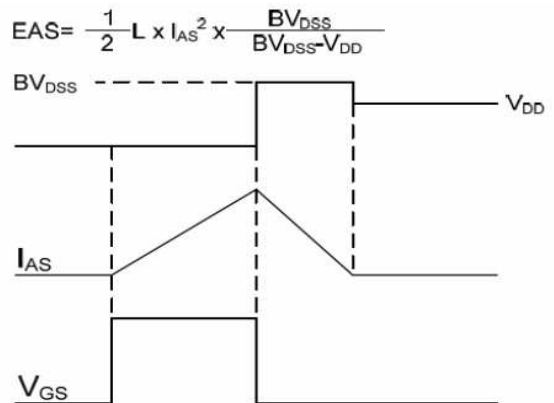


Fig.11 Unclamped Inductive Switching Wave