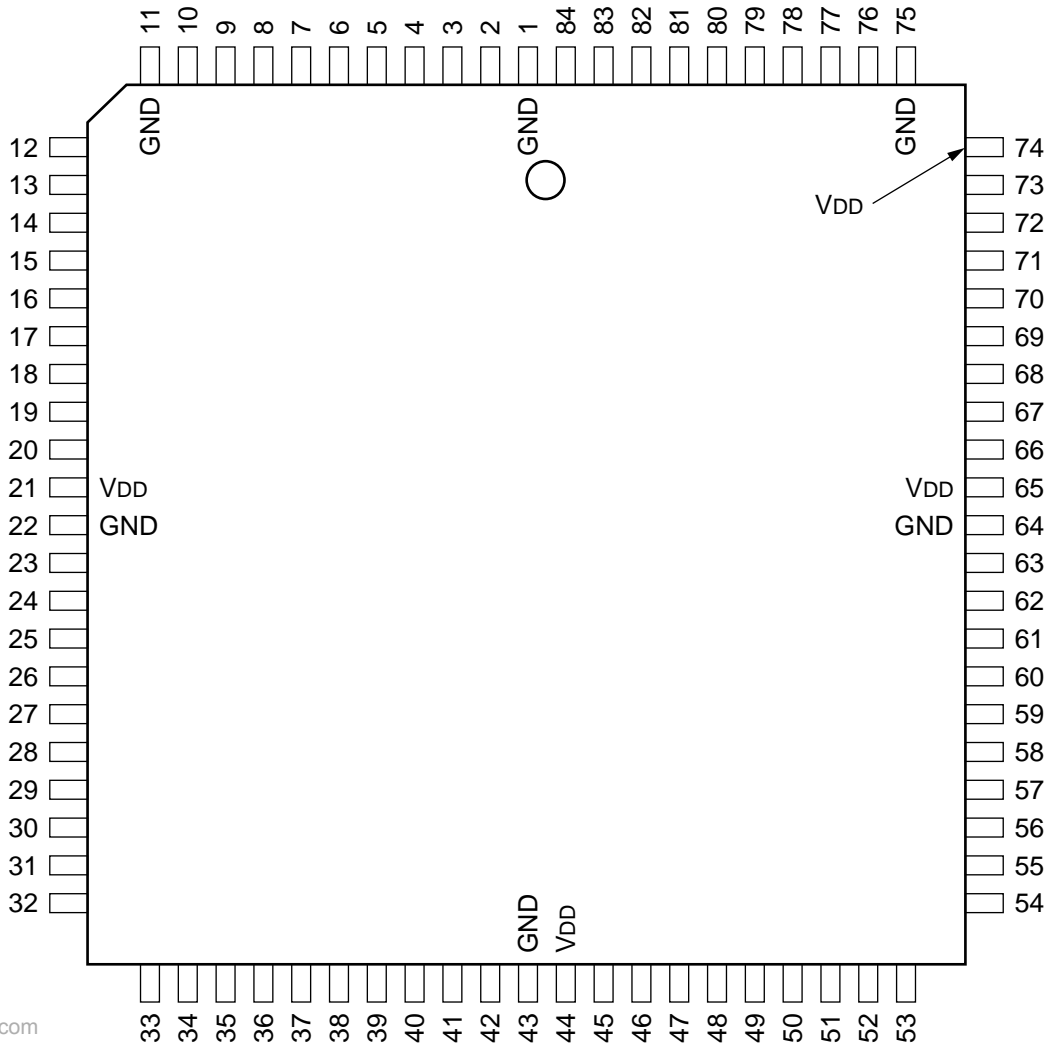


C-MOS (32 × 32 × 1)-BIT DIGITAL CROSSPOINT —TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	GND	22	—	GND	43	—	GND	64	—	GND
2	O	DOUT15	23	I	DIN0	44	—	V _{DD}	65	—	V _{DD}
3	O	DOUT14	24	I	DIN1	45	I	CKX	66	O	DOUT31
4	O	DOUT13	25	I	DIN2	46	I	SADD	67	O	DOUT30
5	O	DOUT12	26	I	DIN3	47	I	CKD	68	O	DOUT29
6	O	DOUT11	27	I	DIN4	48	I	DIO	69	O	DOUT28
7	O	DOUT10	28	I	DIN5	49	I	$\overline{\text{CS}}$	70	O	DOUT27
8	O	DOUT9	29	I	DIN6	50	I	$\overline{\text{RESET}}$	71	O	DOUT26
9	O	DOUT8	30	I	DIN7	51	I	DIN19	72	O	DOUT25
10	O	DOUT7	31	I	DIN8	52	I	DIN20	73	O	DOUT24
11	—	GND	32	I	DIN9	53	I	DIN21	74	—	V _{DD}
12	O	DOUT6	33	I	DIN10	54	I	DIN22	75	—	GND
13	O	DOUT5	34	I	DIN11	55	I	DIN23	76	O	DOUT23
14	O	DOUT4	35	I	DIN12	56	I	DIN24	77	O	DOUT22
15	O	DOUT3	36	I	DIN13	57	I	DIN25	78	O	DOUT21
16	O	DOUT2	37	I	DIN14	58	I	DIN26	79	O	DOUT20
17	O	DOUT1	38	I	DIN15	59	I	DIN27	80	O	DOUT19
18	O	DOUT0	39	I	DIN16	60	I	DIN28	81	O	DOUT18
19	O	PO	40	I	DIN17	61	I	DIN29	82	O	DOUT17
20	I	BLACK	41	I	DIN18	62	I	DIN30	83	O	DOUT16
21	—	V _{DD}	42	I	CLOCK	63	I	DIN31	84	O	DOUT15

INPUT

BLACK : BLACK LEVEL BIT
CKD : SERIAL INTERFACE DATA CLOCK
CKX : SERIAL INTERFACE LATCH CLOCK
CLOCK : DEVICE MASTER CLOCK
 $\overline{\text{CS}}$: SERIAL INTERFACE CHIP SELECT
DIN0 - DIN31 : 32-BIT DATA
DIO : SERIAL INTERFACE DATA
 $\overline{\text{RESET}}$: DEVICE RESET
SADD : SERIAL INTERFACE ADDRESS

OUTPUT

DOUT0 - DOUT31 : 32-BIT DATA
PO : PARAMETRIC OUTPUT FOR DEVICE TEST