

VS-30CPQ1...PbF Series, VS-30CPQ1...-N3 Series

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RoHS

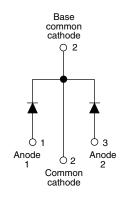
HALOGEN

FREE

Schottky Rectifier, 2 x 15 A



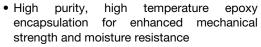
TO-247AC

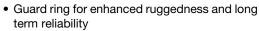


PRODUCT SUMMARY							
TO-247AC							
2 x 15 A							
80 V, 90 V, 100 V							
0.67 V							
7 mA at 125 °C							
175 °C							
Common cathode							
7.5 mJ							

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation





- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

The VS-30CPQ... center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	30	Α						
V_{RRM}		80/100	V						
I _{FSM}	t _p = 5 μs sine	920	Α						
V _F	15 Apk, $T_J = 125$ °C (per leg)	0.67	V						
T _J		- 55 to 175	°C						

VOLTAGE RATINGS										
PARAMETER	SYMBOL	VS- 30CPQ080PbF	VS- 30CPQ080-N3	VS- 30CPQ090PbF	VS- 30CPQ090-N3	VS- 30CPQ100PbF	VS- 30CPQ100-N3	UNITS		
Maximum DC reverse voltage	V _R	00	00	00	00	400	400	.,		
Maximum working peak reverse voltage	V _{RWM}	80	80	80	90	90	100	100	V	

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS				
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 140 °C	30					
Maximum peak one cycle non-repetitive surge current per leg	I	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	920	Α			
See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	240				
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 0.50 A, L = 60 mH		7.50	mJ			
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zer Frequency limited by T _J maxim	0.50	Α				

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
Maximum forward voltage drop per leg See fig. 1		15 A	T _J = 25 °C	0.86				
	V _{FM} ⁽¹⁾	30 A	1j=25 C	1.05	V			
	VFM ('')	15 A	T _{.1} = 125 °C	0.67	V			
		30 A	1j = 125 C	0.81				
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.55	mA			
See fig. 2	IRM (1)	T _J = 125 °C	v _R = nateu v _R	7				
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		500	pF			
Typical series inductance per leg	L _S	Measured lead to lead 5 m	7.5	nΗ				
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBO	L TEST CONDITIONS	VALUES	UNITS					
Maximum junction and storage temperature range	T _J , T _{Stç}		- 55 to 175	°C					
Maximum thermal resistance, junction to case per leg	В	DC operation See fig. 4	2.20						
Maximum thermal resistance, junction to case per package	R _{thJC}	DC operation	1.10	°C/W					
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.24						
Annyayimata waight			6	g					
Approximate weight			0.21	OZ.					
	ninimum	Now to be since to all the sounds	6 (5)	kgf · cm					
Mounting torque m	aximum	Non-lubricated threads	12 (10)	(lbf ⋅ in)					
			30CP	Q080					
Marking device		Case style TO-247AC (JEDEC)	30CP	Q090					
			30CP	Q100					

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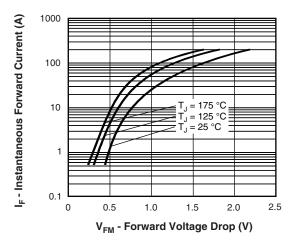


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

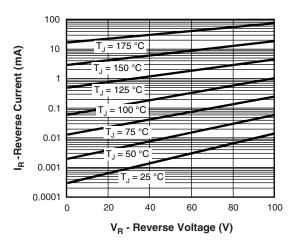


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

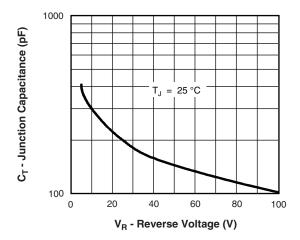


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

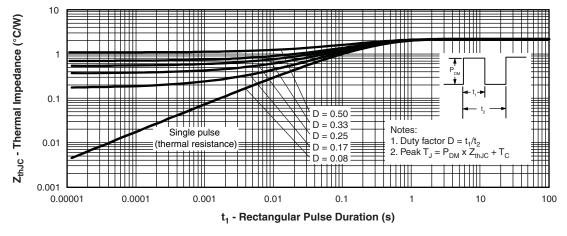


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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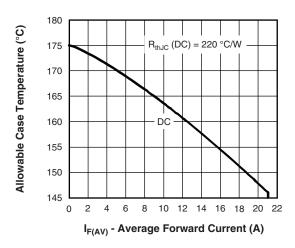


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

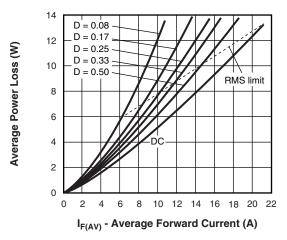


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

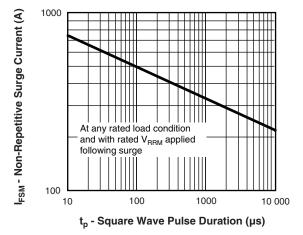


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

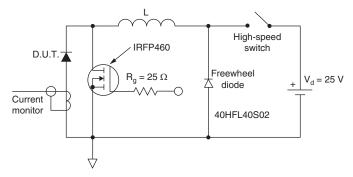


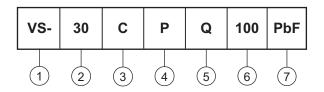
Fig. 8 - Unclamped Inductive Test Circuit

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ORDERING INFORMATION TABLE





Vishay Semiconductors product

2 - Current rating

3 - Circuit configuration:

C = Common cathode

- Package:

P = TO-247

5 - Schottky "Q" series

080 = 80 V 090 = 90 V

6 - Voltage code

100 = 100 V

Environmental digit

PbF = Lead (Pb)-free and RoHS compliant
-N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-30CPQ080PbF	25	500	Antistatic plastic tube						
VS-30CPQ080-N3	25	500	Antistatic plastic tube						
VS-30CPQ090PbF	25	500	Antistatic plastic tube						
VS-30CPQ090-N3	25	500	Antistatic plastic tube						
VS-30CPQ100PbF	25	500	Antistatic plastic tube						
VS-30CPQ100-N3	25	500	Antistatic plastic tube						

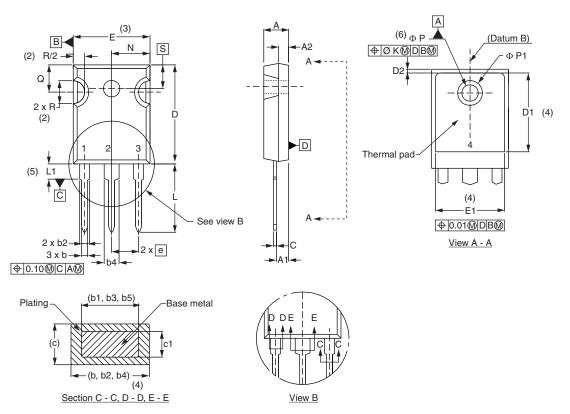
LINKS TO RELATED DOCUMENTS							
Dimensions <u>www.vishay.com/doc?95223</u>							
Part marking information	TO-247AC PbF	www.vishay.com/doc?95226					
	TO-247AC -N3	www.vishay.com/doc?95007					
SPICE model		www.vishay.com/doc?95470					



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TO-247

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209		D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102		Е	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098		E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055		е	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053		ØΚ	2.	54	0.0)10	
b2	1.65	2.39	0.065	0.094		L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092		L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135		Ν	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133		ØΡ	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035		Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033		Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3	R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4	S	5.51	BSC	0.217	BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- $^{(7)}\,$ Outline conforms to JEDEC® outline TO-247 with exception of dimension c



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Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

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