



Features

- XFP MSA Rev 4.5 compliant
- Support multi-rate from 9.95G to 11.1G
- Up to 80km transmission on SMF
- 1550nm EML and APD receiver
- XFI high speed electrical interface
- 2-wire interface with integrated Digital Diagnostic monitoring
- XFP MSA package with duplex LC connector
- +5.0V, +3.3V and +1.8V power supplies
- Power consumption less than 3.5 W
- Operating case temperature: -5~+70°C

Regulatory Compliance

Table 1 - Regulatory Compliance

Feature	Standard	Performance
Electrostatic Discharge	MIL-STD-883E	Class 1(>1000V for SFI
(ESD) to the Electrical Pins	Method 3015.7	pins, >2000V for other pins.)
Electrostatic Discharge (ESD) to the	IEC 61000-4-2	Compatible with standards
Duplex LC Receptacle	GR-1089-CORE	Compatible with standards
	FCC Part 15 Class B	
Electromagnetic	EN55022 Class B (CISPR 22B)	Compatible with standards
Interference (EMI)	VCCI Class B	
Immunity	IEC 61000-4-3	Compatible with standards
Logar Fya Cafaty	FDA 21CFR 1040.10	Compatible with Class I laser
Laser Eye Safety	EN60950, EN (IEC) 60825-1,2	product.
RoHS	2011/65/EU	Compliant with standards



Absolute Maximum Ratings

Table 2 - Absolute Maximum Ratings

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Storage Temperature	Ts	-40	-	+85	°C	
	V _{CC5}	-0.5	-	+6.0	V	
Supply Voltage	V _{CC3}	-0.5	-	+4.0	V	
	V _{CC2}	-0.5		+2.0	V	
Operating Relative Humidity	RH	-	-	+85	%	
Damage Threshold for Receiver	P _{IN-Damage}	-	-	1	dBm	

Recommended Operating Conditions

Table 3 – Recommended Operating Conditions

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Operating Case Temperature	T _C	-5	-	+70	°C	
	V _{CC5}	4.75	5.0	5.25	V	
Power Supply Voltage	V _{CC3}	3.14	3.3	3.46	V	
	V _{CC2}	1.71	1.8	1.89	V	
	I _{CC5}	-	-	500	mA	
Power Supply Current	I _{CC3}	-	-	750	mA	
	I _{CC2}	-	-	1000	mA	
Power Dissipation	P _D	-	-	3.5	W	
Bit Rate	BR	9.95	-	11.1	Gbps	
Transmission Distance	TD	2	-	80,000	m	1

Note 1: Measured with G.652 SMF.

Optical Characteristics

Table 4 – Optical Characteristics

Transmitter						
Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
Center Wavelength Range	λ _C	1530	-	1565	nm	
Average Output Power	P _{out}	0	2	4	dBm	1
Average Output Power (Laser Off)	P _{0UT-OFF}	-	-	-35	dBm	1
Side Mode Suppression Ratio	SMSR	35	-	-	dB	
Spectral Width	Δλ ₂₀	-	-	1	nm	
Extinction Ratio	ER	9	-	-	dB	2



Dispersion Penalty @ 9.95/10.7Gbps	DP₁	_	_	2	dB	3, 4		
Dispersion Penalty @ 11.1Gbps	DP ₂	_	_	3	dB	3, 5		
Jitter Generation (peak-to-peak)	J _{P-P}	-	-	0.1	UI	-, -		
Jitter Generation (RMS)	J _{RMS}	-	-	0.01	UI			
Relative Intensity Noise	RIN	-	-	-130	dB/Hz			
Reflectance Tolerance	RT	-	-	-27	dB			
Optical Eye Mask		Compliant	with ITU-T G.	691-2006	1-2006			
Receiver								
Center Wavelength Range	λ _C	1530	-	1565	nm			
Receiver Sensitivity @ 9.95/10.7Gbps	P _{IN-SENS1}	-	-26	-24	dBm	3		
Receiver Sensitivity @ 11.1Gbps	P _{IN-SENS2}	-	-24	-23	dBm	3		
Receiver Overload	P _{IN-OL}	-7	-	-	dBm	3		
Optical Return Loss	ORL	-	-	-27	dB			
LOS Assert	LOS _A	-34	-	-	dBm			
LOS Deassert	LOS _D	-	-	-24	dBm			
LOS Hysteresis	LOS _H	0.5	-	4	dB			

Notes:

- 1. The optical power is launched into SMF.
- 2. Measured with a PRBS 2³¹-1 test pattern @9.95Gbps.
- 3. Measured with a PRBS 2³¹-1 test pattern, BER≤10⁻¹².
- 4. At 1600 ps/nm.
- 5. At 1450 ps/nm

Electrical Characteristics

Table 5 - Electrical Characteristics

Transmitter						
Parameter Symbol Min. Typ				Max.	Unit	Notes
Differential Data Input Amplitude	$V_{IN,P-P}$	120	-	820	mVpp	
Input Differential Impedance	Z _{IN}	80	100	120	Ω	
Ty Disable D Down/DCT	V _{IL}	-0.3	-	0.8	V	
Tx_Disable, P_Down/RST	V _{IH}	2.0	-	V _{CC} +0.3	V	
		Receiver				
Differential Date Output Amplitude	V _{OUT,P-P}	340	-	850	mVpp	
Output Differential Impedance	Z _d	80	100	120	Ω	
Output Rise Time, 20%~80%	T _R	24	-	-	ps	
Output Fall Time, 20%~80%	T _F	24	-	-	ps	
Dy LOS Mod ND Interrupt	V _{OL}	0	-	0.4	V	
Rx_LOS, Mod_NR, Interrupt	V _{OH}	V _{CC} -0.5	-	V _{CC} +0.3	V	



Recommended Host Board Power Supply Circuit

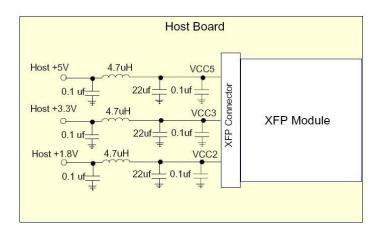


Figure 1, Recommended Host Board Power Supply Circuit

Recommended Interface Circuit

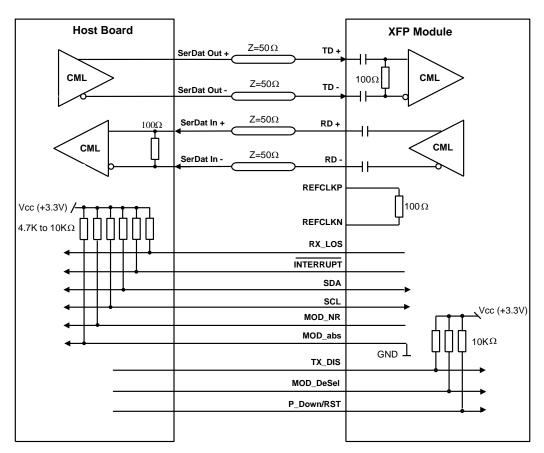


Figure 2, Recommended Interface Circuit



Pin Definitions

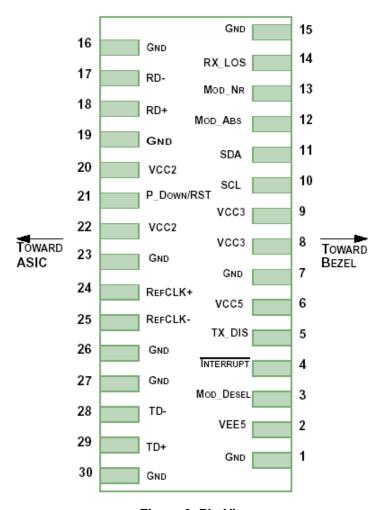


Figure 3, Pin View

Table 6 - Electrical Characteristics

Pin	Logic	Symbol	Name/Description	
1		GND	Module Ground	
2		V _{EE5}	Optional -5.2V Power Supply (Not implemented)	3
3	LVTTL-I	Mod_Desel	Module De-select; When held low allows the module to respond to 2-wire serial interface	
4	LVTTL-O	Interrupt	Interrupt; Indicates presence of an important condition which can be read over the 2-wire serial interface	2
5	LVTTL-I	TX_DIS	Transmitter Disable; Turns off transmitter laser output	
6		V _{CC5}	+5V Power Supply	
7		GND	Module Ground	1
8		V _{CC3}	+3.3V Power Supply	
9		V _{CC3}	+3.3V Power Supply	
10	LVTTL-I/O	SCL	2-Wire Serial Interface Clock	2
11	LVTTL-I/O	SDA	2-Wire Serial Interface Data Line	2



12	LVTTL-O	Mod_Abs	Indicates Module is not present. Grounded in the Module	2
13	LVTTL-O	Mod_NR	Module Not Ready; Indicating Module Operational Fault	2
14	LVTTL-O	RX_LOS	Receiver Loss Of Signal Indicator	2
15		GND	Module Ground	1
16		GND	Module Ground	1
17	CML-O	RD-	Receiver Inverted Data Output	
18	CML-O	RD+	Receiver Non-Inverted Data Output	
19		GND	Module Ground	1
20		V _{CC2}	+1.8V Power Supply	
21	LVTTL-I	P_Down/RST	Power down; When high, requires the module to limit power	
			consumption to 1.5W or below. 2-Wire serial interface must	
			be functional in the low power mode.	
			Reset; The falling edge initiates a complete reset of the	
			module including the2-wire serial interface, equivalent to a	
			power cycle.	
22		V _{CC2}	+1.8V Power Supply	
23		GND	Module Ground	1
24	PECL-I	RefCLK+	Not used, internally terminated to 50ohm (100ohm diff).	4
25	PECL-I	RefCLK-	Not used, internally terminated to 50ohm (100ohm diff).	4
26		GND	Module Ground	1
27		GND	Module Ground	1
28	CML-I	TD-	Transmitter Inverted Data Input	
29	CML-I	TD+	Transmitter Non-Inverted Data Input	
30		GND	Module Ground	1

Notes:

- 1. Module ground pins GND are isolated from the module case and chassis ground within the module.
- 2. Shall be pulled up with 4.7K-10Kohms to a voltage between 3.15V and 3.45V on the host board.
- 3. The pins are open within module.
- 4. Reference Clock is not required



Mechanical Diagram

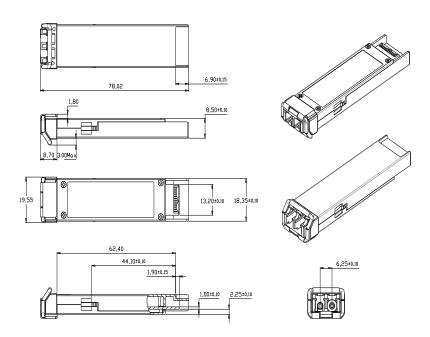


Figure 4, Mechanical Diagram of XFP

Order Information

Table 7 - Order Information

Part No.	Application	Data Rate	Laser Source	Fiber Type	Latch Color
XP-MR-08-CDFB	L-64.2a 10GBASE-ZR	9.95G~11.1G	1550nm EML	SMF	White

Warnings

Handling Precautions: This device is susceptible to damage as a result of electrostatic discharge (ESD). A static free environment is highly recommended. Follow guidelines according to proper ESD procedures. **Laser Safety:** Radiation emitted by laser devices can be dangerous to human eyes. Avoid eye exposure to direct or indirect radiation.

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