TMOS Switching

N-Channel — Enhancement

MAXIMUM RATINGS

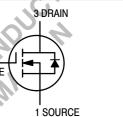
Rating	Symbol	MPF930	MPF960	MPF990	Unit
Drain-Source Voltage	V _{DS}	35	60	90	Vdc
Drain-Gate Voltage	V _{DG}	35	60	90	Vdc
$\begin{array}{l} \mbox{Gate-Source Voltage} \\ \mbox{Continuous} \\ \mbox{Repetitive} \\ \mbox{(}t_p \leq 50 \ \mbox{\mbox{μs$}} \mbox{)} \end{array}$	V _{GS} V _{GSM}		±20 ±40		Vdc Vpk
Drain Current Continuous ⁽¹⁾ Pulsed ⁽²⁾	I _D I _{DM}		2.0 3.0		Adc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D		1.0 8.0		W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}		– 55 to 150		0°
Thermal Resistance	θ_{JA}		125		°C/W



ON Semiconductor®

http://onsemi.com

CASE 29-05, STYLE 22 TO-92 (TO-226AE)



ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	S	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS			·			
Drain-Source Breakdown Voltage (V _{GS} = 0, I _D = 10 μAdc)	MPF930 MPF960 MPF990	V _{(BR)DSX}	35 60 90		 	Vdc
Gate Reverse Current (V_{GS} = 15 Vdc, V_{DS} = 0	0) 5 6	I _{GSS}	—	_	50	nAdc
ON CHARACTERISTICS ⁽²⁾	7×7×7	·				
Zero-Gate-Voltage Drain Current (V _{DS} = Maximum Rating, V _{GS} = 0)	ONEN	I _{DSS}	—	_	10	μAdc
Gate Threshold Voltage (I _D = 1.0 mAdc, V _{DS} = V _{GS})	- City	V _{GS(Th)}	1.0	_	3.5	Vdc
Drain–Source On–Voltage (V _{GS} = 10 Vdc) (I _D = 0.5 Adc)	MPF930 MPF960 MPF990	V _{DS(on)}		0.4 0.6 0.6	0.7 0.8 1.2	Vdc
(I _D = 1.0 Adc)	MPF930 MPF960 MPF990			0.9 1.2 1.2	1.4 1.7 2.4	
(I _D = 2.0 Adc)	MPF930 MPF960 MPF990			2.2 2.8 2.8	3.0 3.5 4.8	

1. The Power Dissipation of the package may result in a lower continuous drain current.

2. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

MPF930

ELECTRICAL CHARACTERISTICS (T _A = 2	25°C unless otherwise noted) (Continued)
--	--

Characteristic		Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS ⁽²⁾ (Continued)		•				
Static Drain–Source On Resistance (V _{GS} = 10 Vdc, I _D = 1.0 Adc)	MPF930 MPF960 MPF990	r _{DS(on)}		0.9 1.2 1.2	1.4 1.7 2.0	Ω
On-State Drain Current (V _{DS} = 25 Vdc, V _{GS} = 10 Vdc)		I _{D(on)}	1.0	2.0		Amps
SMALL-SIGNAL CHARACTERISTICS		•	L			
Input Capacitance (V _{DS} = 25 Vdc, V _{GS} = 0, f = 1.0 MHz)		C _{iss}	—	70		pF
Reverse Transfer Capacitance $(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ MHz})$		C _{rss}	-	20		pF
Output Capacitance $(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ MHz})$		C _{oss}	-	49	6	pF
Forward Transconductance $(V_{DS} = 25 \text{ Vdc}, I_D = 0.5 \text{ Adc})$		g _{fs}	200	380)	mmhos
SWITCHING CHARACTERISTICS				5		
Turn-On Time		t _{on}		7.0	15	ns
Turn–Off Time		t _{off}	5	7.0	15	ns
2. Pulse Test: Pulse Width \leq 300 µs, Duty Cyc	le ≤ 2.0%. RESISTIVE SWITC	t _{on} t _{off}	FORM			

RESISTIVE SWITCHING

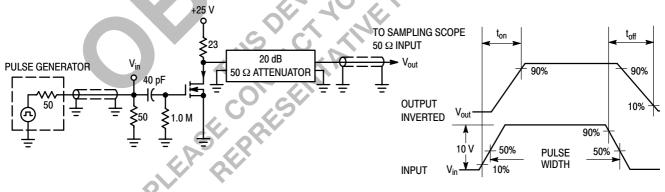
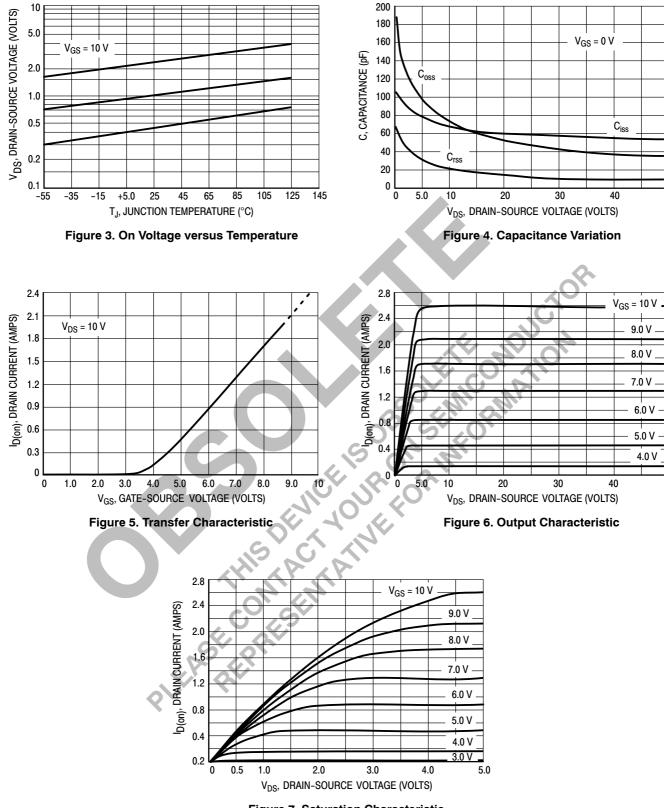


Figure 1. Switching Test Circuit

Figure 2. Switching Waveforms

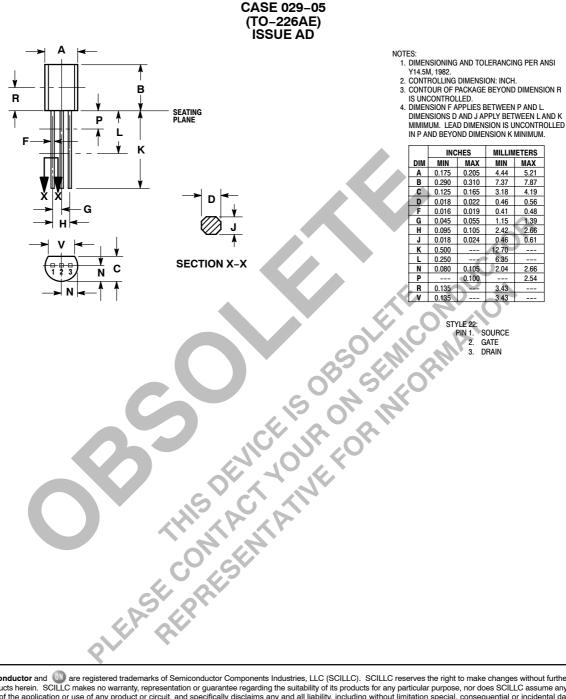
MPF930





MPF930

PACKAGE DIMENSIONS



ON Semiconductor and use registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death agsociated with such unintended or unauthorized use patent solut. Cwas negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunit//Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada Fax: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81–3–5773–3850 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative