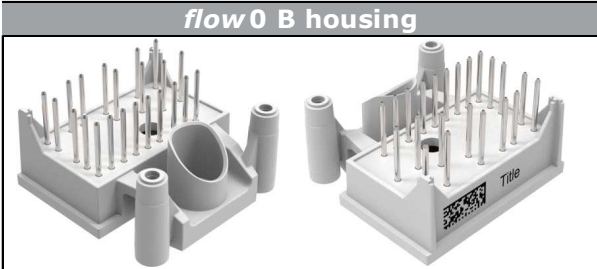
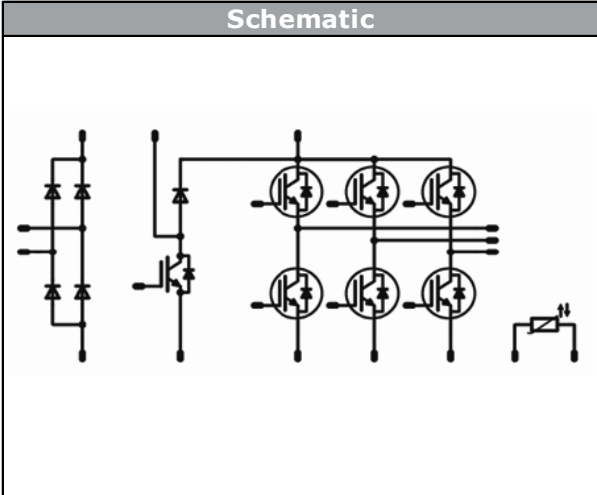




Vincotech

<i>flow</i> PIM 0B + PFC	600 V / 10 A
<div style="background-color: #cccccc; text-align: center; padding: 2px;">Features</div> <ul style="list-style-type: none"> Converter, PFC, inverter in one housing New high speed IGBT for PFC One screw heatsink mounting 	<div style="background-color: #cccccc; text-align: center; padding: 2px;"><i>flow</i> 0 B housing</div> 
<div style="background-color: #cccccc; text-align: center; padding: 2px;">Target applications</div> <ul style="list-style-type: none"> Embedded drives 	<div style="background-color: #cccccc; text-align: center; padding: 2px;">Schematic</div> 
<div style="background-color: #cccccc; text-align: center; padding: 2px;">Types</div> <ul style="list-style-type: none"> 10-0B06PPA010RC-L025A09 	

Maximum Ratings

$T_j=25^{\circ}\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Inverter Switch				
Collector-emitter break down voltage	V_{CES}		600	V
DC collector current	I_C	$T_j=T_{jmax}$ $T_h=80^{\circ}\text{C}$	15	A
Pulsed collector current	I_{Cpulse}	t_p limited by T_{jmax}	30	A
Turn off safe operating area		$T_j \leq 150^{\circ}\text{C}$, $V_{CE} \leq 600\text{V}$	20	A
Power dissipation	P_{tot}	$T_j=T_{jmax}$ $T_h=80^{\circ}\text{C}$	44	W
Gate-emitter peak voltage	V_{GE}		± 20	V
Short circuit ratings	t_{SC} V_{CC}	$T_j \leq 150^{\circ}\text{C}$ $V_{GE} = 15\text{V}$	5 400	μs V
Maximum Junction Temperature	T_{jmax}		175	$^{\circ}\text{C}$



Vincotech

Parameter	Symbol	Condition	Value	Unit
PFC Switch				
Collector-emitter break down voltage	V_{CES}		650	V
DC collector current	I_C	$T_J = T_{Jmax}$ $T_h = 80^\circ C$	27	A
Pulsed collector current	I_{Cpulse}	t_p limited by T_{Jmax}	90	A
Turn off safe operating area		$T_J \leq 150^\circ C$, $V_{CE} \leq 650V$	90	A
Power dissipation	P_{tot}	$T_J = T_{Jmax}$ $T_h = 80^\circ C$	55	W
Gate-emitter peak voltage	V_{GE}		± 20	V
Maximum Junction Temperature	T_{Jmax}		175	$^\circ C$

Parameter	Symbol	Conditions	Value	Unit
PFC Diode				
Peak Repetitive Reverse Voltage	V_{RRM}		650	V
DC forward current	I_F	$T_J = T_{Jmax}$ $T_h = 80^\circ C$	34	A
Repetitive peak forward current	I_{FRM}		60	A
Power dissipation	P_{tot}	$T_J = T_{Jmax}$ $T_h = 80^\circ C$	45	W
Maximum Junction Temperature	T_{Jmax}		175	$^\circ C$

Parameter	Symbol	Conditions	Value	Unit
PFC Switch Protection Diode				
Peak Repetitive Reverse Voltage	V_{RRM}		650	V
DC forward current	I_F	$T_J = T_{Jmax}$ $T_h = 80^\circ C$	12	A
Repetitive peak forward current	I_{FRM}		12	A
Power dissipation	P_{tot}	$T_J = T_{Jmax}$ $T_h = 80^\circ C$	32	W
Maximum Junction Temperature	T_{Jmax}		175	$^\circ C$

Parameter	Symbol	Conditions	Value	Unit
Rectifier Diode				
Peak Repetitive Reverse Voltage	V_{RRM}		1600	V
DC forward current	I_{FAV}	$T_J = T_{Jmax}$ $T_h = 80^\circ C$	13	A
Non-repetitive peak surge current	I_{FSM}	60Hz Single Half Sine Wave	150	A
Power dissipation	P_{tot}	$T_J = T_{Jmax}$ $T_h = 80^\circ C$	34	W
Maximum Junction Temperature	T_{Jmax}		150	$^\circ C$



Vincotech

Characteristic Values

Inverter Switch

Parameter	Symbol	Conditions				Value			Unit	
		V_{GE} [V]	V_{CE} [V]	I_C [A]	T_j [°C]	Min	Typ	Max		
Static										
Gate emitter threshold voltage	$V_{GE(th)}$	$V_{GE}=V_{CE}$			0,00017	25 125	4,4	5	5,6	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		10	25 125 150	1,68	2,19 2,28 2,30	2,42	V
Collector-emitter cut-off	I_{CES}		0	600		25 125			2	μ A
Gate-emitter leakage current	I_{GES}		20	0		25 125			120	nA
Integrated Gate resistor	R_{gint}							none		Ω
Input capacitance	C_{ies}							655		pF
Output capacitance	C_{oss}	f=1 MHz	0	25		25		37		
Reverse transfer capacitance	C_{rss}							22		
Gate charge	Q_{Gate}		15	480	10	25		64		nC

Thermal

Thermal resistance chip to heatsink	R_{thJH}	Phase-Change Material $\lambda=3,4W/mK$						2,15		K/W
-------------------------------------	------------	---	--	--	--	--	--	------	--	-----

IGBT Switching

Turn-on delay time	$t_{d(on)}$	$R_{goff}=32\Omega$ $R_{gon}=32\Omega$	± 15	400	10	25		74		ns
Rise time	t_r					125		71		
Turn-off delay time	$t_{d(off)}$					25		18		
Fall time	t_f					125		22		
Turn-on energy loss per pulse	E_{on}	$Q_{rrFWD}=0,5\mu C$ $Q_{rrFWD}=0,9\mu C$				25		0,244		mWs
Turn-off energy loss per pulse	E_{off}					125		0,367		
						25		0,122		
						125		0,181		

FWD Switching

Peak recovery current	I_{RRM}	$di/dt=452A/\mu s$ $di/dt=483A/\mu s$	± 15	400	10	25		7		A
Reverse recovery time	t_{rr}					125		10		
Reverse recovery charge	Q_{rr}					25		174		
Reverse recovered energy	E_{rec}					125		233		
Peak rate of fall of recovery current	$di(rec)_{max}/dt$					25		0,451		mWs
						125		0,893		
						25		0,121		
						125		0,243		
						25		93		A/ μs
						125		83		



Vincotech

PFC Switch

Parameter	Symbol	Conditions				Value			Unit	
		V_{GE} [V]	V_{CE} [V]	I_C [A]	T_j [°C]	Min	Typ	Max		
Static										
Gate emitter threshold voltage	$V_{GE(th)}$	$V_{GE}=V_{CE}$			0,0003	25 125	3,3	4	4,7	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		30	25 125 150	-	1,69 1,92	2,22	V
Collector-emitter cut-off	I_{CES}		0	650		25 125			40	μ A
Gate-emitter leakage current	I_{GES}		20	0		25 125			120	nA
Integrated Gate resistor	R_{gint}							none		Ω
Input capacitance	C_{ies}							1800		pF
Output capacitance	C_{oss}	f=1MHz	0	25		25		45		
Reverse transfer capacitance	C_{riss}							7		
Gate charge	Q_{Gate}		15	520	30	25		70		nC

Thermal

Thermal resistance chip to heatsink per chip	R_{thJH}	Phase-Change Material $\lambda=3,4W/mK$						1,74		K/W
--	------------	--	--	--	--	--	--	------	--	-----

IGBT Switching

Turn-on delay time	$t_{d(on)}$	$R_{goff}=16\Omega$ $R_{gon}=16\Omega$	15/0	400	10	25		21		ns
Rise time	t_r					125		19		
Turn-off delay time	$t_{d(off)}$					25		5		
Fall time	t_f					125		7		
Turn-on energy loss per pulse	E_{on}	$Q_{rrFWD}=0,4\mu C$ $Q_{rrFWD}=0,9\mu C$				25		0,238		mWs
Turn-off energy loss per pulse	E_{off}					125		0,380		
						25		0,048		
						125		0,091		



Vincotech

PFC Diode

Parameter	Symbol	Conditions					Value			Unit
		di_F/dt [A/us]	V_r [V]	I_F [A]	T_j	Min	Typ	Max		
Static										
Forward voltage	V_F			30	25°C 125°C 150°C		1,43 1,34 1,31	2,22		V
Reverse leakage current	I_{rm}		650		25°C 150°C			1,6 -		μA

Thermal

Thermal resistance chip to heatsink per chip	R_{thJH}	Phase-Change Material $\lambda = 3,4$ W/mK					2,09			K/W
--	------------	--	--	--	--	--	------	--	--	-----

FWD Switching

Peak recovery current	I_{RRM}	$di/dt=538A/\mu s$ $di/dt=651A/\mu s$	15/0	400	10	25		15		A
Reverse recovery time	t_{rr}					125		22		
						25		44		ns
Reverse recovery charge	Q_{rr}					125		66		
						25		0,447		μC
Reverse recovered energy	E_{rec}					125		0,926		
		25		0,079		mWs				
Peak rate of fall of recovery current	$di(rec)_{max}/dt$	125		1996						
		25		1635		A/μs				

PFC Protection Diode

Parameter	Symbol	Conditions					Value			Unit
		di_F/dt [A/us]	V_r [V]	I_F [A]	T_j	Min	Typ	Max		
Static										
Forward voltage	V_F			6	25°C 125°C 150°C		1,73 1,59 1,54	1,87		V
Reverse leakage current	I_{rm}		650		25°C 150°C			0,1 -		μA

Thermal

Thermal resistance chip to heatsink	R_{thJH}	Phase-Change Material $\lambda=3,4W/mK$					3,01			K/W
-------------------------------------	------------	--	--	--	--	--	------	--	--	-----



Vincotech

Rectifier Diode

Parameter	Symbol	Conditions					Value			Unit
		di_F/dt [A/us]	V_r [V]	I_F [A]	T_j	Min	Typ	Max		
Static										
Forward voltage	V_F			7	25°C 125°C 150°C		1,04 0,97 -	1,14		V
Reverse leakage current	I_r		1600		25°C 150°C			20 -		μA
Thermal										
Thermal resistance chip to heatsink per chip	R_{thJH}	Phase-Change Material $\lambda=3,4W/mK$					2,09			K/W

Thermistor

Parameter	Symbol	Conditions					Value			Unit
		V_{GE} [V]	V_{CE} [V]	I_C [A]	T_j [°C]	Min	Typ	Max		
Rated resistance	R				25		21,5			kΩ
Deviation of R100	$\Delta R/R$	R100=1486 Ω			100	-4,5		+4,5		%
Power dissipation	P				25		210			mW
Power dissipation constant					25		3,5			mW/K
B-value	B(25/50)				25		3884			K
B-value	B(25/100)				25		3964			K
Vincotech NTC Reference								F		

Parameter	Symbol	Conditions	Value	Unit
-----------	--------	------------	-------	------

Module Properties

Thermal Properties

Storage temperature	T_{stg}		-40...+125	°C
Operation temperature under switching condition	T_{op}		-40...+(T_{jmax} - 25)	°C

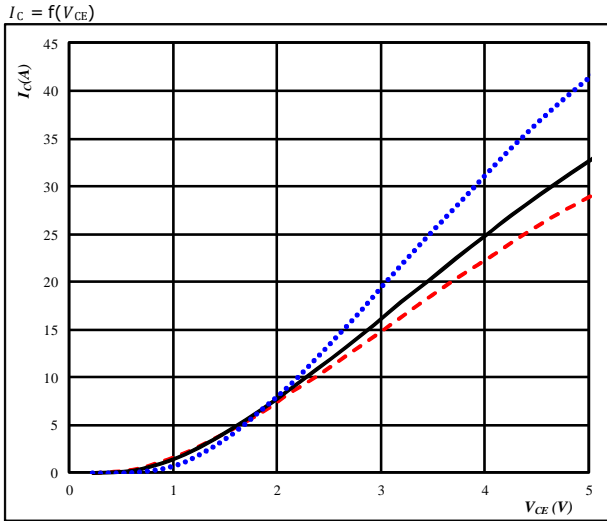
Insulation Properties

Insulation voltage	V_{is}	DC voltage	t=2s	4000	V
Creepage distance				min 12,7	mm
Clearance				min 12,7	mm
Comparative tracking index	CTI			>200	



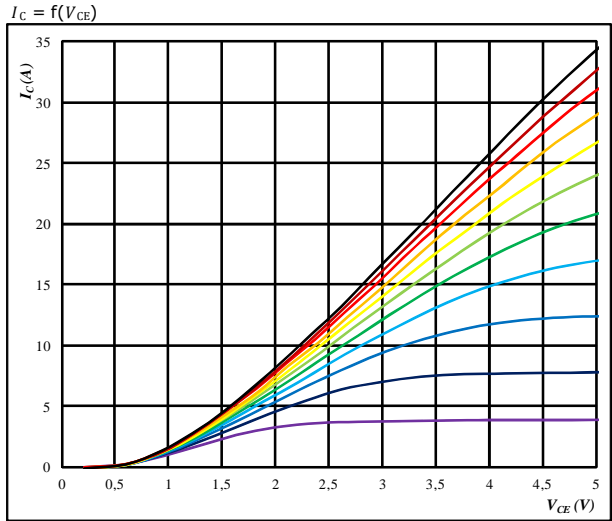
Inverter Switch Characteristics

Typical output characteristics IGBT



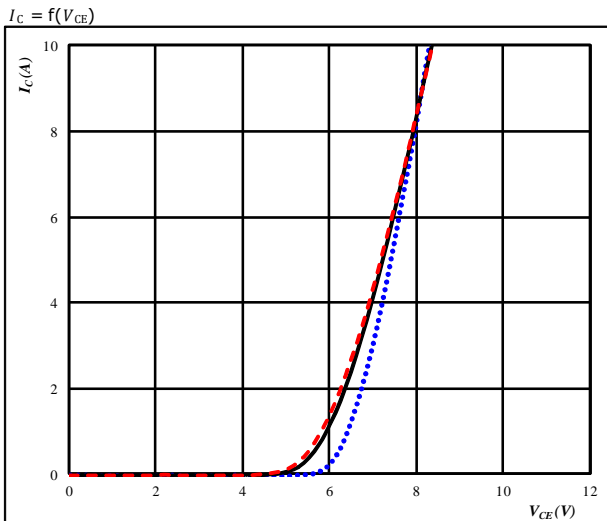
$t_p = 250 \mu s$
 $V_{GE} = 15 V$
 $T_j: 25 \text{ }^\circ C$ (dotted blue)
 $125 \text{ }^\circ C$ (solid black)
 $150 \text{ }^\circ C$ (dashed red)

Typical output characteristics IGBT



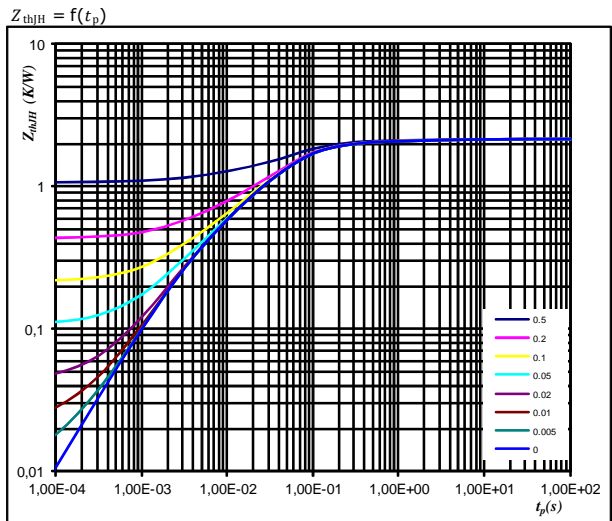
$t_p = 250 \mu s$
 $T_j = 150 \text{ }^\circ C$
 V_{GE} from 7 V to 17 V in steps of 1 V

Typical transfer characteristics IGBT



$t_p = 100 \mu s$
 $V_{CE} = 10 V$
 $T_j: 25 \text{ }^\circ C$ (dotted blue)
 $125 \text{ }^\circ C$ (solid black)
 $150 \text{ }^\circ C$ (dashed red)

Transient thermal impedance as a function of pulse width IGBT



$D = t_p / T$
 $R_{thjH} = 2,15 K/W$

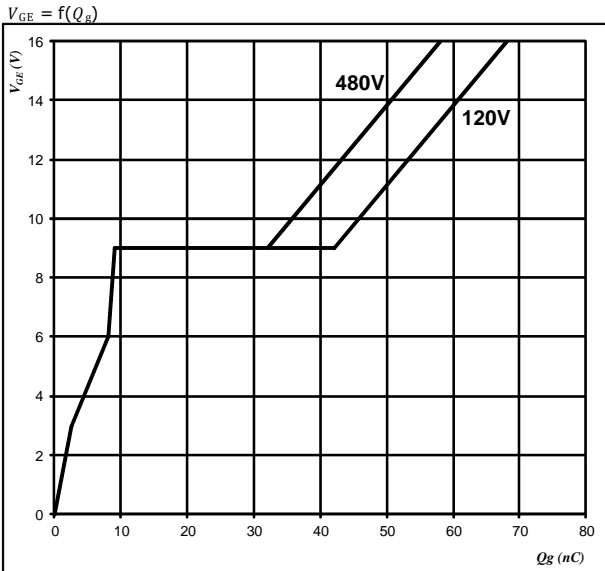
IGBT thermal model values

R (K/W)	Tau (s)
7,60E-02	2,82E+00
1,59E-01	4,19E-01
1,01E+00	6,63E-02
6,48E-01	2,63E-02
2,57E-01	3,72E-03



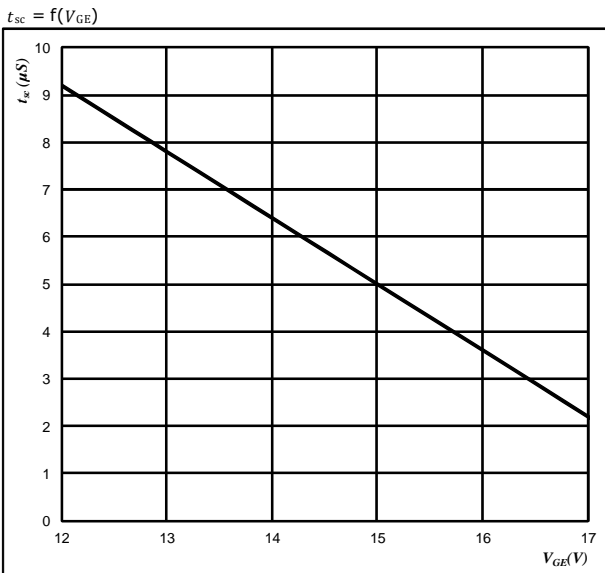
Inverter Switch Characteristics

Gate voltage vs Gate charge IGBT



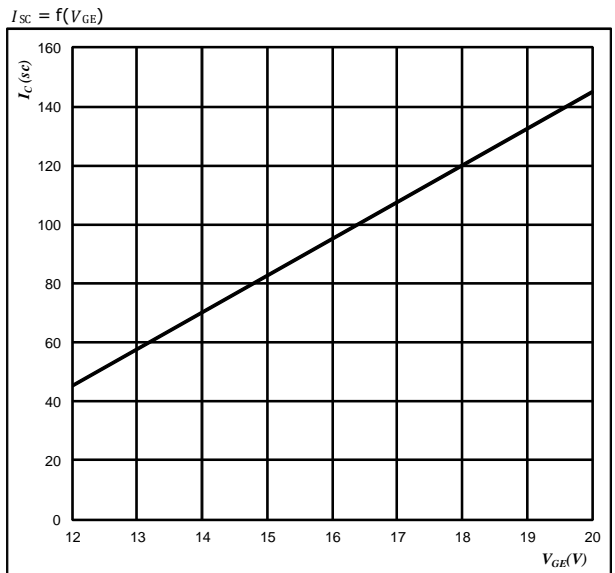
At
I_C = 10 A

Short circuit withstand time as a function of V_{GE} IGBT



At
V_{CE} = 400 V
T_J ≤ 150 °C

Typical short circuit collector current as a function of V_{GE} IGBT

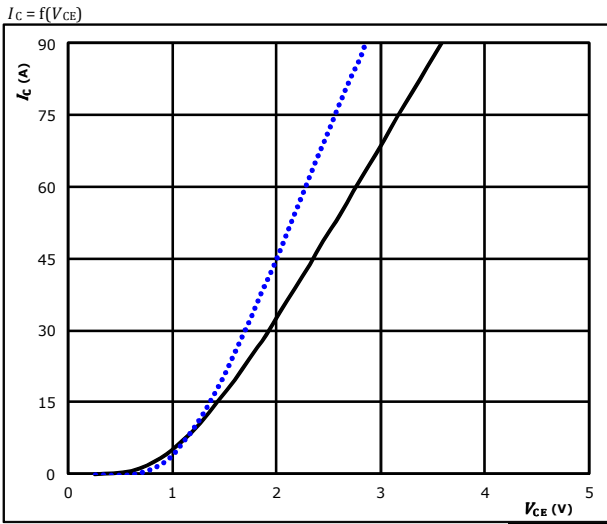


At
V_{CE} ≤ 400 V
T_J ≤ 25 °C



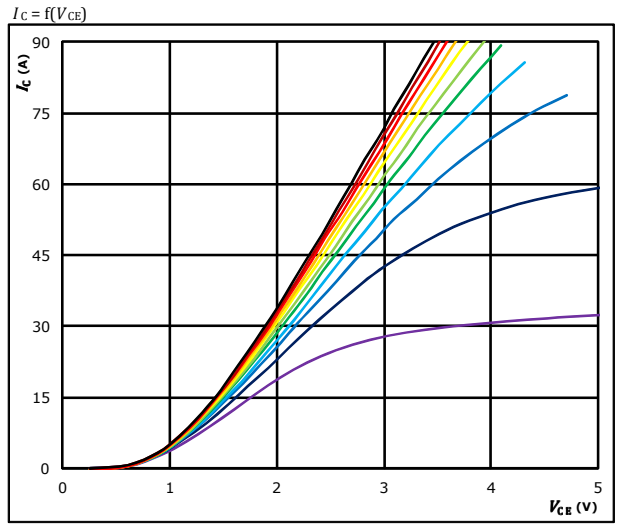
PFC Switch Characteristics

Typical output characteristics IGBT



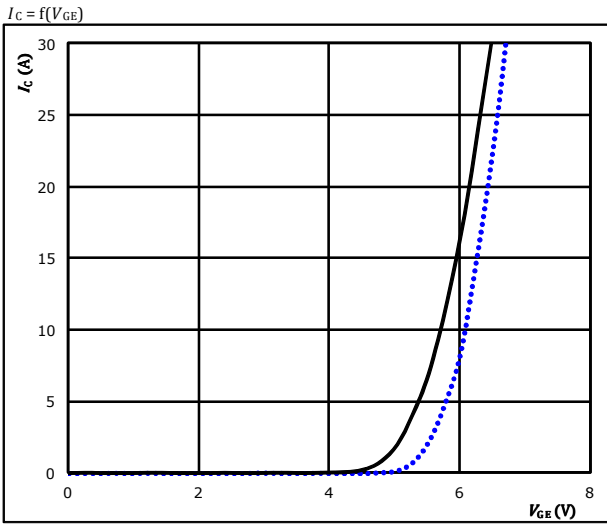
$t_p = 250 \mu s$
 $V_{GE} = 15 V$
 25 °C
 125 °C ———
 150 °C - - - -

Typical output characteristics IGBT



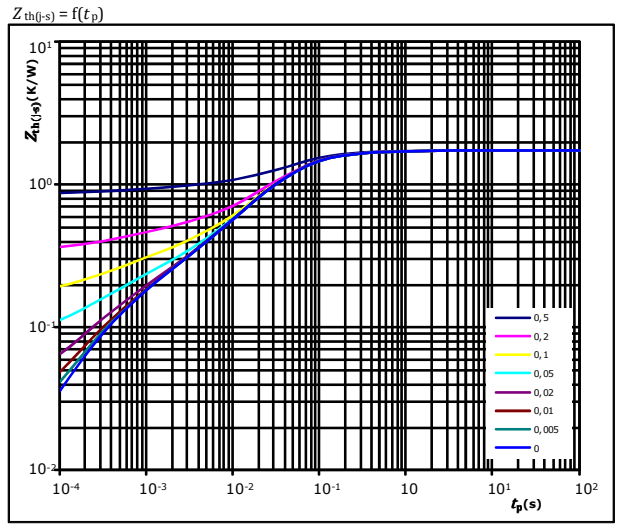
$t_p = 250 \mu s$
 $T_j = 125 \text{ } ^\circ C$
 V_{GE} from 7 V to 17 V in steps of 1 V

Typical transfer characteristics IGBT



$t_p = 100 \mu s$
 $V_{CE} = 10 V$
 25 °C
 125 °C ———
 150 °C - - - -

Transient Thermal Impedance as function of Pulse duration IGBT

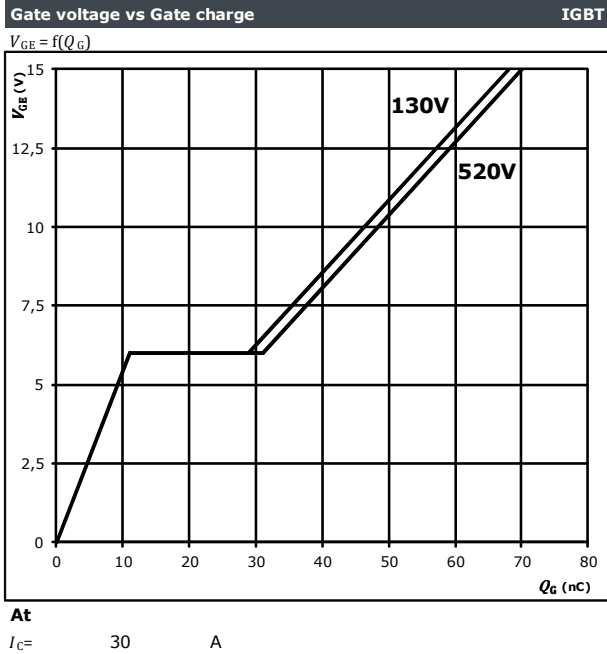


$D = t_p / T$
 $R_{th(j-s)} = 1,74 K/W$
 IGBT thermal model values

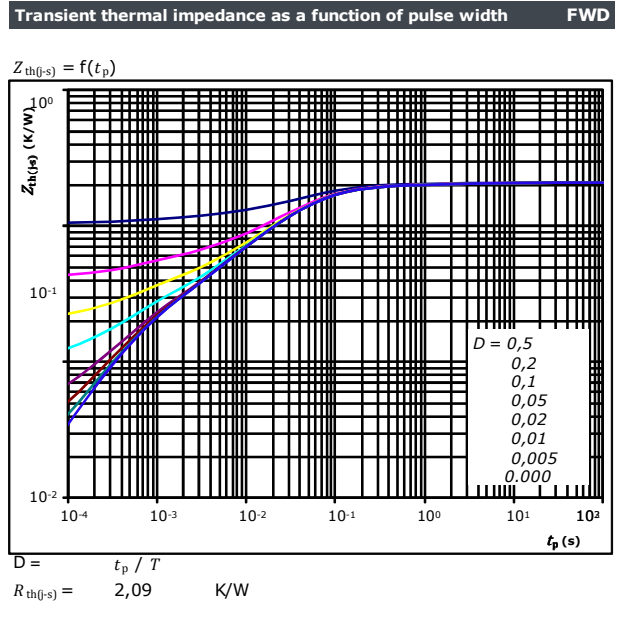
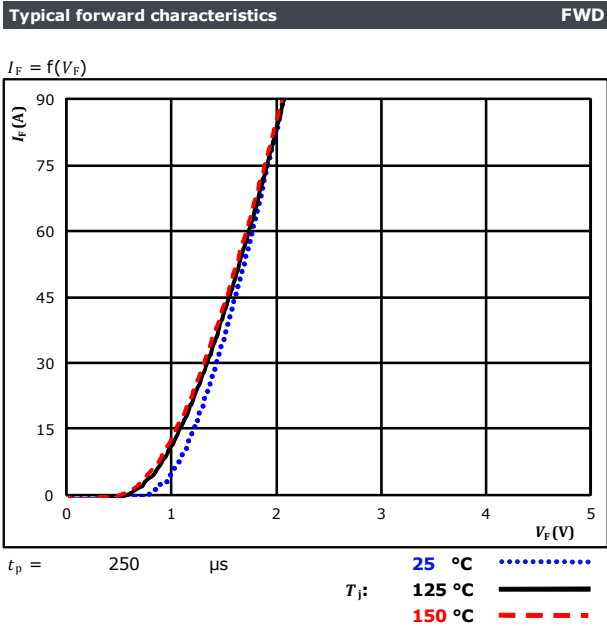
$R_{th} (K/W)$	$\tau (s)$
1,29E-01	5,83E-01
7,29E-01	6,38E-02
6,55E-01	2,28E-02
1,29E-01	2,24E-03
9,92E-02	3,38E-04



PFC Switch Characteristics



PFC Diode Characteristics



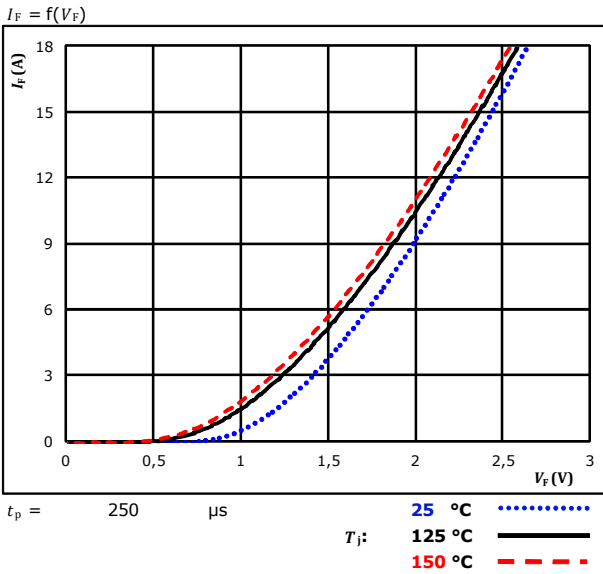
FWD thermal model values

R (K/W)	τ (s)
4,06E-02	7,59E+00
1,41E-01	7,59E-01
6,53E-01	8,62E-02
8,80E-01	2,66E-02
2,25E-01	4,54E-03
1,55E-01	5,55E-04

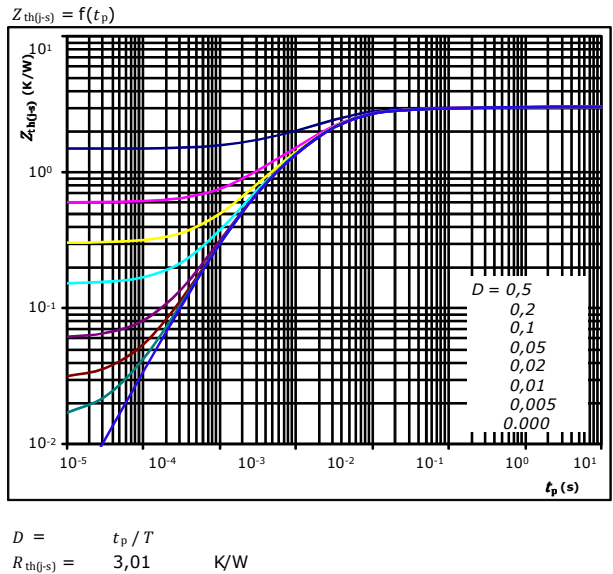


PFC Protection Diode characteristics

Typical forward characteristics FWD



Transient thermal impedance as a function of pulse width FWD



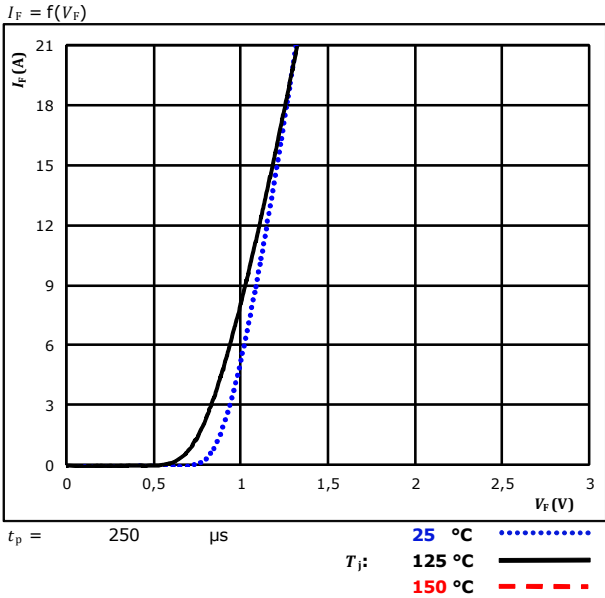
FWD thermal model values

R (K/W)	τ (s)
5,15E-02	9,38E+00
9,53E-02	8,91E-01
3,22E-01	1,25E-01
1,35E+00	2,97E-02
8,32E-01	8,19E-03
3,58E-01	1,78E-03

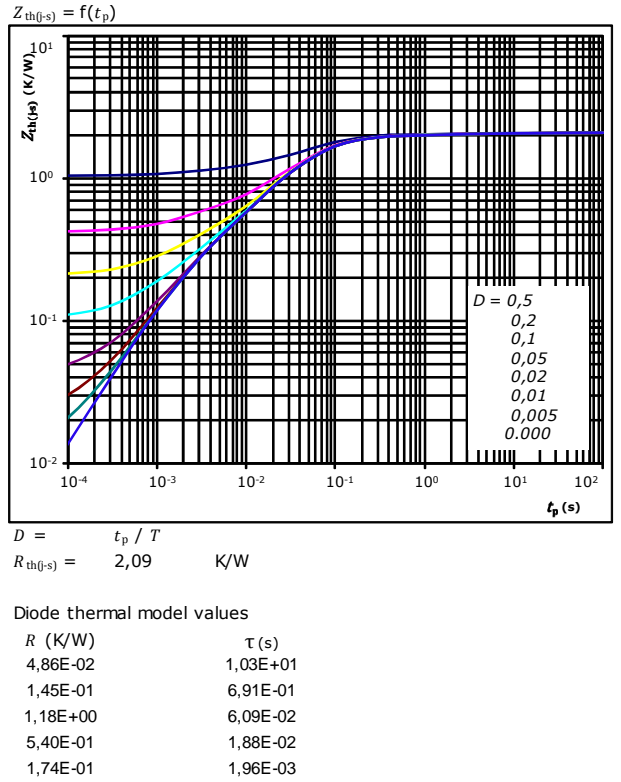


Rectifier characteristics

Typical forward characteristics Rectifier Diode

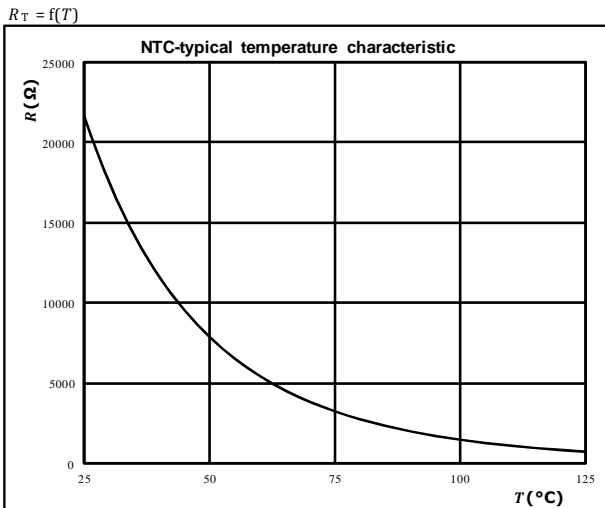


Transient thermal impedance as a function of pulse width Rectifier Diode



Thermistor Characteristics

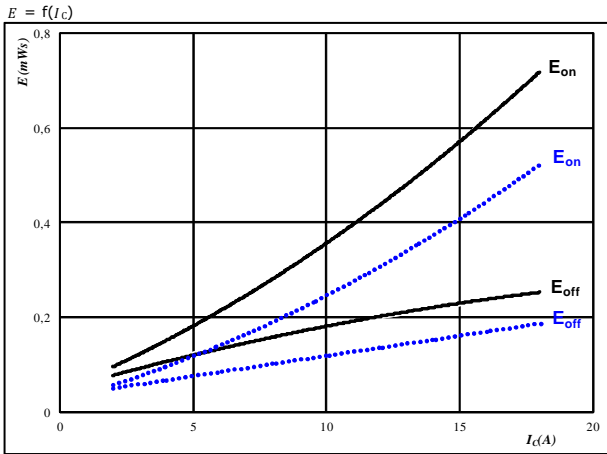
Thermistor typical temperature characteristic
Typical NTC characteristic
as a function of temperature





Inverter Switching Definitions

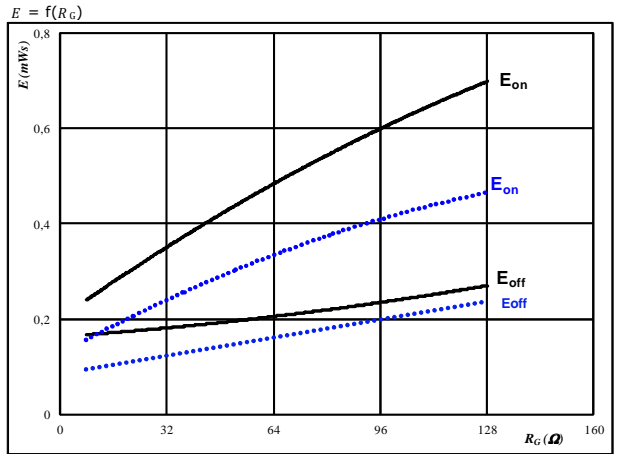
Figure 1. IGBT
Typical switching energy losses as a function of collector current



With an inductive load at

$V_{CE} = 400$ V	$T_j: 25$ °C
$V_{GE} = \pm 15$ V	125 °C	————
$R_{g\text{on}} = 32$ Ω	150 °C	----
$R_{g\text{off}} = 32$ Ω		

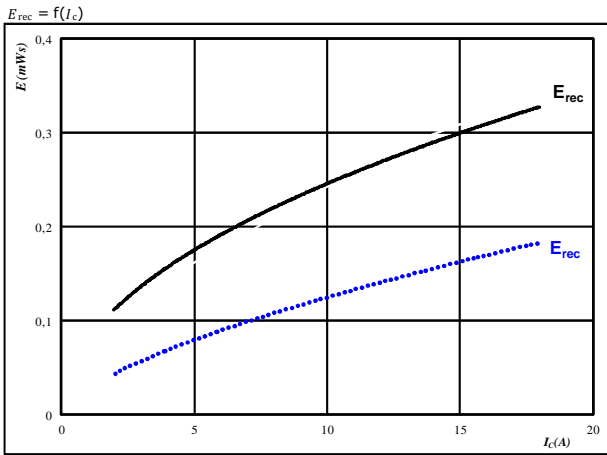
Figure 2. IGBT
Typical switching energy losses as a function of gate resistor



With an inductive load at

$V_{CE} = 400$ V	$T_j: 25$ °C
$V_{GE} = \pm 15$ V	125 °C	————
$I_C = 10$ A	150 °C	----

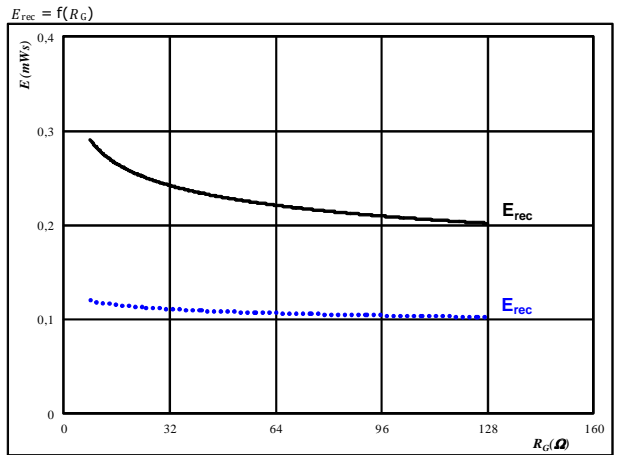
Figure 3. FWD
Typical reverse recovery energy loss as a function of collector current



With an inductive load at

$V_{CE} = 400$ V	$T_j: 25$ °C
$V_{GE} = \pm 15$ V	125 °C	————
$R_{g\text{on}} = 32$ Ω	150 °C	----

Figure 4. FWD
Typical reverse recovery energy loss as a function of gate resistor



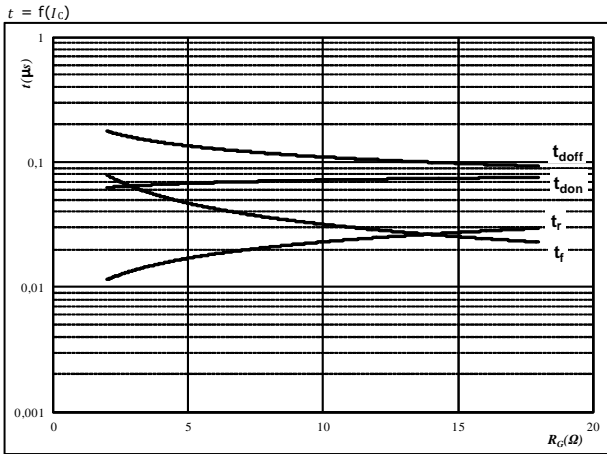
With an inductive load at

$V_{CE} = 400$ V	$T_j: 25$ °C
$V_{GE} = \pm 15$ V	125 °C	————
$I_C = 10$ A	150 °C	----



Inverter Switching Definitions

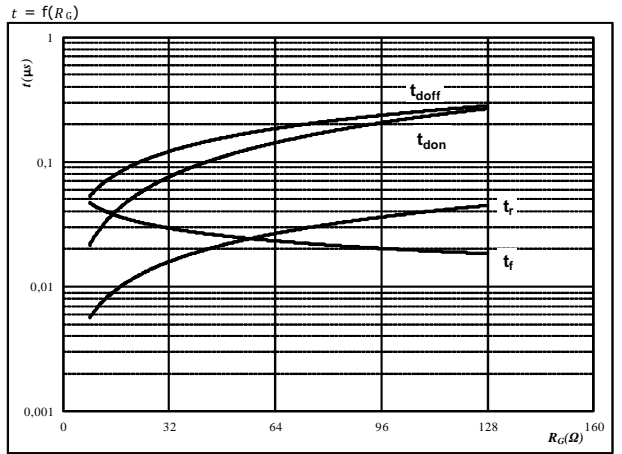
Figure 5. IGBT
Typical switching times as a function of collector current



With an inductive load at

$T_j = 125 \text{ }^\circ\text{C}$
 $V_{CE} = 400 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{g(on)} = 32 \text{ } \Omega$
 $R_{g(off)} = 32 \text{ } \Omega$

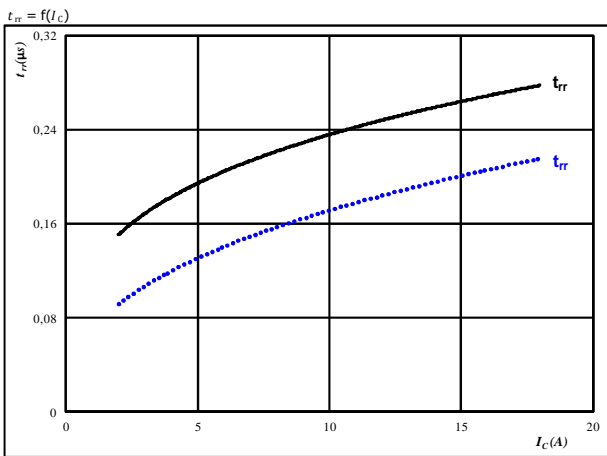
Figure 6. IGBT
Typical switching times as a function of gate resistor



With an inductive load at

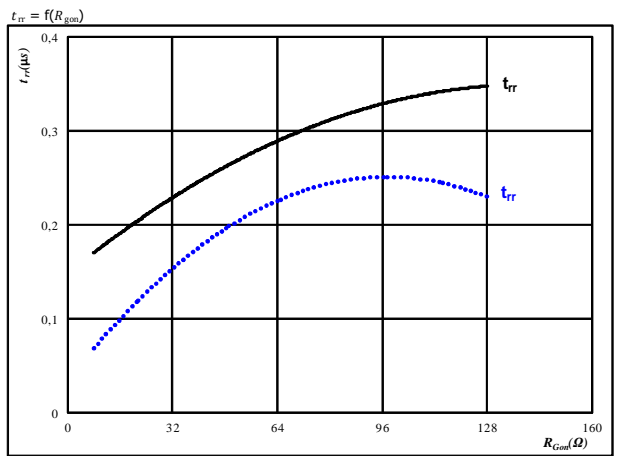
$T_j = 125 \text{ }^\circ\text{C}$
 $V_{CE} = 400 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_C = 10 \text{ A}$

Figure 7. FWD
Typical reverse recovery time as a function of collector current



At $V_{CE} = 400 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $R_{g(on)} = 32 \text{ } \Omega$
 $T_j: 25 \text{ }^\circ\text{C}$ (dotted blue)
 $125 \text{ }^\circ\text{C}$ (solid black)
 $150 \text{ }^\circ\text{C}$ (dashed red)

Figure 8. FWD
Typical reverse recovery time as a function of IGBT turn on gate resistor

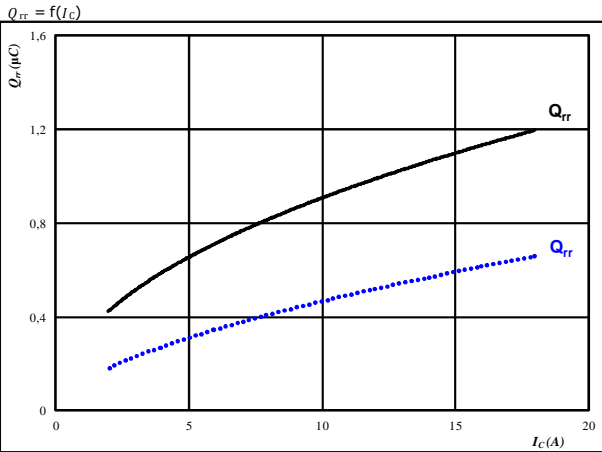


At $V_{CE} = 400 \text{ V}$
 $V_{GE} = \pm 15 \text{ V}$
 $I_C = 10 \text{ A}$
 $T_j: 25 \text{ }^\circ\text{C}$ (dotted blue)
 $125 \text{ }^\circ\text{C}$ (solid black)
 $150 \text{ }^\circ\text{C}$ (dashed red)



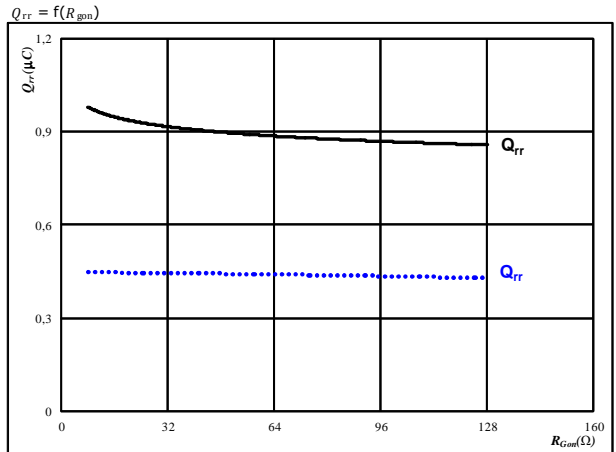
Inverter Switching Definitions

Figure 9. FWD
Typical reverse recovery charge as a function of collector current



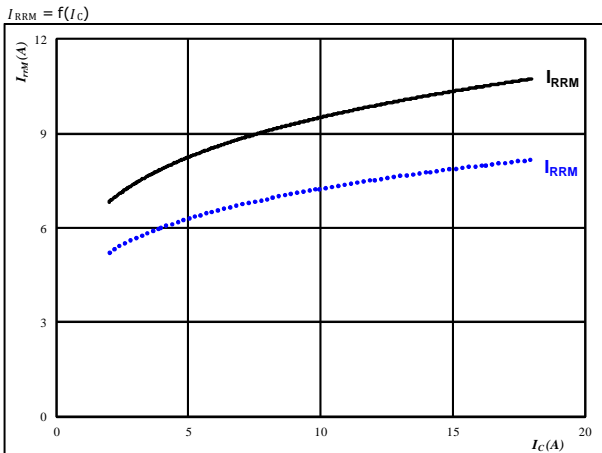
At $V_{CE} = 400$ V $T_j: 25$ °C
 $V_{GE} = \pm 15$ V $T_j: 125$ °C ———
 $R_{gdn} = 32$ Ω $T_j: 150$ °C - - - -

Figure 10. FWD
Typical reverse recovery charge as a function of IGBT turn on gate resistor



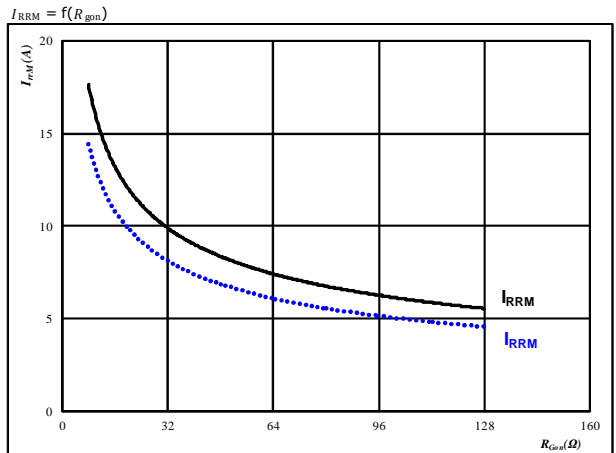
At $V_{CE} = 400$ V $T_j: 25$ °C
 $V_{GE} = \pm 15$ V $T_j: 125$ °C ———
 $I_C = 10$ A $T_j: 150$ °C - - - -

Figure 11. FWD
Typical reverse recovery current as a function of collector current



At $V_{CE} = 400$ V $T_j: 25$ °C
 $V_{GE} = \pm 15$ V $T_j: 125$ °C ———
 $R_{gdn} = 32$ Ω $T_j: 150$ °C - - - -

Figure 12. FWD
Typical reverse recovery current as a function of IGBT turn on gate resistor



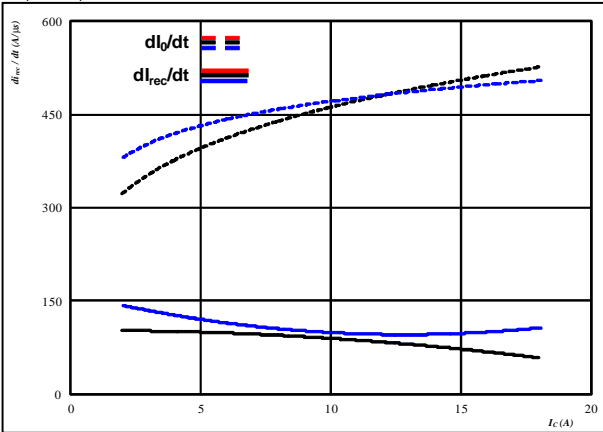
At $V_{CE} = 400$ V $T_j: 25$ °C
 $V_{GE} = \pm 15$ V $T_j: 125$ °C ———
 $I_C = 10$ A $T_j: 150$ °C - - - -



Inverter Switching Definitions

Figure 13. FWD

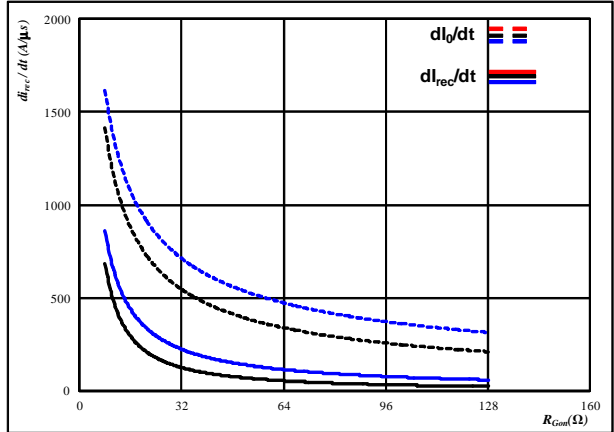
Typical rate of fall of forward and reverse recovery current as a function of collector current
 $di_o/dt, di_{rec}/dt = f(I_c)$



At $V_{CE} = 400$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 32$ Ω

Figure 14. FWD

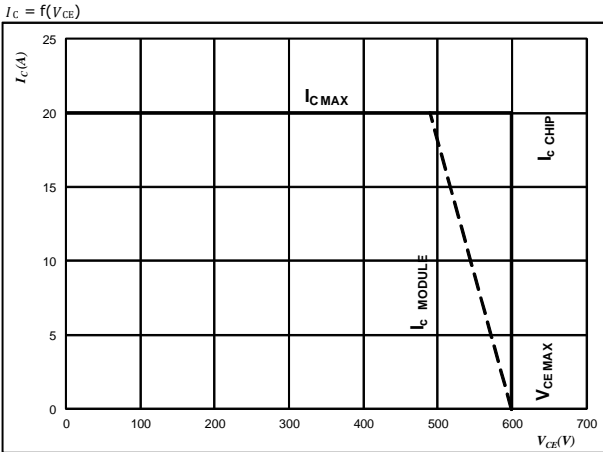
Typical rate of fall of forward and reverse recovery current as a function of IGBT turn on gate resistor



At $V_{CE} = 400$ V
 $V_{GE} = \pm 15$ V
 $I_c = 10$ A

Figure 15. IGBT

Reverse bias safe operating area



At $T_j = 175$ $^{\circ}\text{C}$
 $R_{gon} = 32$ Ω
 $R_{goff} = 32$ Ω



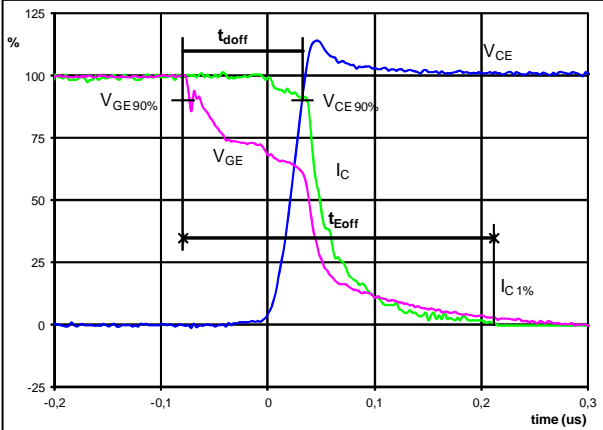
Inverter Switching Definitions

General conditions

T_j	=	125 °C
R_{gon}	=	32 Ω
R_{goff}	=	32 Ω

Figure 1. IGBT

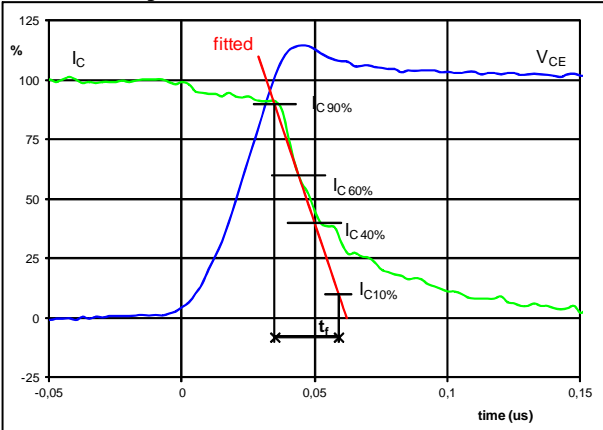
Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff} (t_{Eoff} = integrating time for E_{off})



$V_{GE} (0\%) =$	-15	V
$V_{GE} (100\%) =$	15	V
$V_C (100\%) =$	400	V
$I_C (100\%) =$	10	A
$t_{doff} =$	0,105	μs
$t_{Eoff} =$	0,292	μs

Figure 3. IGBT

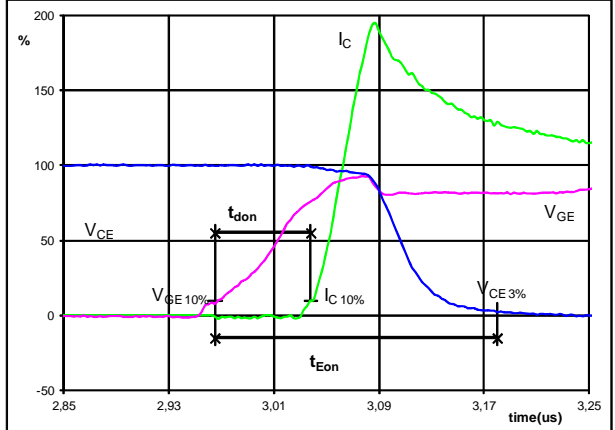
Turn-off Switching Waveforms & definition of t_r



$V_C (100\%) =$	400	V
$I_C (100\%) =$	10	A
$t_r =$	0,035	μs

Figure 2. IGBT

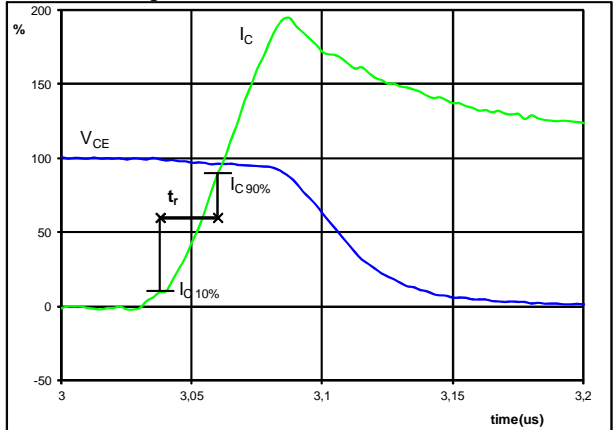
Turn-on Switching Waveforms & definition of t_{don} , t_{Eon} (t_{Eon} = integrating time for E_{on})



$V_{GE} (0\%) =$	-15	V
$V_{GE} (100\%) =$	15	V
$V_C (100\%) =$	400	V
$I_C (100\%) =$	10	A
$t_{don} =$	0,071	μs
$t_{Eon} =$	0,215	μs

Figure 4. IGBT

Turn-on Switching Waveforms & definition of t_r

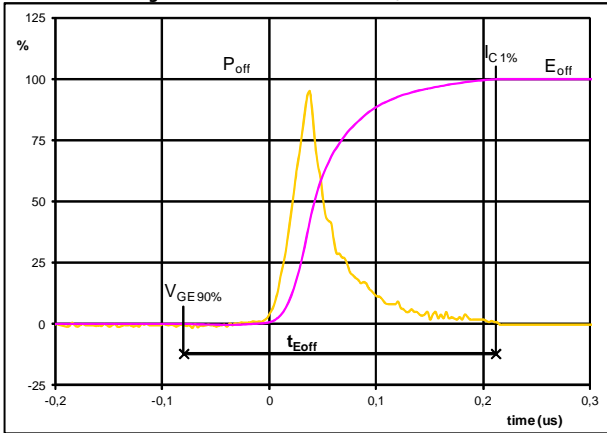


$V_C (100\%) =$	400	V
$I_C (100\%) =$	10	A
$t_r =$	0,022	μs



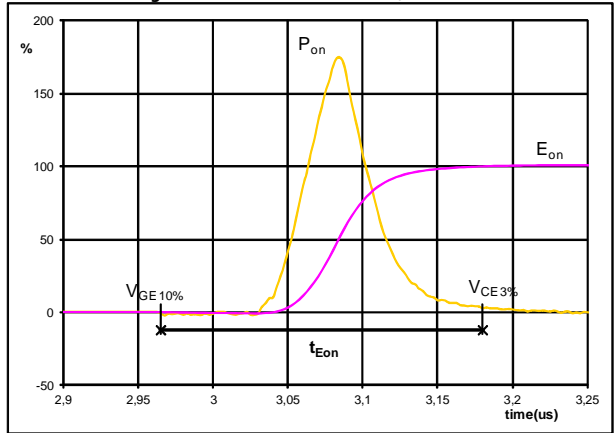
Inverter Switching Definitions

Figure 5. IGBT
Turn-off Switching Waveforms & definition of t_{Eoff}



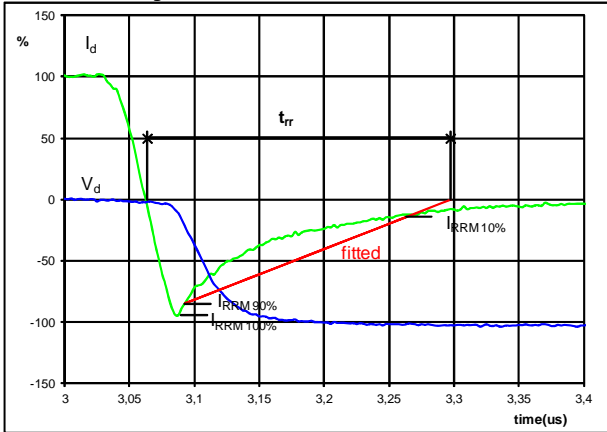
P_{off} (100%) = 4,00 kW
 E_{off} (100%) = 0,18 mJ
 t_{Eoff} = 0,29 μ s

Figure 6. IGBT
Turn-on Switching Waveforms & definition of t_{Eon}



P_{on} (100%) = 4,00 kW
 E_{on} (100%) = 0,36 mJ
 t_{Eon} = 0,21 μ s

Figure 7. FWD
Turn-off Switching Waveforms & definition of t_{rr}

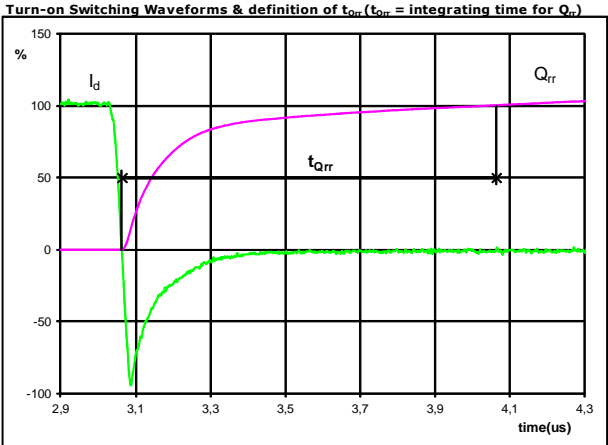


V_d (100%) = 400 V
 I_d (100%) = 10 A
 I_{RRM} (100%) = -10 A
 t_{rr} = 0,233 μ s



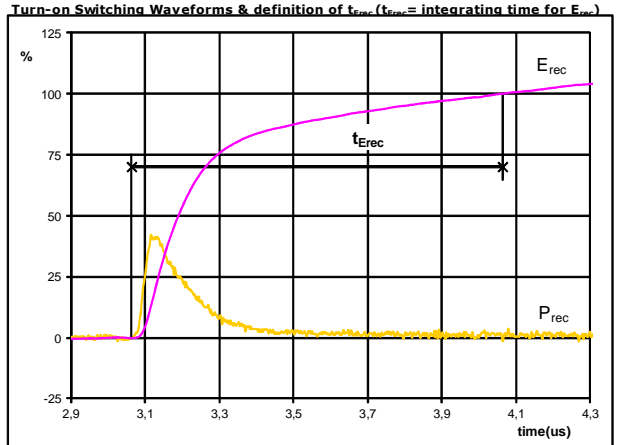
Inverter Switching Definitions

Figure 8. FWD



I_d (100%) = 10 A
 Q_{rr} (100%) = 0,89 μC
 t_{Qrr} = 1,00 μs

Figure 9. FWD

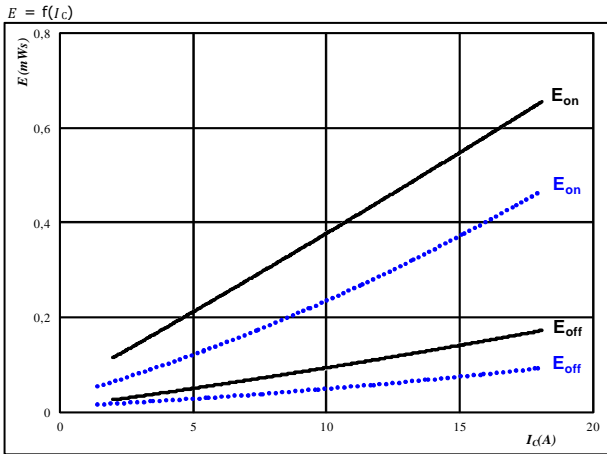


P_{rec} (100%) = 4,00 kW
 E_{rec} (100%) = 0,24 mJ
 t_{Erec} = 1,00 μs



PFC Switching Definitions

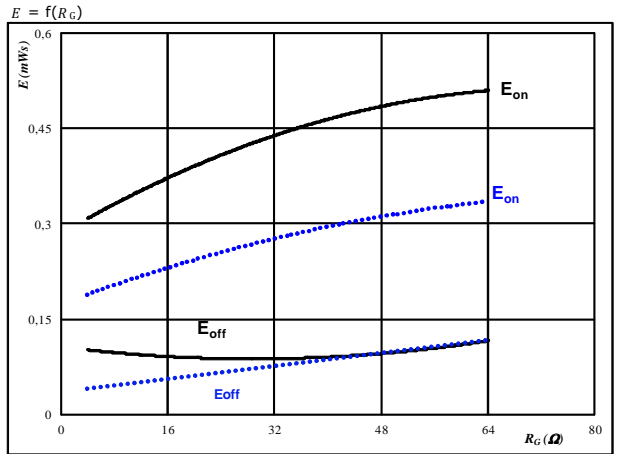
Figure 1. IGBT
Typical switching energy losses as a function of collector current



With an inductive load at

$V_{CE} = 400$ V	$T_j:$ 25 °C
$V_{GE} = 15/0$ V	125 °C	————
$R_{g\text{on}} = 16$ Ω	150 °C	-----
$R_{g\text{off}} = 16$ Ω		

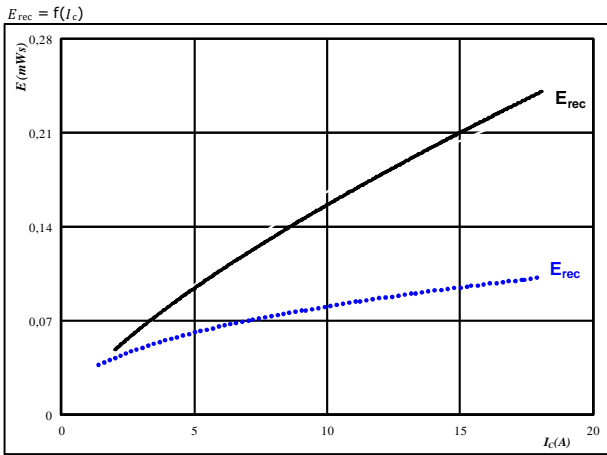
Figure 2. IGBT
Typical switching energy losses as a function of gate resistor



With an inductive load at

$V_{CE} = 400$ V	$T_j:$ 25 °C
$V_{GE} = 15/0$ V	125 °C	————
$I_C = 10$ A	150 °C	-----

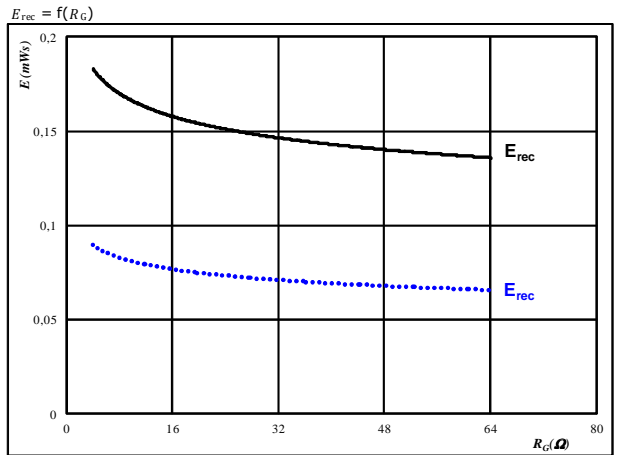
Figure 3. FWD
Typical reverse recovery energy loss as a function of collector current



With an inductive load at

$V_{CE} = 400$ V	$T_j:$ 25 °C
$V_{GE} = 15/0$ V	125 °C	————
$R_{g\text{on}} = 16$ Ω	150 °C	-----

Figure 4. FWD
Typical reverse recovery energy loss as a function of gate resistor



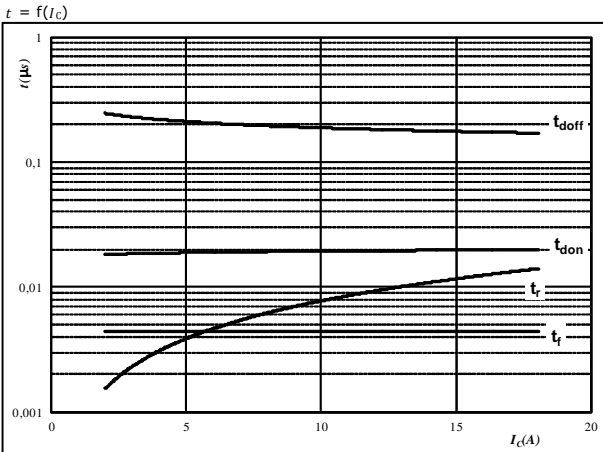
With an inductive load at

$V_{CE} = 400$ V	$T_j:$ 25 °C
$V_{GE} = 15/0$ V	125 °C	————
$I_C = 10$ A	150 °C	-----



PFC Switching Definitions

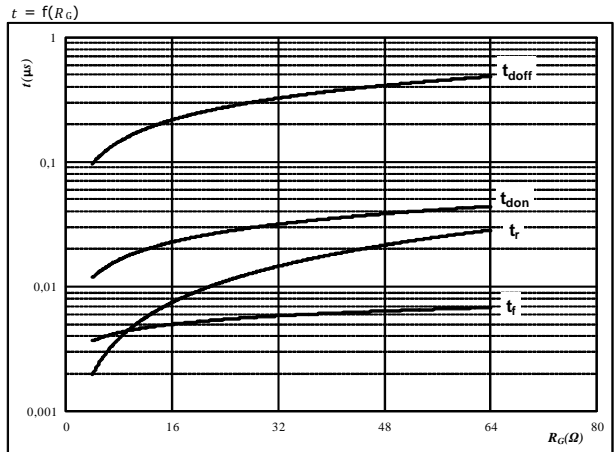
Figure 5. IGBT
Typical switching times as a function of collector current



With an inductive load at

- $T_j = 125 \text{ } ^\circ\text{C}$
- $V_{CE} = 400 \text{ V}$
- $V_{GE} = 15/0 \text{ V}$
- $R_{g(on)} = 16 \text{ } \Omega$
- $R_{g(off)} = 16 \text{ } \Omega$

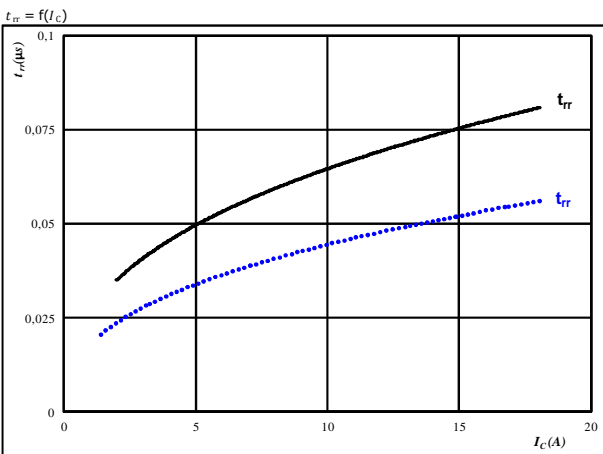
Figure 6. IGBT
Typical switching times as a function of gate resistor



With an inductive load at

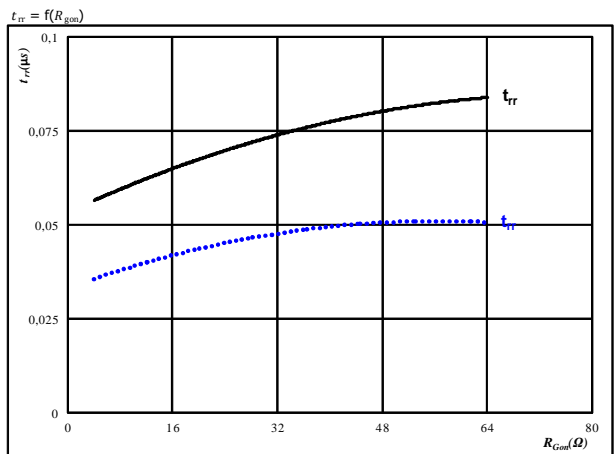
- $T_j = 125 \text{ } ^\circ\text{C}$
- $V_{CE} = 400 \text{ V}$
- $V_{GE} = 15/0 \text{ V}$
- $I_C = 10 \text{ A}$

Figure 7. FWD
Typical reverse recovery time as a function of collector current



- At $V_{CE} = 400 \text{ V}$
 $V_{GE} = 15/0 \text{ V}$
 $R_{g(on)} = 16 \text{ } \Omega$
- T_j : $25 \text{ } ^\circ\text{C}$
 $125 \text{ } ^\circ\text{C}$ ———
 $150 \text{ } ^\circ\text{C}$ - - - -

Figure 8. FWD
Typical reverse recovery time as a function of IGBT turn on gate resistor



- At $V_{CE} = 400 \text{ V}$
 $V_{GE} = 15/0 \text{ V}$
 $I_C = 10 \text{ A}$
- T_j : $25 \text{ } ^\circ\text{C}$
 $125 \text{ } ^\circ\text{C}$ ———
 $150 \text{ } ^\circ\text{C}$ - - - -

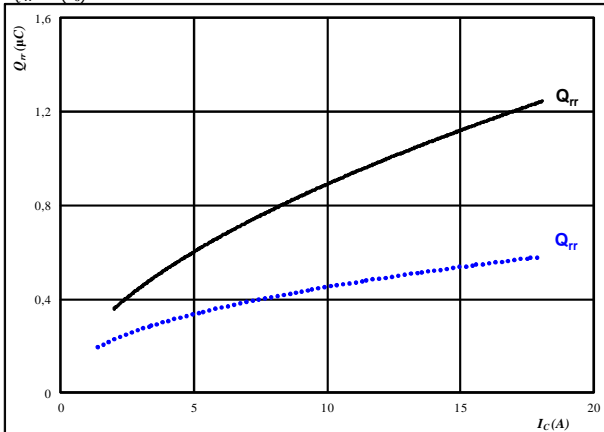


PFC Switching Definitions

Figure 9. FWD

Typical reverse recovery charge as a function of collector current

$$Q_{rr} = f(I_C)$$

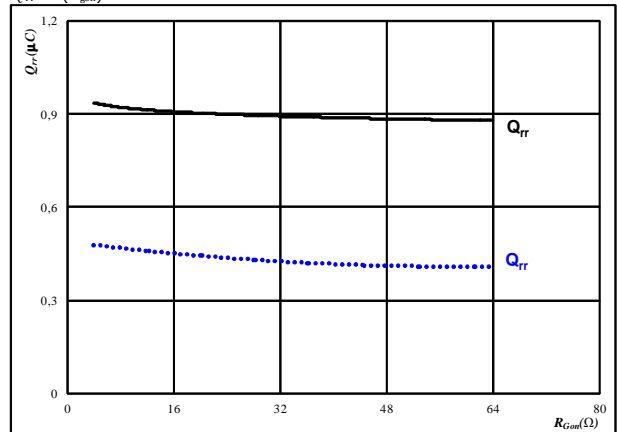


At $V_{CE^*} = 400$ V
 $V_{GE} = 15/0$ V
 $R_{ggn} = 16$ Ω
 T_j : 25 °C (dotted blue line)
 125 °C (solid black line)
 150 °C (dashed red line)

Figure 10. FWD

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$$Q_{rr} = f(R_{ggn})$$

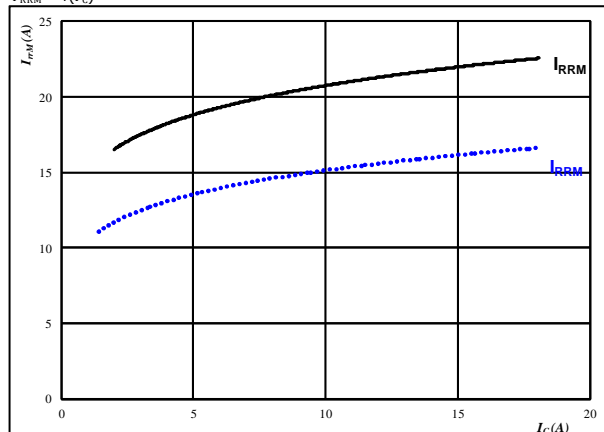


At $V_{CE^*} = 400$ V
 $V_{GE} = 15/0$ V
 $I_C = 10$ A
 T_j : 25 °C (dotted blue line)
 125 °C (solid black line)
 150 °C (dashed red line)

Figure 11. FWD

Typical reverse recovery current as a function of collector current

$$I_{RRM} = f(I_C)$$

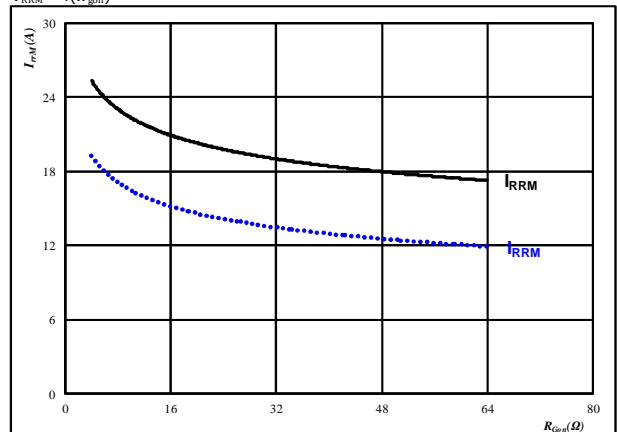


At $V_{CE^*} = 400$ V
 $V_{GE} = 15/0$ V
 $R_{ggn} = 16$ Ω
 T_j : 25 °C (dotted blue line)
 125 °C (solid black line)
 150 °C (dashed red line)

Figure 12. FWD

Typical reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RRM} = f(R_{ggn})$$

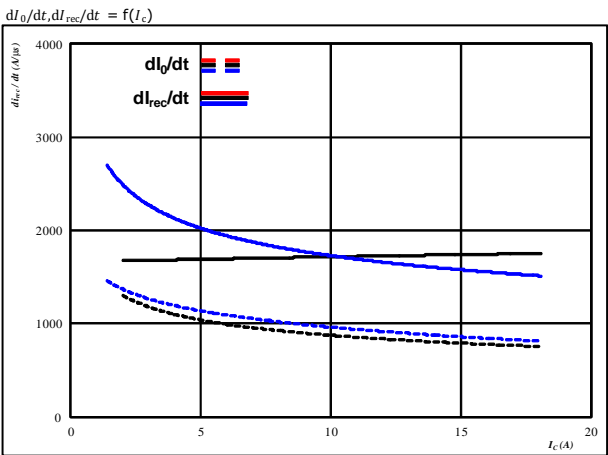


At $V_{CE^*} = 400$ V
 $V_{GE} = 15/0$ V
 $I_C = 10$ A
 T_j : 25 °C (dotted blue line)
 125 °C (solid black line)
 150 °C (dashed red line)



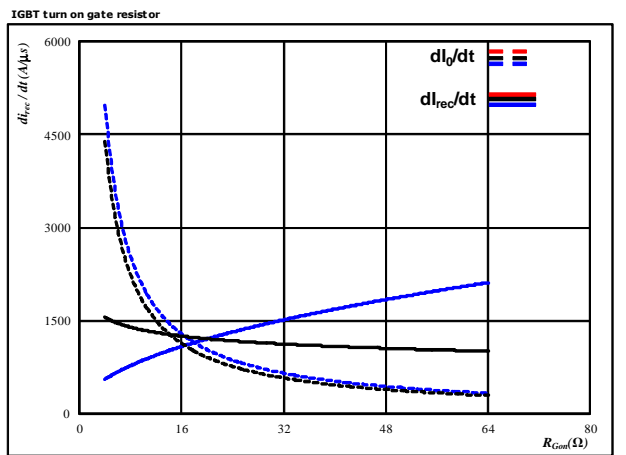
PFC Switching Definitions

Figure 13. FWD
Typical rate of fall of forward and reverse recovery current as a function of collector current



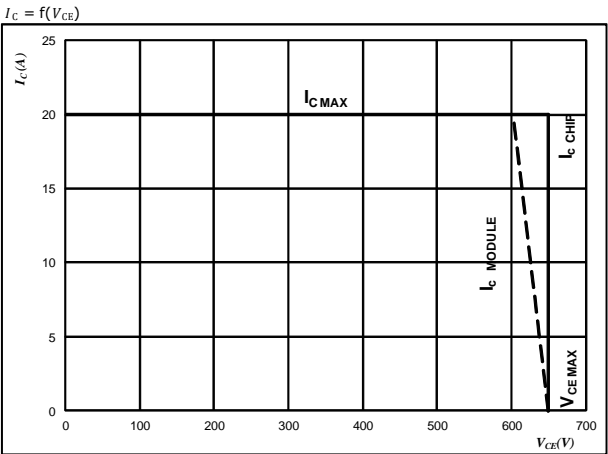
At $V_{CE^*} = 400$ V
 $V_{GE} = 15/0$ V
 $R_{gon} = 16$ Ω

Figure 14. FWD
Typical rate of fall of forward and reverse recovery current as a function of IGBT turn on gate resistor



At $V_{CE^*} = 400$ V
 $V_{GE} = 15/0$ V
 $I_c = 10$ A

Figure 15. IGBT
Reverse bias safe operating area



At $T_j = 175$ $^{\circ}C$
 $R_{gon} = 16$ Ω
 $R_{goff} = 16$ Ω



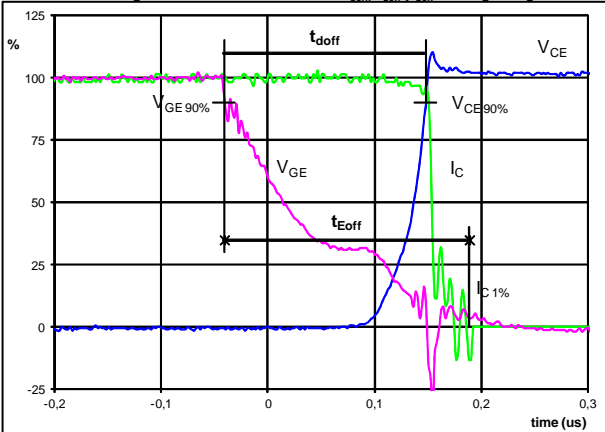
PFC Switching Definitions

General conditions

T_j	=	125 °C
R_{gon}	=	16 Ω
R_{goff}	=	16 Ω

Figure 1. IGBT

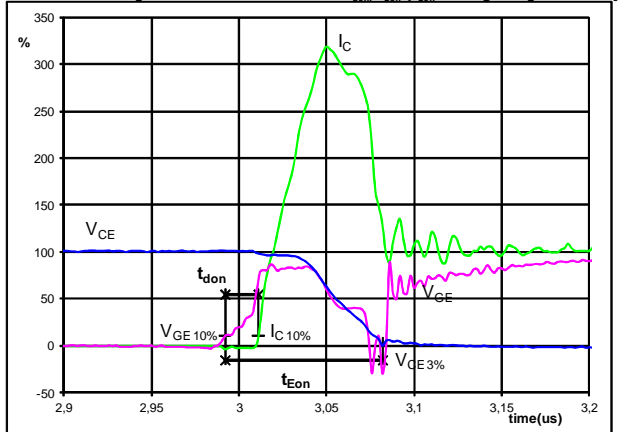
Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff} (t_{Eoff} = integrating time for E_{off})



V_{GE} (0%) =	0	V
V_{GE} (100%) =	15	V
V_C (100%) =	400	V
I_C (100%) =	10	A
t_{doff} =	0,189	μ s
t_{Eoff} =	0,230	μ s

Figure 2. IGBT

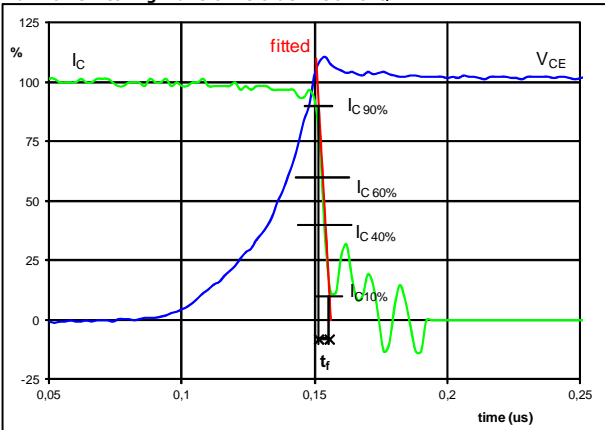
Turn-on Switching Waveforms & definition of t_{don} , t_{Eon} (t_{Eon} = integrating time for E_{on})



V_{GE} (0%) =	0	V
V_{GE} (100%) =	15	V
V_C (100%) =	400	V
I_C (100%) =	10	A
t_{don} =	0,019	μ s
t_{Eon} =	0,090	μ s

Figure 3. IGBT

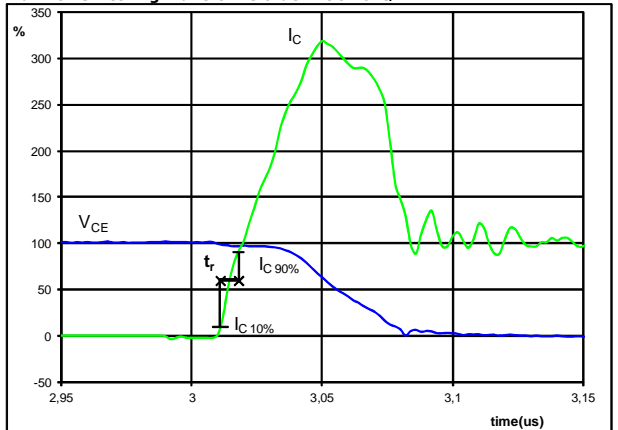
Turn-off Switching Waveforms & definition of t_r



V_C (100%) =	400	V
I_C (100%) =	10	A
t_r =	0,004	μ s

Figure 4. IGBT

Turn-on Switching Waveforms & definition of t_r



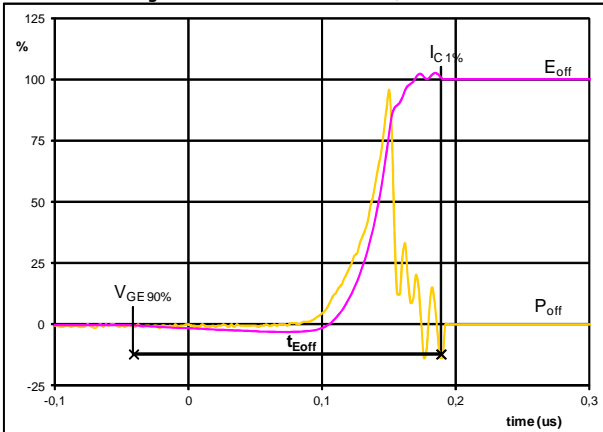
V_C (100%) =	400	V
I_C (100%) =	10	A
t_r =	0,007	μ s



Vincotech

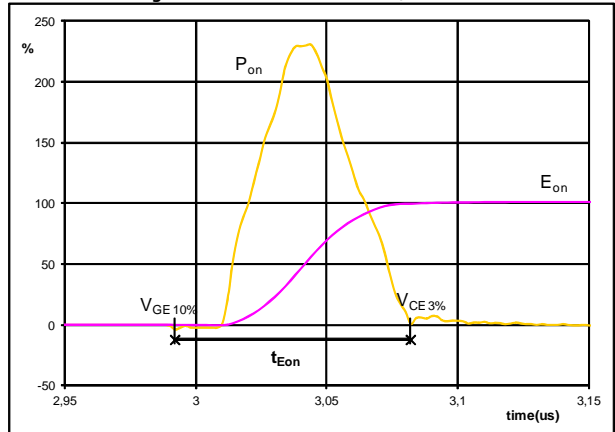
PFC Switching Definitions

Figure 5. IGBT
Turn-off Switching Waveforms & definition of t_{Eoff}



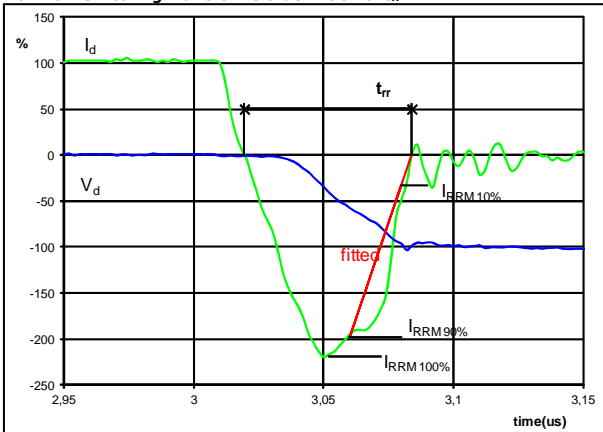
P_{off} (100%) = 3,99 kW
 E_{off} (100%) = 0,09 mJ
 t_{Eoff} = 0,23 μ s

Figure 6. IGBT
Turn-on Switching Waveforms & definition of t_{Eon}



P_{on} (100%) = 3,99 kW
 E_{on} (100%) = 0,38 mJ
 t_{Eon} = 0,09 μ s

Figure 7. FWD
Turn-off Switching Waveforms & definition of t_{rr}

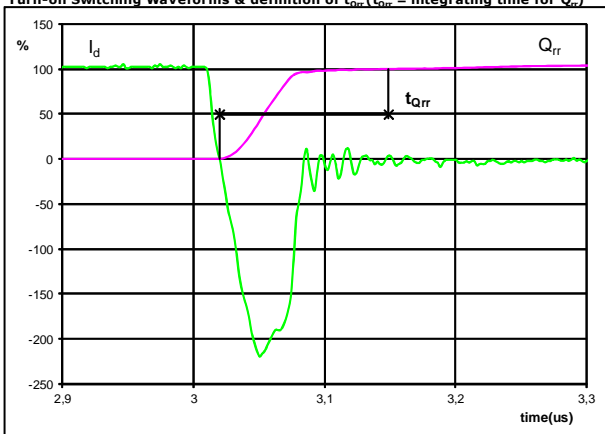


V_d (100%) = 400 V
 I_d (100%) = 10 A
 I_{RRM} (100%) = -22 A
 t_{rr} = 0,066 μ s



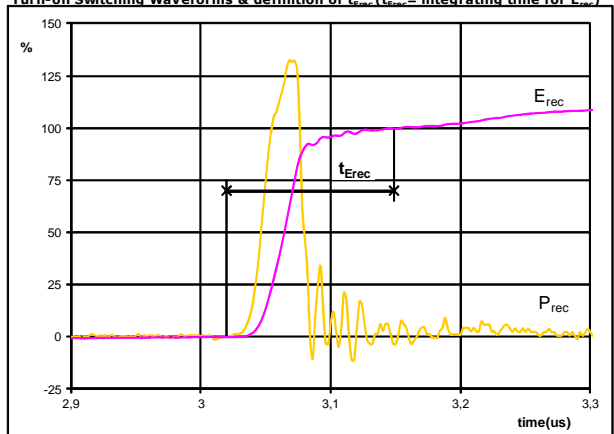
PFC Switching Definitions

Figure 8. FWD
Turn-on Switching Waveforms & definition of t_{Qrr} (t_{Qrr} = integrating time for Q_{rr})



I_d (100%) =	10	A
Q_{rr} (100%) =	0,93	μC
t_{Qrr} =	0,13	μs

Figure 9. FWD
Turn-on Switching Waveforms & definition of t_{Erec} (t_{Erec} = integrating time for E_{rec})



P_{rec} (100%) =	3,99	kW
E_{rec} (100%) =	0,17	mJ
t_{Erec} =	0,13	μs



Vincotech

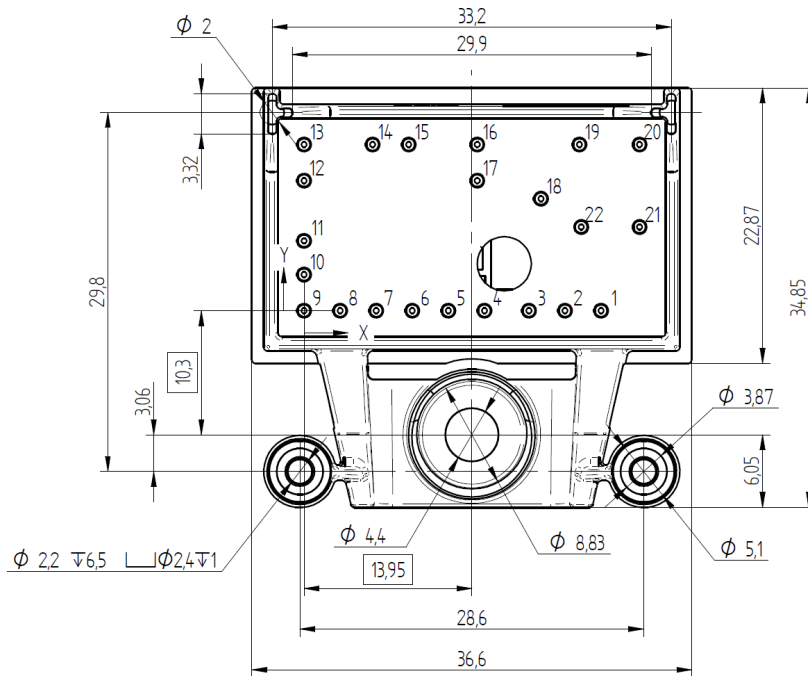
Ordering Code & Marking

Version	Ordering Code	in DataMatrix as	in packaging barcode as
without thermal paste 17mm housing	10-0B06PPA010RC-L025A09	L025A09	L025A09

Text	Name		Date code	UL & Vinco	Lot	Serial
	patamatrix	NN-NNNNNNNNNNNN-TTTTTTT	WWYY	UL Vinco	LLLLL	SSSS
	Type	Lot number	Serial	Date code		
	TTTT-TTT	LLLLL	SSSS	WWYY		

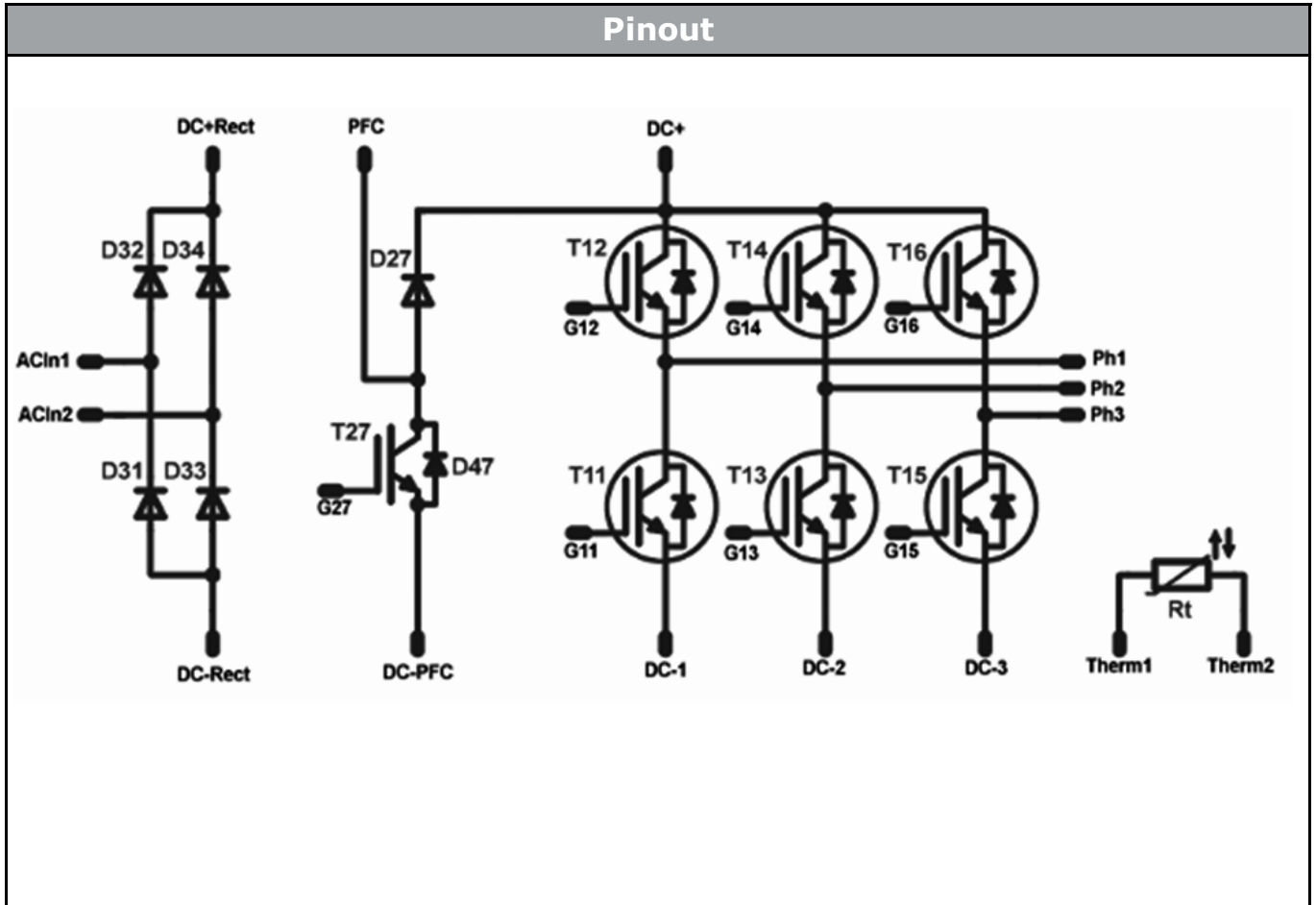
Outline

Pin table [mm]			
Pin	X	Y	Function
1	24,7	0	DC-Rect
2	21,7	0	DC-PFC
3	18,7	0	G27
4	15	0	DC-3
5	12	0	G15
6	9	0	DC-2
7	6	0	G13
8	3	0	DC-1
9	0	0	G11
10	0	3	Therm2
11	0	5,8	Therm1
12	0	10,8	G12
13	0	13,8	Ph1
14	5,7	13,8	G14
15	8,7	13,8	Ph2
16	14,4	13,8	Ph3
17	14,4	10,8	G16
18	19,7	9,3	DC+
19	22,9	13,8	PFC
20	27,9	13,8	ACIn1
21	27,9	6,95	ACIn2
22	23,05	6,95	DC+Rect





Vincotech



Identification						
ID	Component	Voltage	Technology	Current	Function	Comment
T11-T16	IGBT	600V		10A	Inverter switch	
T27	IGBT	650V		30A	PFC Switch	
D27	FWD	650V		30A	PFC Diode	
D47	Diode	650V		6A	PFC Switch Protection Diode	
D31-D34	Diode	1600V		7A	Rectifier Diode	
R _t	NTC	-		-	Thermistor	



Vincotech

Packaging instruction					
Standard packaging quantity (SPQ)	200	>SPQ	Standard	<SPQ	Sample

Handling instruction	
Handling instructions for <i>flow</i> 0 B packages see vincotech.com website.	

Document No.:	Date:	Modification:	Pages
10-0B06PPA010RC-L025A09-D2-14	04 Jun. 2015	3D drawing, Outline Pin-table (Pin 16 - 17)	1, 27

DISCLAIMER

The information, specifications, procedures, methods and recommendations herein (together "information") are presented by Vincotech to reader in good faith, are believed to be accurate and reliable, but may well be incomplete and/or not applicable to all conditions or situations that may exist or occur. Vincotech reserves the right to make any changes without further notice to any products to improve reliability, function or design. No representation, guarantee or warranty is made to reader as to the accuracy, reliability or completeness of said information or that the application or use of any of the same will avoid hazards, accidents, losses, damages or injury of any kind to persons or property or that the same will not infringe third parties rights or give desired results. It is reader's sole responsibility to test and determine the suitability of the information and the product for reader's intended use.

LIFE SUPPORT POLICY

Vincotech products are not authorised for use as critical components in life support devices or systems without the express written approval of Vincotech.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in labelling can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.