

SANYO Semiconductors DATA SHEET



Monolithic Linear IC **LA5160AM** — 5V/0.1A Constant-voltage Power Supply IC with Output ON/OFF Feature

Overview

The LA5160AM is a general-purpose constant-voltage power supply IC incorporating the output ON/OFF function, which offers advantages such as small current drain when output OFF and saves power dissipation of the equipment.

Functions & Features

- Output voltage ON/OFF control with the strobe pin (active, low)
- Output current of 100mA obtainable
- Small current drain when output OFF and optimum for power saving
- MFP8 package, ensuring easy mounting design
- Full compliment of protection circuits incorporated (including overcurrent protection, thermal protection)

Specifications

Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Input Voltage	V _{IN} max		40	V
Strobe Pin Voltage	VST max		V _{IN} max	V
Allowable Power Dissipation 1	Pd max1	Independent IC	0.3	W
Allowable Power Dissipation 2	Pd max2	* Mounted on a specified board	0.8	W
Operating Temperature	Topr		-40 to +125	°C
Storage Temperature	Tstg		-55 to +150	°C

* Specified board: 114.3mm×76.1mm×1.6mm Board material: glass epoxy

Recommended Operating Conditions at Ta=-40°C • +25°C • +125°C

Parameter	Symbol	Conditions	Ratings	Unit
Input Voltage	VIN		7.5 to 20	V
Output Current	IOUT		0 to 100	mA
Output ON Control Voltage	VSTL		-0.3 to 0.7	V
Output OFF Control Voltage	VSTH		2.0 to V _{IN}	V

- Any and all SANYO Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your SANYO Semiconductor representative nearest you before using any SANYO Semiconductor products described or contained herein in such applications.
- SANYO Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO Semiconductor products described or contained herein.

SANYO Semiconductor Co., Ltd. TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

LA5160AM

Operation Characteristics at Ta=-40°C • +25°C • +125°C, unless otherwise specified,

$V_{\ensuremath{IN}\xspace}{10V}$ and $I_{\ensuremath{OUT}\xspace}{50mA}$ in the specified test circuit

Doromotor	Querra ha a l	Qualitiens	Ratings				
Parameter	Symbol	Conditions	min	typ	max	Unit	
Output Voltage 1	VOUT ¹		4.8	5.0	5.2	V	
Line Regulation 1	ΔVOLN1	$7V \le V_{IN} \le 25V, I_{OUT}=20mA$		3	50	mV	
Line Regulation 2	ΔVOLN2	$8V \le V_{IN} \le 20V$, $I_{OUT}=20mA$		1	25	mV	
Load Regulation 1	ΔVOLD1	$1mA \le I_{OUT} \le 50mA$			100	mV	
Load Regulation 2	∆VOLD2	$1mA \le I_{OUT} \le 20mA$			50	mV	
Output Voltage 2	V _{OUT} 2	$7V \le V_{IN} \le 20V$, $1mA \le I_{OUT} \le 50mA$	4.75		5.25	V	
Current Dissipation	IQ			1.9	5.0	mA	
Output Noise Voltage	VNO	$10Hz \le f \le 100kHz$		90		μVrms	
	Rrej1	f=120Hz 8V≤V _{IN} ≤19V I _{OUT} =10mA	60	63		dB	
Ripple Rejection	Rrej2	f=120Hz 8V ≤ V _{IN} ≤ 19V I _{OUT} =50mA	50	54		dB	
Minimum I/O Voltage Difference	Vdrop	I _{OUT} =50mA		2.0		V	
Output ON Control Voltage	VSTL				0.7	V	
Short-Circuit Current	IOSC	V _{IN} =35V, relative to GND		300		mA	
Peak Output Current	IOP			700		mA	

[VST="H" when the output is OFF]

Deremeter	Symbol	Conditions	Ratings			Linit
Parameter	Symbol	Conditions	min	typ	max	Unit
"L" Output Voltage	VOOFF	VST=5V		20	200	mV
Quiescent Current	IQOFF	Excluding VST=5V and ISTB		35	40	μA
Output OFF Control Voltage	VSTH		2.0		VIN	V

Thermal protection

Parameter	Symbol	Conditions	Ratings			11-14
			min	typ	max	Unit
Operating Temperature *	TTSD		150	180		°C

Note : The value with an asterisk mark for thermal protection is the design target value and no measurement is made.

Pin Assignment



Top view

Note: Do not use NC pins (Pin Nos. 1, 3, 6, and 8)

Package Dimensions

unit : mm 3032D



Equivalent Circuit Block Diagram



Specified Test Circuit Diagram



Cautions for application

- Note: 1) To stabilize the operation, arrange CIN and COUT as near as possible to the IC.
 - 2) COUT must have 0.1μ F or more, with less capacitance fluctuation due to temperature, so as to prevent oscillation at low temperature (such as a tantalum capacitor, etc.).
 - 3) With the STB pin OPEN, the internal bias causes the output in the ON state.
 - When the STB function is not to be used, connect the STB pin to GND to ensure trouble-free operation of STB.4) Note that wrong connection, such as connection of V_{IN} to minus and GND to plus, may cause flow of the overcurrent.

Function Table

VSTB	VOUT	
L	н	
Н	L	



Equivalent Circuit for ON/OFF Control Input, etc.







The values shown in the characteristics diagrams are all typical values.

- Specifications of any and all SANYO Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- SANYO Semiconductor Co., Ltd. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of SANYO Semiconductor Co., Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO Semiconductor product that you intend to use.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. SANYO Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

This catalog provides information as of June, 2005. Specifications and information herein are subject to change without notice.