



Package: QFN, 16-Pin, 0.9mmx3mmx3mm

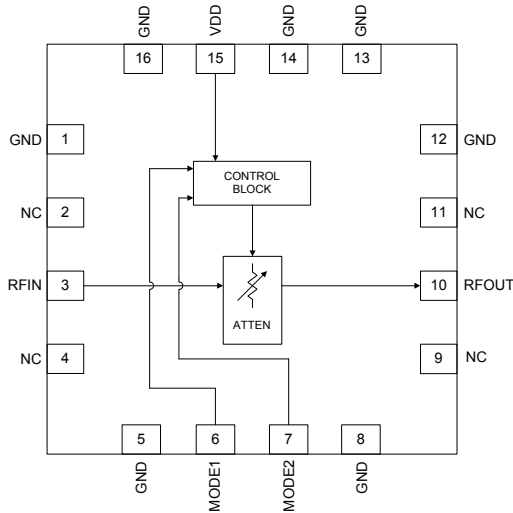


Features

- Patent Pending Circuit Architecture
- Broadband 50MHz to 6000MHz Frequency Range
- 3 Selectable Attenuation versus Temperature Slopes
- +55dBm IIP3 Typical
- +85dBm IIP2 Typical
- High 1dB Compression Point >+30dBm
- Low Supply Current 1mA Typical
- 3.3V Power Supply
- Class 1C ESD (1000V)
- Complete Solution in a Small 3mmx3mm QFN Package
- 5V Part Available - RFSA4013

Applications

- Cellular, 3G Infrastructure
- WiBro, WiMax, LTE
- Microwave Radio
- High-Linearity Level Control



Functional Block Diagram

Product Description

RFMD's RFSA4023 is a fully monolithic analog temperature compensating attenuator (TCA) featuring exceptional linearity over its entire gain control range. It is designed to offset the gain reduction of an RF component over temperature without the need for closed loop feedback. Three customer selectable temperature coefficients make it a flexible solution for RF lineups. It incorporates revolutionary new circuit architecture to solve a long standing industry problem with regards to attenuator architecture: high IP3, low DC current and broad bandwidth. Traditional approaches for solving this problem require expensive co-fired ceramics with temperature sensitive materials or current hungry PIN diodes with elaborate area consuming control circuits. This temperature compensating attenuator requires only a single supply voltage and two logic bits to set control attenuation slope versus temperature. The RFSA4023 draws a very low 1mA current and is packaged in a small 3mmx3mm QFN. This attenuator is matched to 50Ω over its rated control range and frequency with no external matching components required.

Ordering Information

RFSA4023SQ	Sample bag with 25 pieces
RFSA4023SR	7" Sample reel with 100 pieces
RFSA4023TR7	7" Reel with 2500 pieces
RFSA4023PCK-410	50MHz to 6000MHz PCBA with 5-piece sample bag

Optimum Technology Matching® Applied

- | | | | |
|--------------------------------------|--------------------------------------|---|------------------------------------|
| <input type="checkbox"/> GaAs HBT | <input type="checkbox"/> SiGe BiCMOS | <input type="checkbox"/> GaAs pHEMT | <input type="checkbox"/> GaN HEMT |
| <input type="checkbox"/> GaAs MESFET | <input type="checkbox"/> Si BiCMOS | <input checked="" type="checkbox"/> Si CMOS | <input type="checkbox"/> BIFET HBT |
| <input type="checkbox"/> InGaP HBT | <input type="checkbox"/> SiGe HBT | <input type="checkbox"/> Si BJT | <input type="checkbox"/> LDMOS |

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Absolute Maximum Ratings

Parameter	Rating	Unit
Supply Voltage (V_{DD})	-0.5 to +4	V
Mode Pin Voltage (MODE 1)	-0.5 to +4	V
Mode Pin Voltage (MODE 2)	-0.5 to +4	V
RF Input Power	+30	dBm
Operating Temperature	-40 to +85	°C
Storage Temperature	-65 to +150	°C
Maximum Junction Temperature	+125	°C
ESD - Human Body Model (HBM)	1000	V



Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

RoHS status based on EU Directive 2011/65/EU (at time of this document revision).

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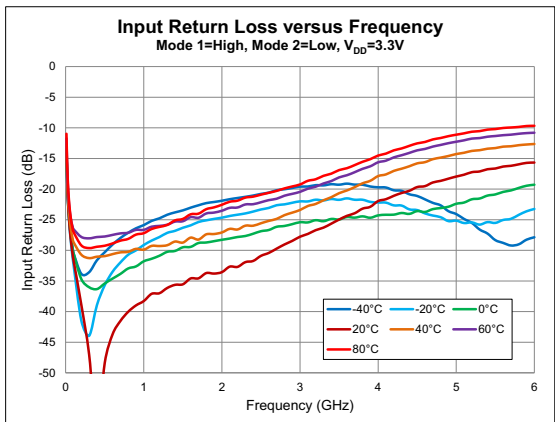
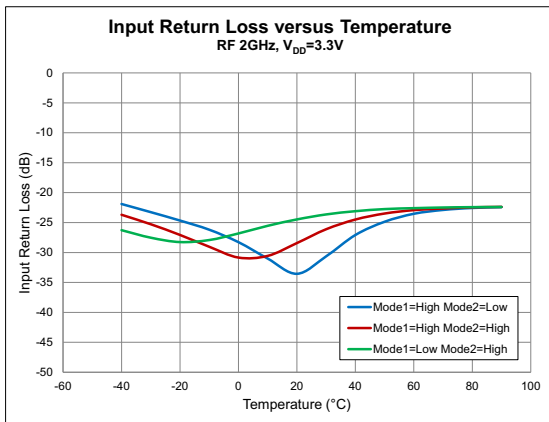
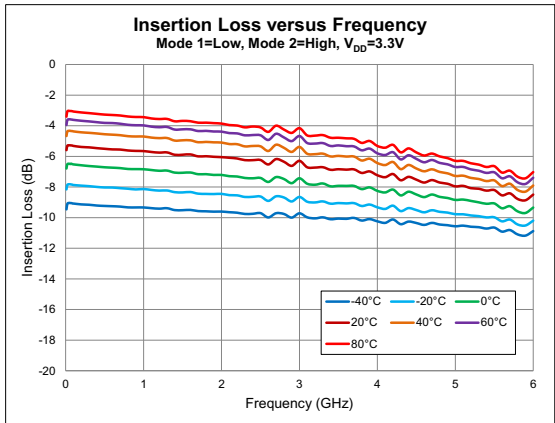
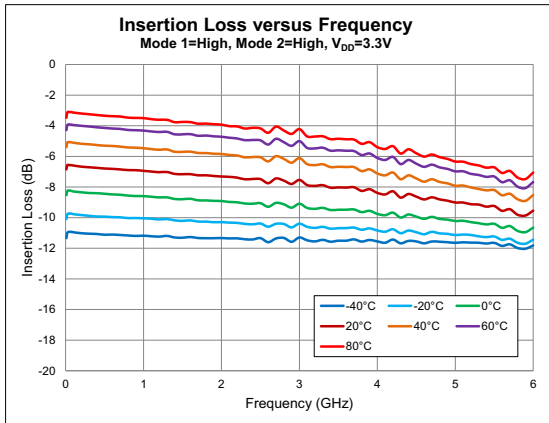
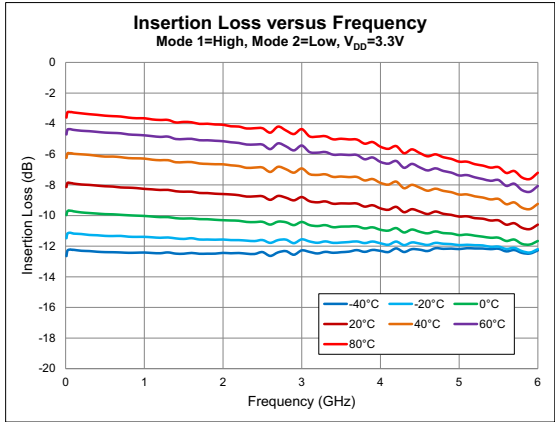
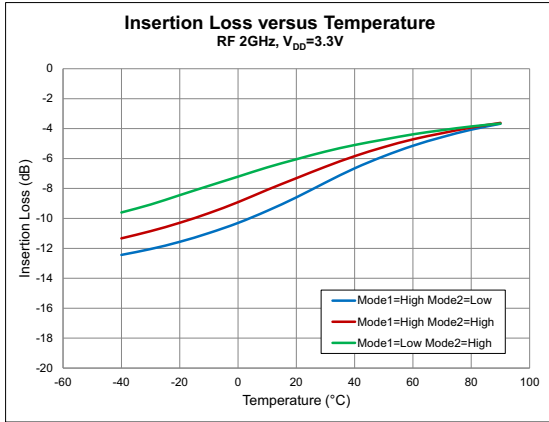


RFMD Green: RoHS compliant per EU Directive 2011/65/EU, halogen free per IEC 61249-2-21, < 1000 ppm each of antimony trioxide in polymeric materials and red phosphorus as a flame retardant, and <2% antimony in solder.

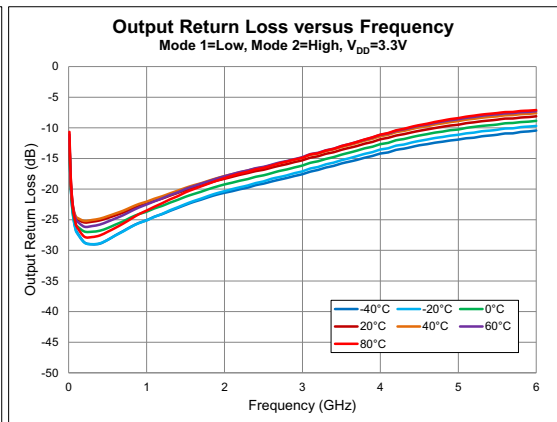
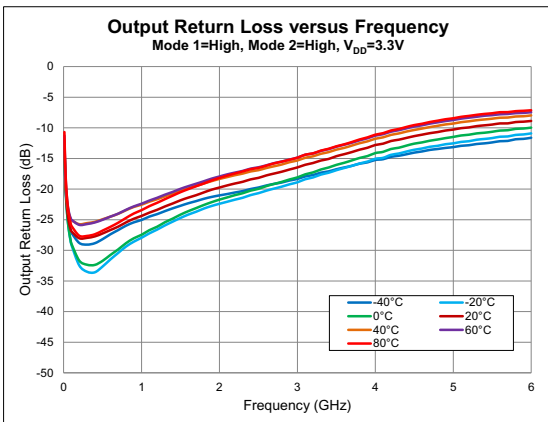
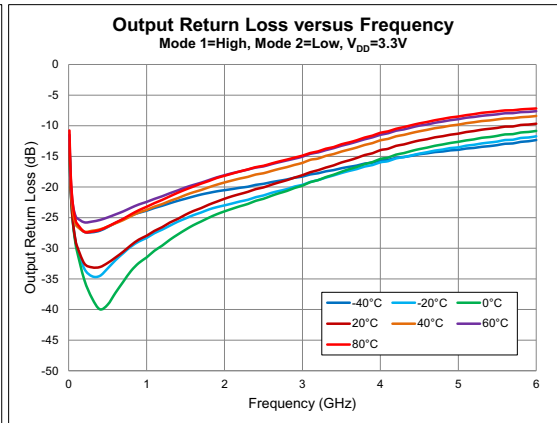
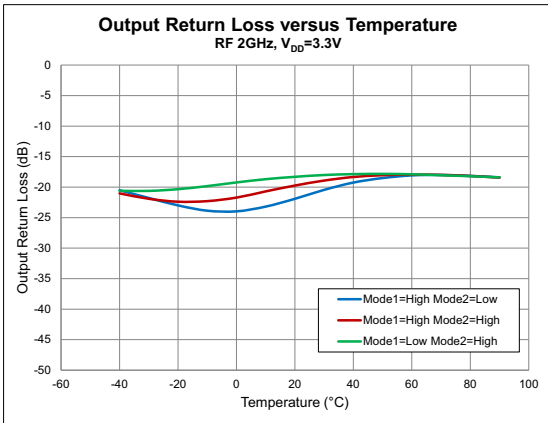
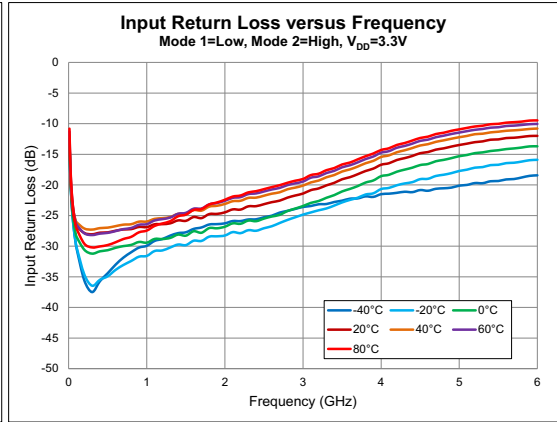
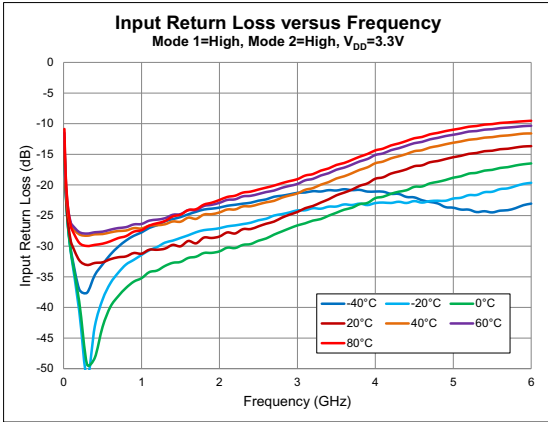
Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
General					
Supply Voltage (V_{DD})	3	3.3	3.5	V	
Supply Current		1		mA	
Operating Temperature	-40		85	°C	
Thermal Resistance		45		°C/W	
RF Input Power			27	dBm	
RF Performance					
Frequency Range	50		6000	MHz	
Attenuation Slope Mode1=Low Mode2=High		0.045		dB/°C	-40 °C to 85 °C
Attenuation Slope Mode1=High Mode2=High		0.058		dB/°C	-40 °C to 85 °C
Attenuation Slope Mode1=High Mode2=Low		0.066		dB/°C	-40 °C to 85 °C
Nominal +25 °C Attenuation Mode1=Low Mode2=High		5.6		dB	
Nominal +25 °C Attenuation Mode1=High Mode2=High		6.70		dB	
Nominal +25 °C Attenuation Mode1=High Mode2=Low		7.85		dB	
Relative Phase		3		Deg	Insertion Phase at +25 °C temperature relative to max temperature
Return Loss		20		dB	
Input 1dB Compression Point		30		dBm	
Input IP3	45	55		dBm	$P_{IN} + (IM3_{dBc}/2)$
Input IP2		85		dBm	$P_{IN} + IM2_{dBc}$; IM2 is F1 + F2
Input IH2		87		dBm	$P_{IN} + H2_{dBc}$; H2 is Second Harmonic
Input IH3		58		dBm	$P_{IN} + (H3_{dBc}/2)$; H3 is Third Harmonic
Power Supply					
MODE pin Logic Low			+0.4	V	
MODE pin Logic High	1			V	

NOTE: Typical performance at nominal conditions unless otherwise noted: Supply voltage=3.3V, Operating temperature=25 °C, RF frequency 2GHz

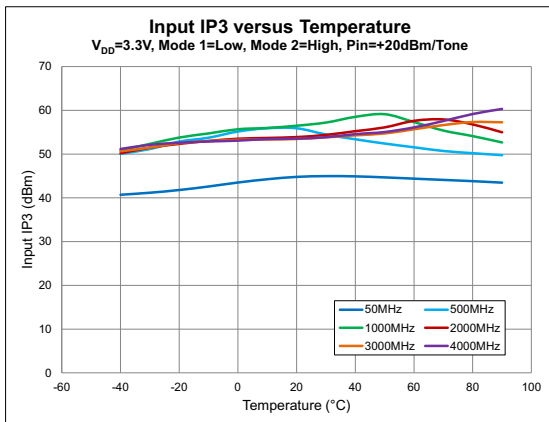
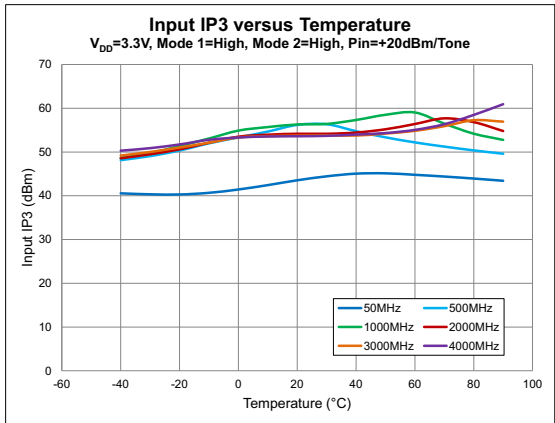
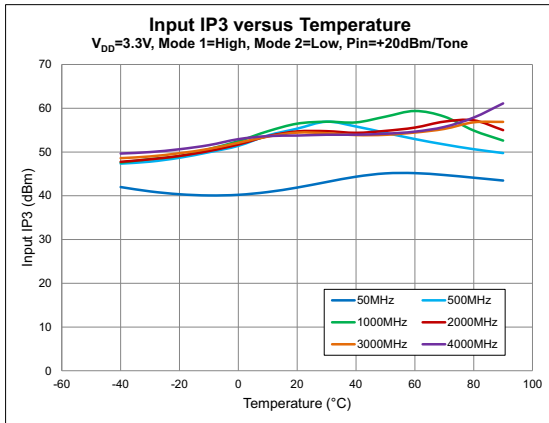
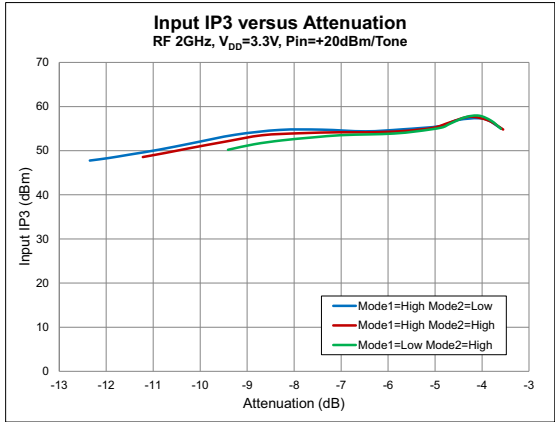
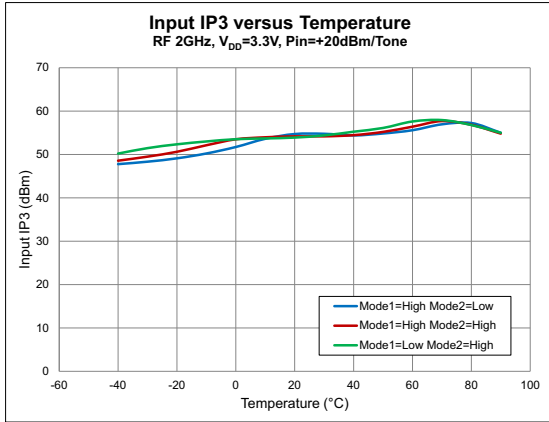
Measured Performance



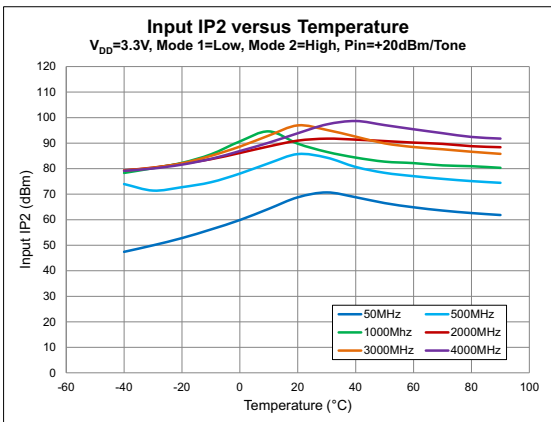
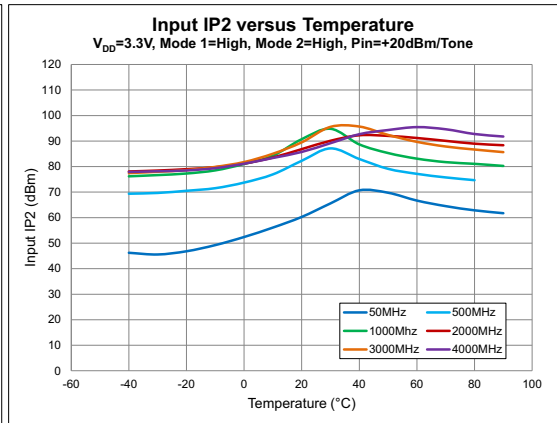
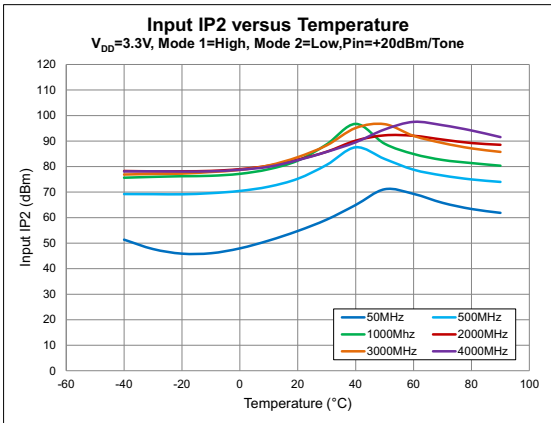
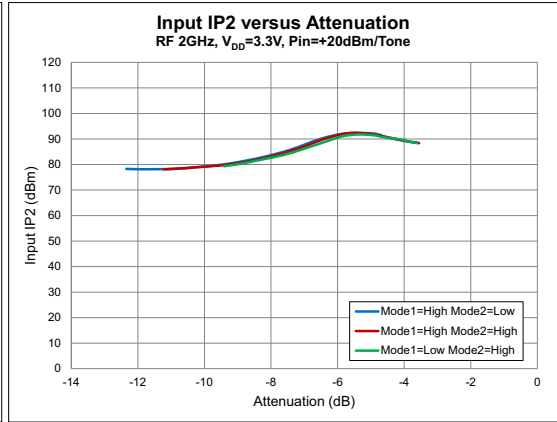
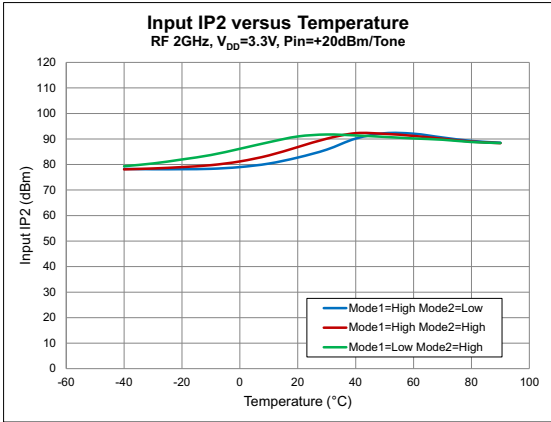
Measured Performance



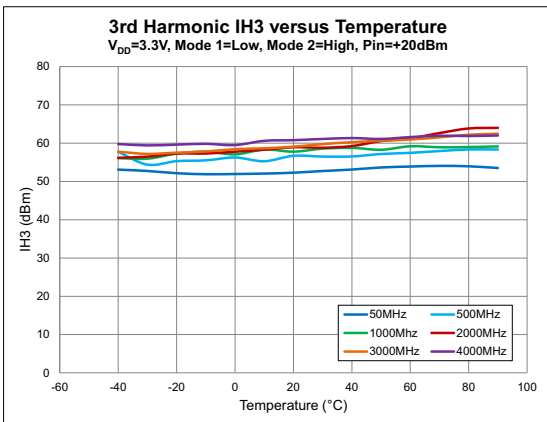
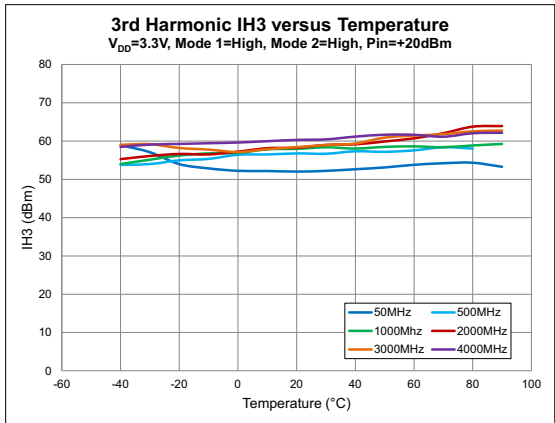
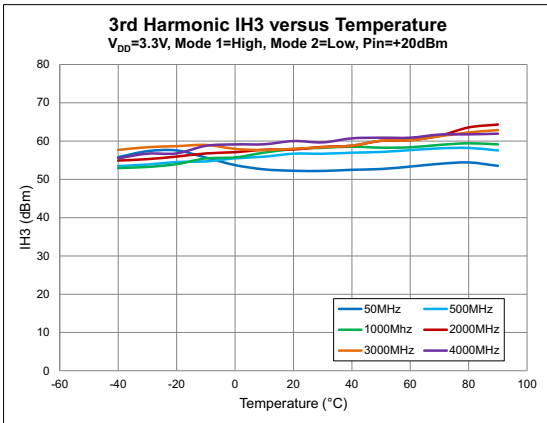
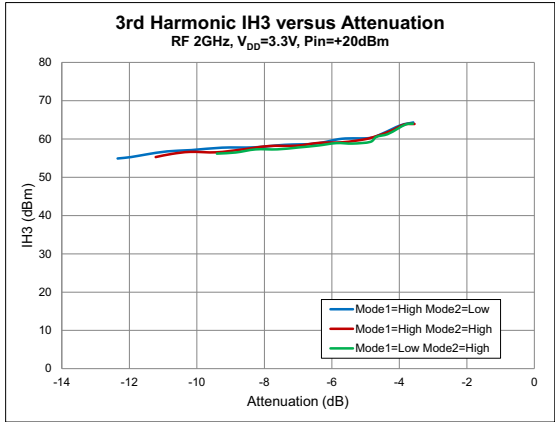
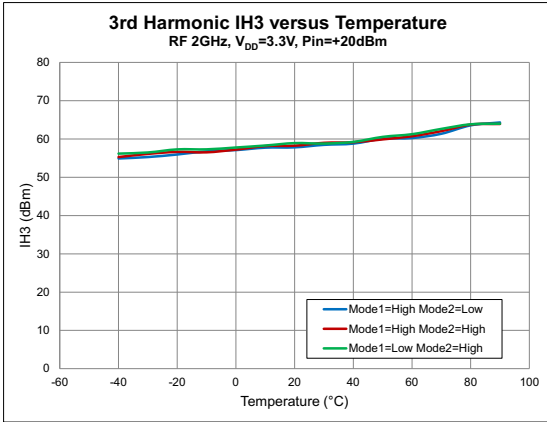
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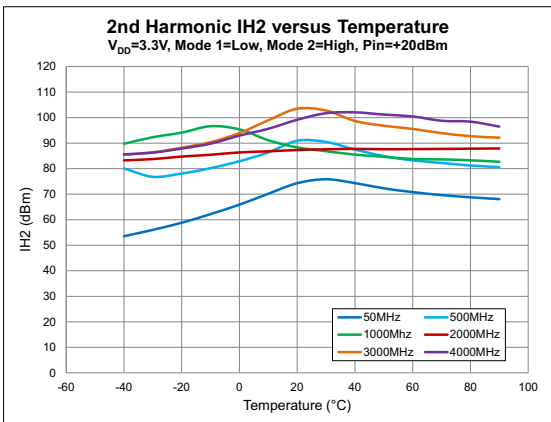
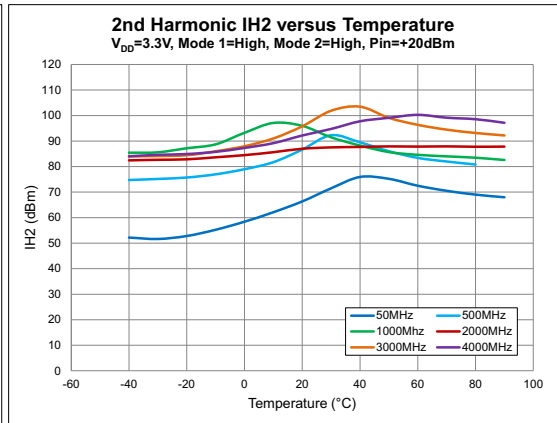
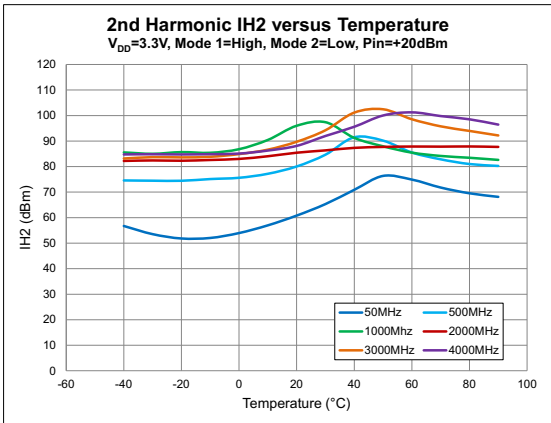
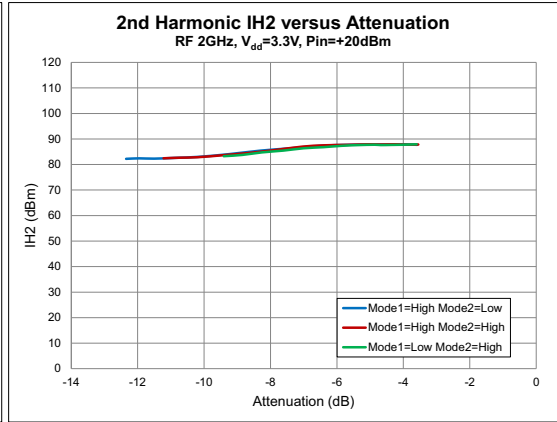
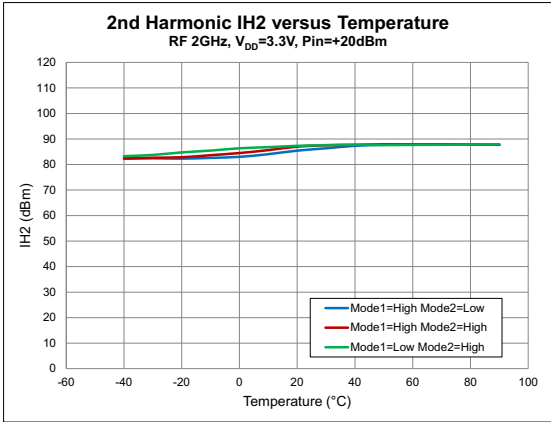
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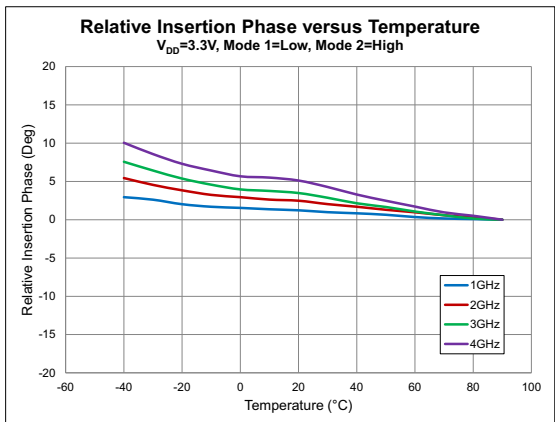
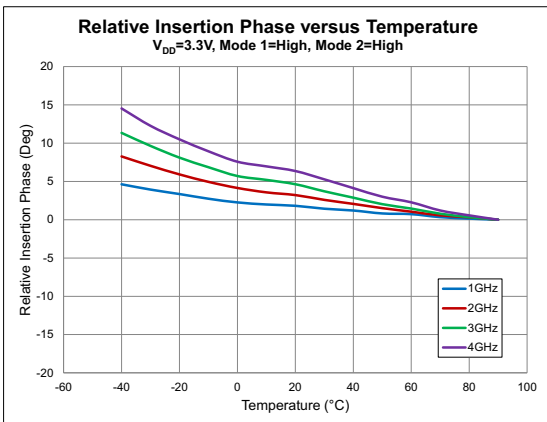
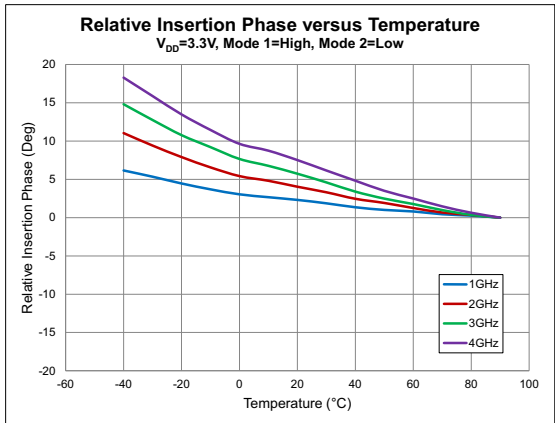
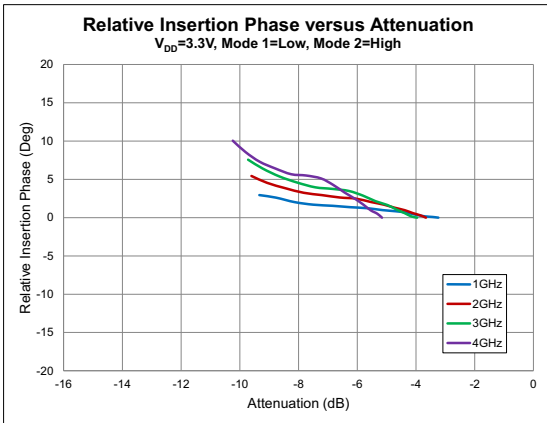
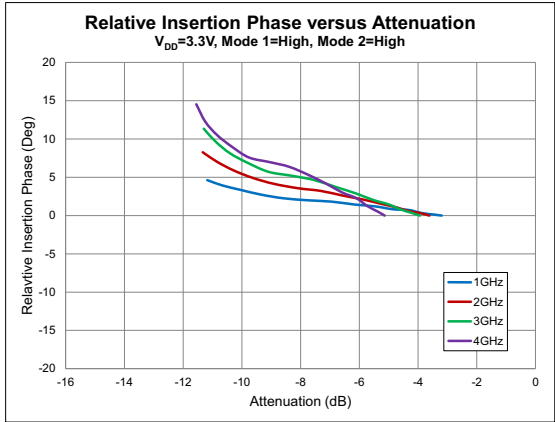
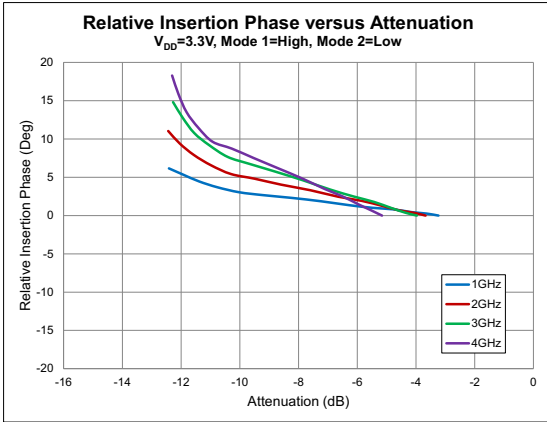
Measured Performance



Measured Performance



Measured Performance



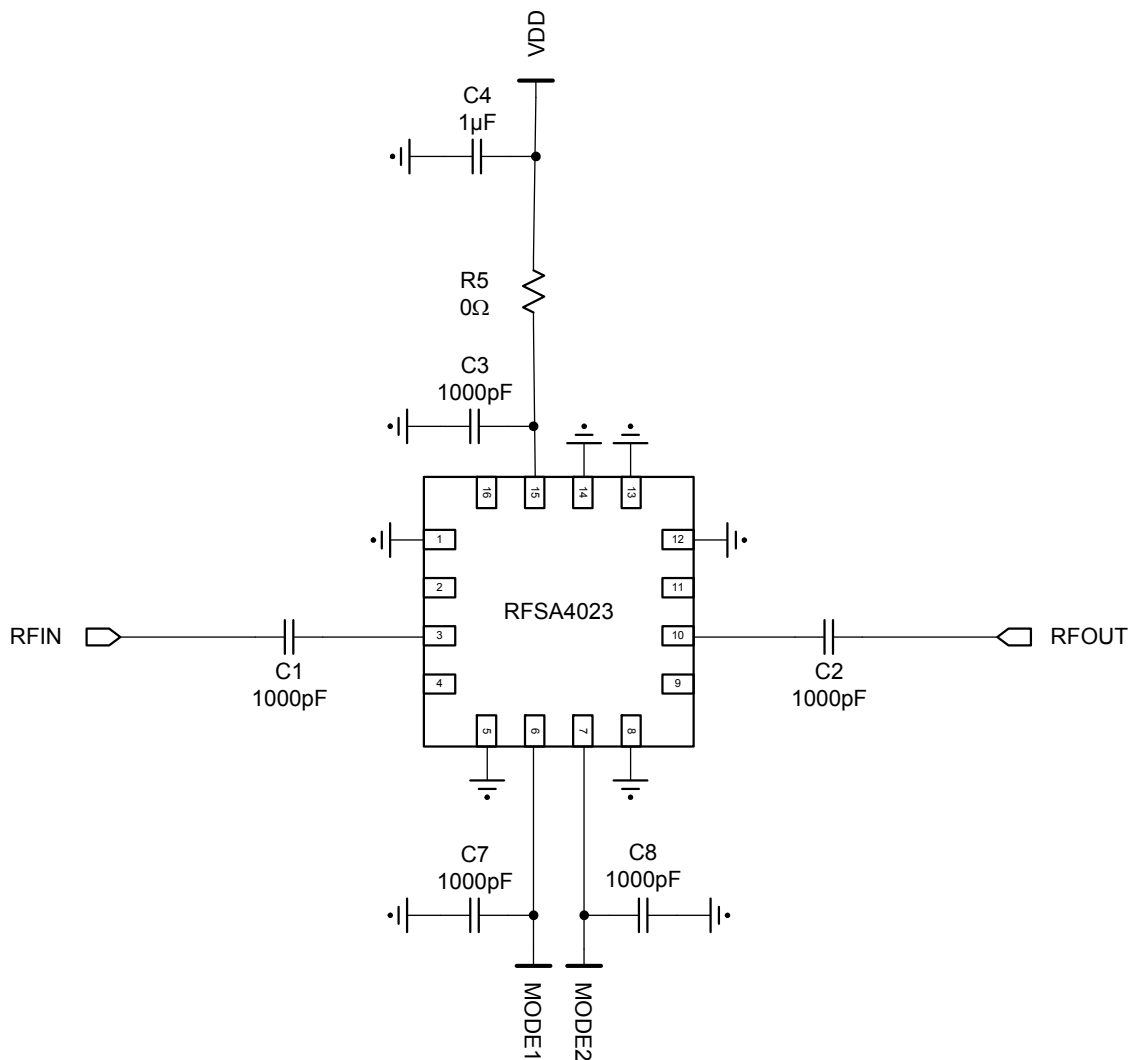
Pin Names and Description

Pin	Function	Description
1	GND	Ground Pin
2	NC	No Connection. Do not Connect to PC Board Ground Plane.
3	RFIN	RF Input. Use External DC Block.
4	NC	No Connection. Do not Connect to PC Board Ground Plane.
5	GND	Ground Pin
6	MODE1	Logic Input for Slope Selection. See Table for Options.
7	MODE2	Logic Input for Slope Selection. See Table for Options.
8	GND	Ground Pin
9	NC	No Connection. Do not Connect to PC Board Ground Plane.
10	RFOUT	RF Output. Use External DC Block.
11	NC	No Connection. Do not Connect to PC Board Ground Plane.
12	GND	Ground Pin
13	GND	Ground Pin
14	GND	Ground Pin
15	VDD	Supply Voltage (3.3V)
16	GND	Ground Pin
GND	GND	Exposed Package Ground Paddle is RF and DC Ground

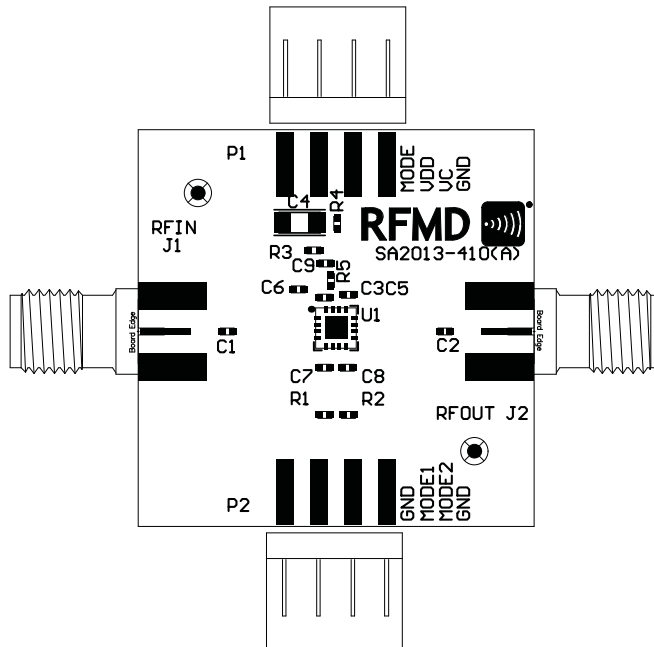
Mode Selection Table

Mode1	Mode2	Slope dB/ ° C	25 ° C Atten (dB)
LOW	HIGH	0.045	5.6
HIGH	HIGH	0.058	6.7
HIGH	LOW	0.066	7.85

Evaluation Board Schematic



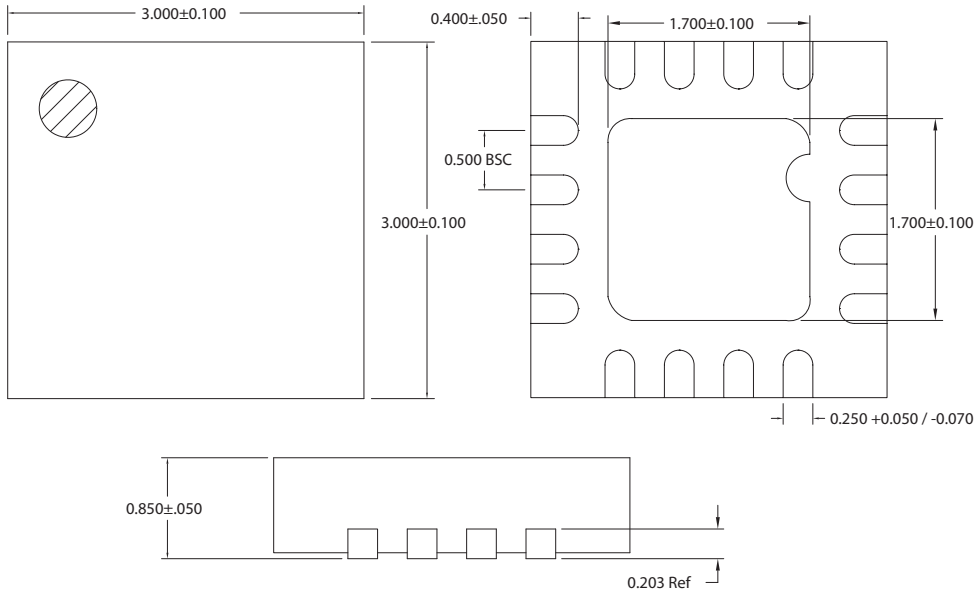
Evaluation Board Assembly Drawing



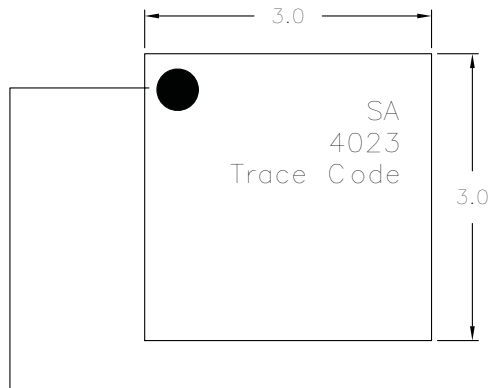
Evaluation Board Build of Materials (BOM)

Description	Reference Designator	Manufacturer	Manufacturer's P/N
Temperature Compensating Attenuator TCA, 3.3V	U1	RFMD	RFSA4023
CONN, SMA, END LNCH, MINI, FLT, 0.068"	J1-J2	Emerson Network Power	142-0741-851
CONN, HDR, ST, 4-PIN, 0.100", T/H	P1-P2	MOLEX	22-28-4043
PCB, SA2013-410		DDI	SA2013-410(A)
CAP, 1000 pF, 10%, 25V, X7R, 0402	C1-C3, C7-C8	Murata Electronics	GRM155R71H102KA01D
CAP, 1 μF, 10%, 16V, X7R, 1206	C4	Murata Electronics	GRM31MR71E105KC01L
RES, 0 Ω, 0402	R5	Kamaya, Inc	RMC1/16S-101JTH
DNP	C5-C6, C9	N/A	N/A
DNP	R1-R4	N/A	N/A

Package Drawing
(0.9mmx3mmx3mm)



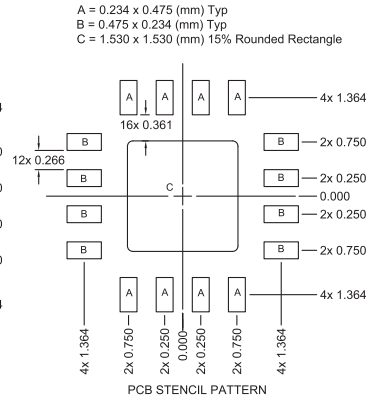
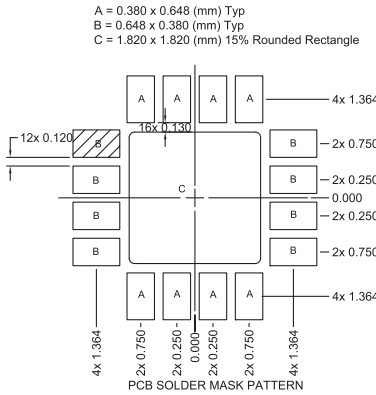
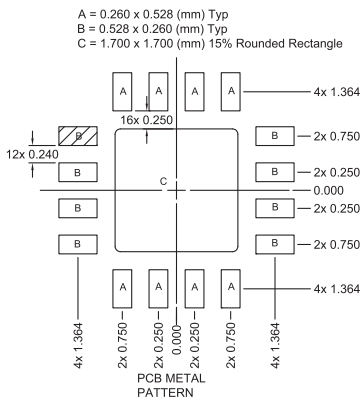
Branding Diagram
(0.9mmx3mmx3mm)



Pin 1 Indicator

Trace Code to be assigned by SubCon

PCB Patterns



Thermal vias for center slug "C" should be incorporated into the PCB design. The number and size of thermal vias will depend on the application, the power dissipation, and the electrical requirements. Example of the number and size of vias can be found on the RFMD evaluation board layout.

Shaded area represents Pin 1 location.