



# **Description**

The IMX7/IMS7 Series of board-mountable 7 Watt DC-DC converters have been designed according to the latest industry requirements and standards. The converters are particularly suitable for use in mobile or stationary applications in transport, railways, industry, or telecom, where variable input voltages or high transient voltages are prevailent.

Covering a total input voltage range from 8.4~V to 150~V with six different models, the converters are available with single and electrically-isolated double outputs from 3.3~up to 48~V with flexible load distribution on double outputs. A shutdown input allows remote on/off.

Features include efficient input and output filtering with good transient and surge protection, low output ripple and noise, consistently high efficiency over the entire input voltage range, and high reliability as well as excellent dynamic response to load and line changes.

The converters provide supplementary insulation with SELV outputs as for instances required in battery-supported

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#### **Features**

- RoHS lead-free-solder and lead-solder-exempted products are available.
- Input voltage ranges up to 150 VDC
- 1 or 2 isolated outputs up to 48 V
- 1200 to 2000 VAC I/O electric strength test
- Extremely wide input voltage ranges
- Immunity according to IEC/EN 61000-4-2, -3, -4, -5, -6
- High efficiency (typ. 84%)
- · Input undervoltage lockout
- · Inhibit input, adjustable output voltages
- · Flex power: flexible load distribution on outputs
- · Outputs no-load, overload, and short-circuit proof
- · High reliability and no derating
- Operating ambient temperature -40 to +85 °C
- · Thermal protection
- · Supplementary insulation
- · Planar technology for best mechanical stability
- 2" × 1" case with 10.5 mm profile

Safety-approved to IEC 60950-1 and UL/CSA 60950-1 2<sup>nd</sup> Edition.





<sup>1</sup>70/110IMX7 models

systems, where the bus voltage may exceed the SELV limit of 60 V. The models 70IMX7 and 110IMX7 are CE-marked.

The circuitry is comprised of integral planar magnetics. All components are automatically assembled and securely soldered onto a single PCB without any wire connections. Magnetic feedback ensures maximum reliability and repeatability in the control loop over all operating conditions. Careful considerations of possible thermal stresses ensure the absence of hot spots, providing long life in environments, where temperature cycles are a reality. The thermal design allows operation at full load up to an ambient temperature of 85 °C in free air without using any potting material. For extremely high vibration environments the case has holes for screw mounting.

Several options, such as open-frame, provide a high level of application-specific engineering and design-in flexibility.

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# **Model Selection**

Table 1: Model Selection

Outp	put 1	Outp	ut 21	Output power	Input voltage	Eff	iciency	Model	Options
V <sub>o1 nom</sub> [V]	I <sub>o1 nom</sub> [A]	V <sub>o2 nom</sub> [V]	I <sub>o2 nom</sub> [A] <sup>1</sup>	P <sub>o nom</sub> [W]	range [V]	η <sub>min</sub> [%]	η <sub>typ</sub> [%]		
3.3	1.5	-	-	5	8.4 – 36	74	78	20IMX7-03-8	Z, G
3.3	1.5	-	-	5	14 – 36	75.5	79	24IMS7-03-9	Z, G
3.3	1.5	-	-	5	16.8 <sup>2</sup> – 75	74.7	79	40IMX7-03-8	Z, G
3.3	1.5	-	-	5	36 – 75	77	79	48IMS7-03-9	Z, G
3.3	1.5	-	-	5	40 – 121	76	78	70IMX7-03-8	Z, G
3.3	1.5	-	-	5	60 – 150 <sup>2</sup>	74.5	78	110IMX7-03-8	Z, G
5.1	1.2	-	-	6.1	8.4 – 36	75.5	78	20IMX7-05-8	<i>M</i> , Z, G
5.1	1.2	-	-	6.1	14 – 36	77	80	24IMS7-05-9	Z, G
5.1	1.2	-	-	6.1	16.8 <sup>2</sup> – 75	79	81	40IMX7-05-8	<i>M</i> , Z, G
5.1	1.2	-	-	6.1	36 – 75	78	81	48IMS7-05-9	Z, G
5.1	1.2	-	-	6.1	40 – 121	75.5	78	70IMX7-05-8	<i>M</i> , Z, G
5.1	1.2	-	-	6.1	60 – 150 <sup>3</sup>	76	79	110IMX7-05-8	<i>M</i> , Z, G
5	0.6	5	0.6	6	8.4 – 36	77	81	20IMX7-05-05-8	<i>M</i> , Z, G
5	0.7	5	0.7	7	14 – 36	79	82	24IMS7-05-05-9	Z, G
5	0.7	5	0.7	7	16.8 <sup>2</sup> – 75	79.5	82	40IMX7-05-05-8	<i>M</i> , Z, G
5	0.7	5	0.7	7	36 – 75	81.5	83	48IMS7-05-05-9	Z, G
5	0.7	5	0.7	7	40 – 121	81	82	70IMX7-05-05-8	M, Z, G
5	0.7	5	0.7	7	60 – 150 <sup>3</sup>	78	80	110IMX7-05-05-8	<i>M</i> , Z, G
12	0.25	12	0.25	6	8.4 – 36	80.5	83	20IMX7-12-12-8	<i>M</i> , Z, G
12	0.3	12	0.3	7.2	14 – 36	81.5	85	24IMS7-12-12-9	Z, G
12	0.3	12	0.3	7.2	16.8 <sup>2</sup> – 75	82.5	84	40IMX7-12-12-8	<i>M</i> , Z, G
12	0.3	12	0.3	7.2	36 – 75	82	84	48IMS7-12-12-9	Z, G
12	0.3	12	0.3	7.2	40 – 121	83	85	70IMX7-12-12-8	M, Z, G
12	0.3	12	0.3	7.2	60 – 150 <sup>3</sup>	80	83	110IMX7-12-12-8	M, Z, G
15	0.2	15	0.2	6	8.4 – 36	81	83	20IMX7-15-15-8	<i>M</i> , Z, G
15	0.24	15	0.24	7.2	14 – 36	81.5	84	24IMS7-15-15-9	Z, G
15	0.24	15	0.24	7.2	16.8 <sup>2</sup> – 75	81	84	40IMX7-15-15-8	<i>M</i> , Z, G
15	0.24	15	0.24	7.2	36 – 75	82	84	48IMS7-15-15-9	Z, G
15	0.24	15	0.24	7.2	40 – 121	81.5	83	70IMX7-15-15-8	<i>M</i> , Z, G
15	0.24	15	0.24	7.2	60 – 150 <sup>3</sup>	79	82	110IMX7-15-15-8	M, Z, G
24	0.13	24	0.13	6.2	8.4 – 36	79	83	20IMX7-24-24-8	M, Z, G
24	0.15	24	0.15	7.2	14 – 36	82	84	24IMS7-24-24-9	Z, G
24	0.15	24	0.15	7.2	16.8 <sup>2</sup> – 75	82	84	40IMX7-24-24-8	<i>M</i> , Z, G
24	0.15	24	0.15	7.2	36 – 75	82	84	48IMS7-24-24-9	Z, G
24	0.15	24	0.15	7.2	40 – 121	80	83	70IMX7-24-24-8	<i>M</i> , Z, G
24	0.15	24	0.15	7.2	60 – 150 <sup>3</sup>	81	84	110IMX7-24-24-8	<i>M</i> , Z, G

<sup>&</sup>lt;sup>1</sup> Flexible load distribution on double-outputs possible. If only one output is used, connect both outputs of double-output models in parallel.

NFND: Not for new designs.

Preferred for new designs

Note: Use 20IMX7 to replace 24IMS7 and 40IMX7 to replace 48IMS7.

<sup>&</sup>lt;sup>2</sup> Operation at low input voltage possible, if  $P_0$  is reduced to approx. 80% of  $P_0$  nom at  $V_{i,min}$  = 14.1 V.

<sup>&</sup>lt;sup>3</sup> Up to 154 V for 2 s



## **Part Number Description**

Input voltag	e range <i>V</i> i	
	8.4 – 36 V	20
	14 – 36 V	24
	16.8 – 75 V	40
	36 – 75 V	48
	40 – 121 V	
	60 – 150 V	110
Series		IMX7, IMS7
Output volta	age of output 1	. 03, 05, 12, 15, 24
Output volta	age of output 2	05, 12, 15, 24
Operating a	imbient temperature $T_A$	
_	–40 to 85 °C	8
	-40 to 71 °C	9
Options:	Surface mount version <sup>1</sup>	M
	Open frame <sup>1</sup>	Z
	RoHS-compliant for all six	substances G
Obsolete o	otion: C-pinout	C

Option M and Z exclude each other.
Note: The sequence of options must follow the order above.

Example: 40IMX7-05-05-8ZG: DC-DC converter, input voltage range 16.8 – 75 V, 2 outputs providing each 5 V, 700 mA, temperature range –40 to 85 °C, open frame, RoHS compliant for all six substances.

# **Functional Description**

The IMX7/IMS7 Series DC-DC converters are feedback-controlled flyback converters using current mode PWM (Pulse Width Modulation).

In the case of single-output models, the output is directly sensed and fed back to the primary control circuit via a pulse transformer, resulting in tight regulation of the output voltage. The R input is referenced to the secondary side and allows for programming the output voltages in the range 75 to 105% of  $V_{\text{o nom}}$ , using either an external resistor or an external voltage source.

For double-output models, the output voltage is sensed by a separate transformer winding close to the secondary and fed

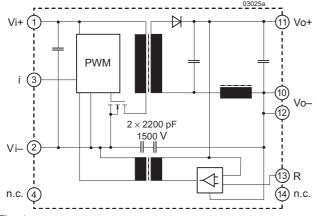
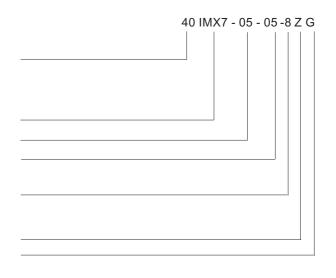


Fig. 1
Block diagram of single-output models



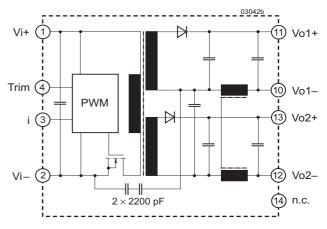


Fig. 2 Block diagram of dual-output models

back to the primary control circuit. Close magnetic coupling is provided by the planar construction, ensuring very good regulation, and allowing for flexible load distribution. The Trim input of double-output models is referenced to the primary side, and allows for programming the output voltages in the range of 100 to 105% of  $V_{\rm 0\ nom}$  by means of an external resistor, or within 75 to 100% of  $V_{\rm 0\ nom}$ , using an external voltage source.

Current limitation is provided by the primary circuit, thus limiting the total output current ( $I_{0\,\mathrm{nom}}$  for the single- and the sum  $I_{01\,\mathrm{nom}}$  +  $I_{02\,\mathrm{nom}}$  for double-output models).

The inhibit input i allows remote control of the outputs; pin i must be connected to Vi- to activate the converter.



# **Electrical Input Data**

General conditions:

- $-T_A$  = 25 °C, unless  $T_C$  is specified.
- Connector pin i connected with Vi-.
- Trim or R input not connected.

Table 2a: Input data of IMX7

Input					2	OIMX	7	4	OIMX	7	7	OIMX	7	11	OIM	<b>(7</b>	
Charac	cteristics		Condit	ions	min	typ	max	min	typ	max	min	typ	max	min	typ	max	Unit
Vi	Input voltage ra	ange <sup>1</sup>	T <sub>C min</sub> –	$_{\text{nin}} - T_{\text{C max}}$ 8.4			36	16.8 <sup>6</sup>		75	40		121	60		150	V
V <sub>i nom</sub>	Nominal input v	roltage	$I_0 = 0 - I$	l <sub>o nom</sub>		20			40			70			110		
V <sub>i sur</sub>	Repetitive surg	e voltage	abs. ma	nax input (3 s)			50 <sup>5</sup>			100		150					
t <sub>start up</sub>	Converter <sup>2</sup>	switch on	Worst c	ase condition at		0.25	0.5		0.25	0.5		0.25	0.5		0.25	0.5	s
	start-up time	inh. release	V <sub>i min</sub> an	d full load			0.1			0.1			0.1			0.1	
t <sub>rise</sub>	Rise time 2		V <sub>i nom</sub>	resistive load		5			5			5			5		ms
			I <sub>o nom</sub>	capacitive load		12			12			12			12		
I <sub>i o</sub>	No load input of	current	$I_0 = 0, V$	i <sub>min</sub> – V <sub>i max</sub>		15	30		8	15		8	15		8	15	mA
Ci	Input capacitar	nce	(for sur	ge calculation)		4.7			0.5			0.2			0.2		μF
V <sub>inh</sub>	Inhibit voltage		converte	er operating	-10		+0.8	-10		+0.8	-10		+0.8	-10		+0.8	V
			converte	er inhibited	2.4		V <sub>i max</sub>	2.4		V <sub>i max</sub>	2.4		V <sub>i max</sub>			V <sub>imax</sub>	
						r oper	1		r oper	1		r opei	1		r ope		
<i>I</i> <sub>inh</sub>	Inhibit current		converte	er operating		-0.5			-0.5			-0.5			-0.5		mA
			converte	er inhibited		1			1			1			1		
I <sub>i inh</sub>	Input current w converter is inf		V <sub>i min</sub> – \	/i max			3			3			3			3	
I <sub>inr p</sub>	Inrush peak cu	ırrent	V <sub>i</sub> = V <sub>i no</sub>	om <sup>4</sup>		3.8			3.7			4.2			5.6		Α
fs	Switching free	quency	V <sub>i min</sub> – V	$I_{\text{imax}}$ , $I_{\text{o}} = 0 - I_{\text{o nom}}$	арі	orox. 4	100	арр	rox. 4	100	арр	rox. 4	100	арр	rox.	300	kHz
I <sub>i rr</sub>	Reflected ripple	e current	$I_0 = 0 - I$	o nom			50			30			20			10	$mA_{pp}$
$V_{iRFI}$	Input RFI level	conducted	EN 5502	223		Α			Α			Α			Α		

<sup>1</sup> V<sub>i min</sub> will not be as stated, if V<sub>o</sub> is increased above V<sub>o nom</sub> by use of the R or Trim input. If the output voltage is set to a higher value, V<sub>i min</sub> will be proportionally increased.

<sup>&</sup>lt;sup>6</sup> Operation at lower input voltage possible:  $P_o$  approx. 80% of  $P_{o \text{ nom}}$  at  $V_{i \text{ min}}$  = 14.4 V.

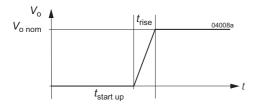


Fig. 3
Converter start-up and rise time

 $<sup>^{\</sup>rm 2}$  Measured with a resistive and the max. admissible capacitive load.

 $<sup>^3</sup>$  Measured with a lead length of 0.1 m, leads twisted. Double-output models with both outputs in parallel. 70/110 IMX7 models need an external capacitor at the input, e.g., 1  $\mu$ F film or ceramic.

 $<sup>^{\</sup>rm 4}$  Source impedance according to ETS 300132-2, version 4.3, at  $\textit{V}_{\textrm{i nom}}$ 

 $<sup>^{\</sup>rm 5}$  The DC-DC converter shuts down automatically at approx. 38 V.



Table 2b: Input data of IMS7; general conditions as in table 2a

Input						24IMS7			48IMS7		
Chara	cteristics		Condit	ions	min	typ	max	min	typ	max	Unit
Vi	Input voltage r	ange <sup>1</sup>	T <sub>C min</sub> –	T <sub>C max</sub>	14		36	36		75	V
V <sub>i nom</sub>	Nominal input	/oltage	$I_0 = 0 -$	$_{\rm o} = 0 - I_{\rm o \ nom}$		24			48		
V <sub>i sur</sub>	Repetitive surg	ge voltage	abs. ma	bs. max input (3 s)			50			100	
t <sub>start up</sub>	Converter <sup>2</sup>	switch on		orst case condition at		0.25	0.5		0.25	0.5	s
	start-up time	inh. release	V <sub>i min</sub> ar	in and full load			0.1			0.1	
trise	Rise time <sup>2</sup>		V <sub>i nom</sub>			5			5		ms
			I <sub>o nom</sub>	capacitive load		12			12		
I <sub>i o</sub>	No load input	current	$I_0 = 0, V$	= 0, V <sub>i min</sub> – V <sub>i max</sub>		12	24		6	12	mA
Ci	Input capacita	nce	(for sur	ge calculation)		0.8			0.5		μF
V <sub>inh</sub>	Inhibit voltage		convert	er operating	-10		+0.8	-10		+0.8	V
			convert	er inhibited	2.4	or open	$V_{\rm imax}$	2.4	or open	$V_{i\text{max}}$	
I <sub>inh</sub>	Inhibit current		convert	er operating		-0.5			-0.5		mA
			convert	er inhibited		1			1		
I <sub>i inh</sub>	Input current v		V <sub>i min</sub> –	V <sub>i max</sub>			3			3	
I <sub>inr p</sub>	Inrush peak cu	ırrent	$V_i = V_{in}$	om <sup>4</sup>		4.0			3.9		Α
fs	Switching free	quency	V <sub>i min</sub> – \	$I_{\text{imax}}$ , $I_{\text{o}} = 0 - I_{\text{o nom}}$		approx. 330			approx. 330		kHz
I <sub>i rr</sub>	Reflected rippl	e current	$I_0 = 0 - 1$	l <sub>o nom</sub>			40			30	mA <sub>pp</sub>
V <sub>i RFI</sub>	Input RFI level	conducted	EN 550	22 <sup>3</sup>		Α			Α		

<sup>&</sup>lt;sup>1</sup>  $V_{i \, min}$  will not be as stated, if  $V_{o}$  is increased above  $V_{o \, nom}$  by use of the R or Trim input. If the output voltage is set to a higher value,  $V_{i \, min}$  will be proportionally increased.

<sup>&</sup>lt;sup>2</sup> Measured with a resistive and the max. admissible capacitive load.

<sup>&</sup>lt;sup>3</sup> Measured with a lead length of 0.1 m, leads twisted. Double-output models with both outputs in parallel. 70/110 IMX7 models need an external capacitor at the input, e.g. 1 µF film or ceramic.

<sup>&</sup>lt;sup>4</sup> Source impedance according to ETS 300132-2, version 4.3, at V<sub>i nom</sub>



## **Input Transient Voltage Protection**

In many applications transient voltages on the input of the converter may occur and are possibly caused by short circuits between Vi+ and Vi-, where the network inductance may cause high energy pulses.

In order to protect the converters, transient suppressors are fitted to the input; see table below:

Table 3: Built-in transient voltage suppressor

Model	Breakdown Voltage V <sub>BR nom</sub>	Peak Power at 1 ms	Peak Pulse Current I <sub>PP</sub>
20IMX71	overvolta	age lockout at ap	prox. 38 V
24IMS7 <sup>2</sup>	53 V	600 W	7.7 A
40IMX7 <sup>2</sup>	100 V	1500 W	11 A
48IMS7 <sup>2</sup>	100 V	600 W	4.1 A
70IMX7 <sup>2</sup>	144 V	600 W	2.9 A
110IMX7 <sup>2</sup>	167 V	600 W	2.5 A

- 1 The built-in overvoltage trigger shuts down the converter at approx. 38 V protecting the input up to 50 V. For higher transient voltages an external suppressor or voltage limiting circuit as, e.g., for IEC/EN 61000-4-5, level 2 compliance should be provided.
- <sup>2</sup> If transients generating currents above the peak pulse current are possible, an external limiting network such as the circuit recommended for IEC/EN 61000-4-5, level 2 compliance, is necessary.

To achieve IEC/EN 61000-4-5, level 2 compliance, an additional inductor and capacitor should be provided externally as shown in the figure below. The components should have similar characteristics as listed in table 4.

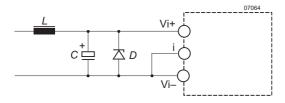


Fig. 4
Example for external circuitry to comply with IEC/EN 61000-4-5, level 2 (transzorb D only for 20IMX7).

Table 4: Components for external circuitry to comply with IEC/EN 61000-4-5

Model	L	С	D
20IMX7	330 μH, 1 A, 0.115 Ω	68 μF, 63 V	1.5KE39A
24IMS7	330 μH, 0.6 A, 0.42 Ω	68 μF, 63 V	n.a.
40IMX7	330 μH, 0.6 A, 0.42 Ω	68 μF, 100 V	n.a.
48IMS7	330 μH, 0.6 A, 0.42 Ω	68 μF, 100 V	n.a.
70IMX7	1000 μH, 0.6 A, 0.42 Ω	68 μF, 100 V	n.a.
110IMX7	330 µH, 0.2 A	200 μF, 200 V	n.a.

**Note:** Suppressor diode D is only necessary for 20IMX7 models. To withstand 150 V transients according to 19 Pfl 1, applicable for 40 IMX7 models, the same external circuitry with similar components as for IEC/EN 61000-4-5, level 2 compliance can be used.

# Input Fuse and Reverse Polarity Protection

The suppressor diode on the input also provides for reverse polarity protection by conducting current in the reverse direction, thus protecting the converter. An external fuse is required to limit this current. We recommend for:

- 20IMX7 and 24IMS7 a fast 2 A (F2A) fuse
- 40IMX7 and 48IMS7 a fast 1 A (F1A) fuse
- 70IMX7 a fast 0.63 A (F.63A) fuse
- 110IMX7 a fast 0.63 A (F.63A) fuse.

#### **Inrush Current**

The inrush current has been kept as low as possible by choosing a very small input capacitance. A series resistor may be inserted in the input line to limit this current further.

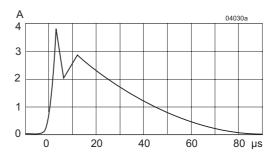


Fig. 5 Typical inrush current at  $V_{i \text{ nom}}$ ,  $P_{o \text{ nom}}$  measured according to ETS 300132-2 (40IMX7).



# **Electrical Output Data**

General conditions:

- $T_A$  = 25 °C, unless  $T_C$  is specified.
- Pin i connected with Vi-; Trim or R input left open-circuit.

Table 5a: Output data for single-output models

Outp	ut		Von	om	3.3 V	1		5.1 V	,	Unit
Chara	acteristics		Conditions	mir	typ	max	min	typ	max	
V <sub>o</sub>	Output vo	Itage setting	V <sub>i nom</sub>	3.28	3	3.32	5.07		5.13	V
I <sub>o nom</sub>	Output cu	rrent (nom.)	V <sub>i min</sub> – V <sub>i max</sub>		1.5			1.2		Α
I <sub>o L</sub>	Current lim	nit <sup>2</sup>	V <sub>i nom</sub>	1.78	3	2.7	1.56		2.0	
ΔV <sub>o</sub>	Line and lo	oad regulation	$V_{\text{i min}} - V_{\text{i max}}$ $I_{\text{o}} = (0.05 - 1) I_{\text{o not}}$	m		±1			±1	%
V <sub>o</sub>	Output vo	Itage noise	V <sub>i min</sub> – V <sub>i max</sub>	5		70			70	$mV_{pp}$
		other models	$I_{\rm o} = I_{\rm o nom}$	6	20	40		20	40	
		110IMX7			40	50		40	50	
V <sub>o clp</sub>	Output ove	ervoltage limit 7	Minimum load 1%	115		130	115		130	%
C <sub>o ext</sub>	Admissible	e capacitive load				2500			2000	μF
V <sub>od</sub>	Dynamic	Voltage deviat.	V <sub>i nom</sub>		±250			±250		mV
$t_{\sf d}$	load regulation	Recovery time	$I_{\text{o nom}} \leftrightarrow {}^{1}/_{2} I_{\text{o nom}}$		1			1		ms
$\alpha_{Vo}$	•	ure coefficient $T_{C \text{ min}}$ to $T_{C \text{ max}}$ )	$V_{\text{i min}} - V_{\text{i max}}$ $I_{\text{o}} = (0.05 - 1) I_{\text{o not}}$	m	±0.02	)		±0.02	!	%/K

Table 5b: Output data for double-output models

Outpu	ut		V <sub>onom</sub>		2 × 5 \	V	2	× 12	V	2	×15	V	2	× 24 \	V	Unit
Chara	cteristics	i	Conditions	min	typ	max	min	typ	max	min	typ	max	min	typ	max	
V <sub>o1</sub> V <sub>o2</sub>	Output vo setting	Itage IMX7	$V_{\text{i nom}}$ $I_{\text{o1}} = I_{\text{o2}} = 0.5 I_{\text{o nom}}$	4.96 4.95		5.04 5.05	11.90 11.88			14.88 14.85		15.12 15.15	23.81 23.75		24.19 24.25	V
V <sub>o1</sub> V <sub>o2</sub>		IMS7	$V_{\text{i nom}}$ $I_{\text{o1}} = I_{\text{o2}} = 0.5 I_{\text{o nom}}$	4.92 4.92		5.08 5.08	11.82 11.82			14.78 14.78			23.64 23.64		24.36 24.36	
I <sub>o nom</sub>	Output cu	rrent 20IMX	V <sub>i min</sub> – V <sub>i max</sub>		2 × 0.6	6	2	× 0.2	5	2	2 × 0.2	2	2	× 0.13	3	Α
	(nom.) <sup>1</sup>	other models	5		2 × 0.	7	2	× 0.3	3	2	× 0.2	4	2	× 0.1	5	
I <sub>o L</sub>	Current	20IMX7	V <sub>i nom</sub>	1.8		2.1	0.7		0.9	0.5		0.7	0.35		0.45	
	limit <sup>2 3</sup>	other models	5	1.8		2.0	0.8		1.0	0.55		0.9	0.38		0.5	
$\Delta V_{o1}$	Line regul	ation	$V_{\text{i min}} - V_{\text{i max}}, I_{\text{o nom}}$			±1			±1			±1			±1	%
ΔV <sub>ol</sub>	Load regu	lation <sup>4</sup>	$V_{\text{i nom}}$ , $(0.1-1) I_{\text{o nom}}$			±3			±3			±3			±3	
V <sub>01, 2</sub>	Output vo	Itage noise	$V_{\text{i min}} - V_{\text{i ma}}$ 5			80			120			150			240	$mV_{pp}$
			$I_{\rm o} = I_{\rm o  nom}$		20	40		25	50		30	60		50	100	
V <sub>o clp</sub>	Output ove	ervoltage limit 7	Minimum load 1%	115		130	115		130	115		130	115		130	%
C <sub>o ext</sub>	Admissible	e capacitive load	1			2000			300			200			100	μF
V <sub>o d</sub>	Dynamic	Voltage deviat.	V <sub>i nom</sub>		±150			±330			±350			±600		mV
t <sub>d</sub>	load Recovery time regulation		$I_{\text{o nom}} \leftrightarrow {}^{1}I_{2} I_{\text{o nom}}$		1			1			1			1		ms
$\alpha_{Vo}$	Temperation of output	ure coefficient voltage	$V_{i  min} - V_{i  max}$ (0.05 - 1) $I_{o  nom}$		±0.02		=	±0.02			±0.02			±0.02		%/K

<sup>&</sup>lt;sup>1</sup> Each output is capable of delivering full output power  $P_{o \text{ nom}}$  according to table *Model Selection*.

<sup>&</sup>lt;sup>2</sup> The current limit is primary side controlled.  $I_{0 L}$  is defined when  $V_{0}$  dropped to 85 to 94%.

<sup>&</sup>lt;sup>3</sup> Measured with both outputs connected in parallel.

<sup>&</sup>lt;sup>4</sup> Conditions for specified output. Other output loaded with constant current  $I_0$  = 0.5  $I_{0 \text{ nom}}$ .

<sup>&</sup>lt;sup>5</sup> BW = 20 MHz

<sup>&</sup>lt;sup>6</sup> Measured with a probe according to EN 61204.

<sup>&</sup>lt;sup>7</sup> The overvoltage protection is not tracking with the R control.



#### **Thermal Considerations**

If the converter, mounted on a PCB, is located in free, quasistationary air (convection cooling) at the maximum ambient temperature  $T_{\rm A\,max}$  (see table Temperature specifications) and is operated at nominal input voltage and output power, the case temperature  $T_{\rm C}$  measured at the measuring point of case temperature (see Mechanical Data) will approach  $T_{\rm C\,max}$  after the warm-up phase. However, the relationship between  $T_{\rm A}$  and  $T_{\rm C}$  depends heavily on the conditions of operation and integration into a system, such as input voltage, output current, airflow, temperature of surrounding components and surfaces, and the properties of the printed circuit board.  $T_{\rm A\,max}$  is therefore only an indicative value, and under practical operating conditions,  $T_{\rm A}$  may be higher or lower.

**Caution:** The case temperature  $T_{\rm C}$  measured at the measuring point of case temperature (see *Mechanical Data*) may under no circumstances exceed the specified maximum. The installer must ensure that under all operating conditions  $T_{\rm C}$  remains within the limits stated in the table *Temperature specifications*.

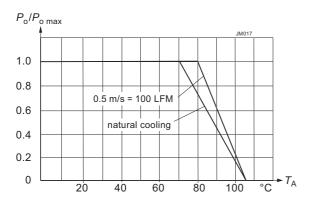


Fig. 6
Maximum allowed output power versus ambient temperature

## **Short-Circuit Behavior**

The current limitation shuts down the converter, when a short circuit is applied to the output. It acts self-protecting, and automatically recovers after removal of the overload condition.

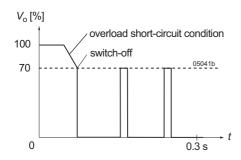


Fig. 7
Overload switch-off (hiccup mode).

#### **Output Overvoltage Protection**

The outputs are protected against overvoltage by Zener diodes. In the event of an overvoltage, the converter will shut down and attempt to restart automatically. The main purpose of this feature is to protect against possible overvoltage, which could occur due to a failure in the feedback control circuit. The converters are not designed to withstand external overvoltages applied to the outputs.

## **Connection in Series or Parallel**

The outputs of single- or double-output models can be connected in series without any precautions, taking into consideration that the output voltage should remain below 60 V for SELV operation.

Both outputs of double-output models can be connected in parallel without any precautions. Several converters (single-or double-output models) with equal output voltage can be put in parallel and will share their output current quite equally. However, this may cause start-up problems and is only recommended in applications, where one converter is able to deliver the full load current, e.g., in true redundant systems.

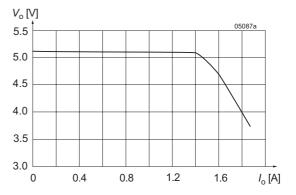


Fig. 8a  $V_o$  versus  $I_o$  (typ.) of single-output models with  $V_o = 5.1 \text{ V}$ 

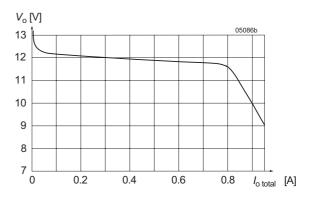


Fig. 8b  $V_o$  versus  $I_o$  (typ.) of double-output models (2 ×12 V) with both outputs in parallel



# **Typical Performance Curves**

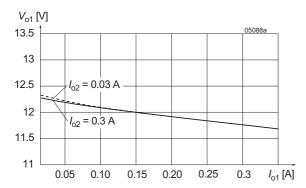
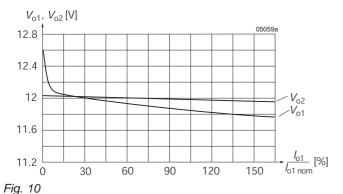


Fig. 9
Cross load regulation (typ.) of double-output models (2×12 V). The cross-load effect is negligible.



Flexible load distribution on double-outputs models (2  $\times$ 12 V) with load variation from 0 to 150% of  $P_{\rm o1\ nom}$  on output 1. Output 2 loaded with 25% of  $P_{\rm o2\ nom}$ .

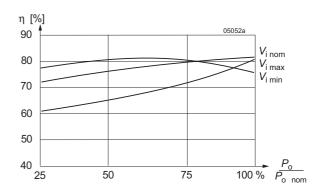


Fig. 11
Efficiency versus input voltage and load.
Typical values (40IMX7-12-12-8).

# **Auxiliary Functions**

## **Inhibit Function**

The output(s) of the converter may be enabled or disabled by means of a logic signal (TTL, CMOS, etc.) applied to the inhibit pin. No output voltage overshoot will occur, when the converter is turned on. If the inhibit function is not required, the inhibit pin should be connected to Vi– to enable the output (active low logic, fail safe).

Converter operating: -10 V to 0.8 V

Converter inhibited

or inhibit pin i left open:  $2.4\,\mathrm{V}\,\mathrm{to}~V_{\mathrm{i}\,\mathrm{max}}$  (20/401MX7,

24/48IMS7)

2.4 V to 75 V (70/110 IMX7)

# **Adjustable Output Voltage**

As a standard feature, all IMX7 and IMS7 converters offer adjustable output voltage(s) by using a control pin. If this pin is left open-circuit, the output voltage is set to  $V_{\rm o\ nom}$ . The output voltage is adjustable in the range of 75 to 105% of  $V_{\rm o\ nom}$ . The circuit works for single- and double-output models in a different way. For output voltages  $V_{\rm o\ nom}$ , the minimum input voltage  $V_{\rm i\ min}$  (see *Electrical Input Data*) increases proportionally to  $V_{\rm o\ No\ nom}$ .



#### Single-output models with R-input:

The R-input (pin 13) is referenced to the secondary side of the converter. Adjustment of the output voltage is possible by means of either an external resistor or a voltage source.

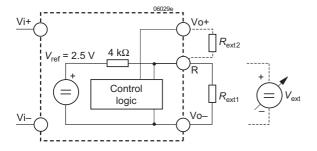


Fig. 12
Output voltage adjust for single-output models

a) Adjustment by means of an external resistor:
 Depending upon the value of the required output voltage,
 a resistor R<sub>ext1</sub> or R<sub>ext2</sub> shall be connected as shown in the figure below:

**Either:**  $R_{\text{ext1}}$  between the R pin and Vo– to achieve an output voltage adjustment range of  $V_0 = 75$  to 100% of  $V_{0 \text{ nom}}$  (85 to 100% for 3.3 V outputs):

$$R_{\text{ext1}} \approx 4 \text{ k}\Omega \cdot \frac{V_0}{V_{0 \text{ nom}} - V_0}$$

**or:**  $R_{\text{ext2}}$  between the R pin and Vo+ to achieve an output voltage range of approximately  $V_0 = 100$  to 105% of  $V_{0 \text{ nom}}$ :

$$R_{\text{ext2}} \approx 4 \text{ k}\Omega \cdot \frac{(V_{\text{o}} - 2.5 \text{ V})}{2.5 \text{ V} \cdot (V_{\text{o}}/V_{\text{o nom}} - 1)}$$

b) Adjustment by means of an external voltage  $V_{\rm ext}$  between Vo– and R pin.

The control voltage is 1.96 to 2.62 V and allows for adjustment in the range of 75 to 105% of  $V_{o\,\text{nom}}$ .

$$V_{\text{ext}} \approx \frac{V_{\text{o}} \cdot 2.5 \text{ V}}{V_{\text{o nom}}}$$

Caution: Applying an external voltage >2.75 V may damage the converter.

**Note:** Attempting to adjust the output below this range will cause the converter to shut down (hiccup mode).

#### Double-output models with Trim input:

The Trim input (pin 4) of double-output models is referenced to the primary side. The figure 15 shows the circuitry. Adjustment of the output voltage is possible in the range of 100 to 105% of  $V_{\rm 0\ nom}$  using an external resistor, or in the range of 75 to 105% of  $V_{\rm 0\ nom}$  using an external voltage source.

a) Adjustment by means of an external resistor  $R_{\rm ext}$ . Programming of the output voltage by means of an external resistor  $R_{\rm ext1}$  is possible within a limited range of 100 to 105%  $V_{\rm o\,nom}$ .  $R_{\rm ext}$  should be connected between pin 4 and Vi—. The following table indicates suitable resistor values for typical output voltages under nominal conditions ( $V_{\rm i\,nom}$ ,  $I_{\rm o}$  = 0.5  $I_{\rm o\,nom}$ ), with paralleled outputs or equal load conditions on each output.

**Caution:** Connection of  $R_{\text{ext}}$  to Vi+ may damage the converter.

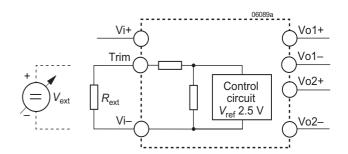


Fig. 13
Output voltage adjust for double-output models

Table 6:  $R_{\text{ext}}$  for  $V_0 > V_{0 \text{ nom}}$ approximate values ( $V_{1 \text{ nom}}$ ,  $I_{01} = I_{02} = 0.5 I_{01/2 \text{ nom}}$ )

V <sub>o</sub> [% V <sub>onom</sub> ]	R <sub>ext</sub> [kΩ]
105 to 108 (107 typically)	0
105	1.5
104	5.6
103	12
102	27
101	68
100	∞

Note: Applying a control voltage greater than 20 V will set the converter into a hiccup mode.

b) Adjustment by means of an external voltage source  $V_{\rm ext}$ . For external output voltages in the range of 75 to 105% of  $V_{\rm o\;nom}$  a voltage source  $V_{\rm ext}$  (0 to 20 V) is required, connected to the Trim-input (pin 4) and Vi—. The table below indicates typical  $V_{\rm o}$  versus  $V_{\rm ext}$  values under nominal conditions ( $V_{\rm i\;nom}$ ,  $I_{\rm o}$  = 0.5  $I_{\rm o\;nom}$ ), with paralleled outputs or equal load conditions on each output. Direct paralleling of the Trim-inputs of converters connected in parallel is feasible.

Table 7:  $V_{\text{ext}}$  for  $V_{\text{o}} = 75$  to 105%  $V_{\text{o nom}}$ ; typical values ( $V_{\text{i nom}}$ ,  $I_{\text{o1}} = I_{\text{o2}} = 0.5 I_{\text{o1/2 nom}}$ )

V <sub>o</sub> [% V <sub>o nom</sub> ]	V <sub>ext</sub> [V]
≥105	0
102	1.6
95	4.5
85	9
75	13



# **Electromagnetic Compatibility (EMC)**

A suppressor diode together with an input filter form an effective protection against high input transient voltages,

which typically occur in many installations, but especially in battery-driven mobile applications.

# **Electromagnetic Immunity**

Table 8: Immunity type tests

Phenomenon	Standard	Class level	Coupling mode <sup>1</sup>	Value applied	Waveform	Source imped.	Test procedure	In oper.	Perf- crit. <sup>2</sup>
Electrostatic discharge to case <sup>5</sup>	IEC/EN 61000-4-2	3	contact discharge	4000 V <sub>p</sub> 8000 V <sub>p</sub>	1/50 ns	330 Ω	10 positive and 10 negative discharges	yes	В
Electromagnetic field	IEC/EN	3 <sup>6</sup> 2 <sup>3</sup>	antenna	10 V/m 3 V/m <sup>3</sup>	AM 80% 1 kHz	n.a.	80 – 1000 MHz	yes	Α
	ENV 50204	3 2 <sup>3</sup>	antenna	10 V/m 3 V/m <sup>3</sup>	PM, 50% duty cycle, 200 Hz repetition frequ.	n.a.	900 MHz	yes	Α
Electrical fast transients/burst	IEC/EN 61000-4-4	4 3 <sup>3</sup>	direct +i/–i	4000 V <sub>p</sub> 2000 V <sub>p</sub> <sup>3</sup>	bursts of 5/50 ns 5 kHz repet. rate, transients with 15 ms burst duration, and 300 ms period	50 Ω	60 s positive, 60 s negative transients per coupling mode	yes	В
Surges	IEC/EN 61000-4-5	3 <sup>4</sup> 2 <sup>34</sup>	+i/_i	2000 V <sub>p</sub> 1000 V <sub>p</sub> <sup>3</sup>	1.2/50 µs	2 Ω	5 pos. and 5 neg. surges	yes	В
RF conducted immunity	IEC/EN 61000-4-6	3 2 <sup>3</sup>	+i/ <u>—</u> i	10 VAC 3 VAC	AM modul. 80 % 1 kHz	50 Ω	0.15 to 80 MHz 150 Ω	yes	Α

<sup>&</sup>lt;sup>1</sup> i = input, o = output

## **Electromagnetic Emissions**

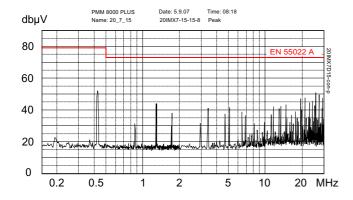


Fig. 14a 20IMX7-15-15-8: Typical conducted emissions (peak) at the input at  $V_{i \text{ nom}}$  and  $I_{o \text{ nom}}$  according to EN 55011/22. Output leads 0.1 m, twisted. External input capacitor (1  $\mu$ F ceramic + 100  $\mu$ F electrolytic cap)

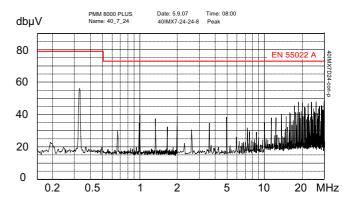


Fig. 14b 40IMX7-24-24-8: Typical conducted emissions (peak) at the input at  $V_{i \text{ nom}}$  and  $I_{o \text{ nom}}$  according to EN 55011/22. Output leads 0.1 m, twisted. External input capacitor (1  $\mu$ F ceramic + 47  $\mu$ F electrolytic cap)

<sup>&</sup>lt;sup>2</sup> Performance criterion: A = normal operation, no deviation from specifications, B = temporary deviation from specs possible

<sup>&</sup>lt;sup>3</sup> Valid for 24 IMS7 and 48 IMS7

<sup>&</sup>lt;sup>4</sup> External components required

 $<sup>^{\</sup>rm 5}\,$  R or Trim pin open, i pin connected with Vi–

<sup>&</sup>lt;sup>6</sup> Corresponds to the railway standard EN 50121-3-2:2000, table 9.1

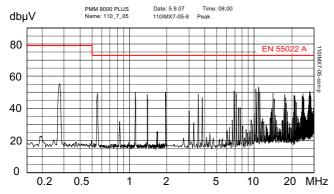


Fig. 14c

110IMX7-05-8: Typical conducted emissions (peak) at the input at  $V_{\rm i\,nom}$  and  $I_{\rm o\,nom}$  according to EN 55011/22. Output leads 0.1 m, twisted. External input capacitor (0.15  $\mu F$  ceramic + 4.7  $\mu F$  electrolytic cap).

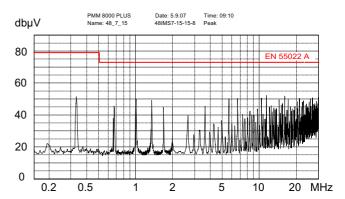


Fig. 15

48IMS7-15-15-8: Typical conducted emissions (peak) at the input at  $V_{\rm i\;nom}$  and  $I_{\rm o\;nom}$  according to EN 55011/22. Output leads 0.1 m, twisted. External input capacitor (1  $\mu$ F ceramic + 47  $\mu$ F electrolytic cap).

# **Immunity to Environmental Conditions**

Table 9: Mechanical and climatic stress

Test	Method	Standard	Test conditions	Status	
Cab	Damp heat steady state	IEC/EN 60068-2-78 MIL-STD-810D section 507.2	Temperature: Relative humidity: Duration:	40 <sup>±2</sup> °C 93 <sup>+2/-3</sup> % 56 days	Converter not operating
Ea	Shock (half-sinusoidal)	IEC/EN 60068-2-27 <sup>1</sup> MIL-STD-810D section 516.3	Acceleration amplitude: Bump duration: Number of bumps:	100 $g_n = 981 \text{ m/s}^2$ 6 ms 18 (3 each direction)	Converter operating
Eb	Bump (half-sinusoidal)	IEC/EN 60068-2-29 MIL-STD-810D section 516.3	Acceleration amplitude: Bump duration: Number of bumps:	$40 g_n = 392 \text{ m/s}^2$ 6 ms 6000 (1000 each direction)	Converter operating
Fc	Vibration (sinusoidal)	IEC/EN 60068-2-6	Acceleration amplitude: Frequency (1 Oct/min): Test duration:	0.35 mm (10 to 60 Hz) $5 g_n = 49 \text{ m/s}^2 (60 \text{ to } 2000 \text{ Hz})$ 10 to 2000 Hz 7.5 h (2.5 h each axis)	Converter operating
Fh	Vibration, broad-band random (digital control)	IEC/EN 60068-2-64	Acceleration spectral density: Frequency band: Acceleration magnitude: Test duration:	0.05 g <sub>n</sub> <sup>2</sup> /Hz 10 to 500 Hz 4.9 g <sub>n rms</sub> 3 h (1 h each axis)	Converter operating
Kb	Salt mist, cyclic (sodium chloride NaCl solution)	IEC/EN 60068-2-52	Concentration: Duration: Storage: Storage duration: Number of cycles:	5% (30 °C) 2 h per cycle 40 °C, 93% rel. humidity 22 h per cycle 3	Converter not operating

<sup>&</sup>lt;sup>1</sup> Covers also EN 50155/EN 61373 category 1, class B, body mounted (= chassis of coach)

# **Temperatures**

Table 10: Temperature specifications, valid for air pressure of 800 to 1200 hPa (800 to 1200 mbar)

Temperature			-	9	-8		Unit
Characteristics		Conditions	min	max	min	max	
$T_{A}$	Ambient temperature	Operational <sup>1</sup>	-40	71	-40	85	°C
$T_{C}$	Case temperature		-40	95	-40	105	
Ts	Storage temperature	Non operational	<b>–</b> 55	100	<b>–</b> 55	105	

<sup>&</sup>lt;sup>1</sup> See Thermal Considerations



#### **Failure Rates**

Table 11: MTBF and device hours

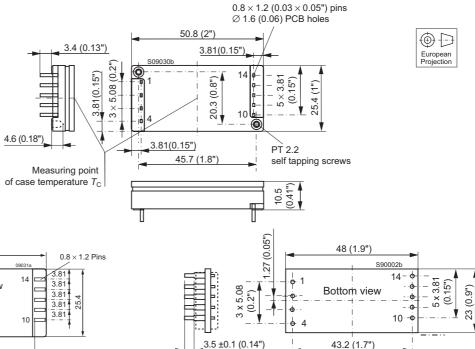
Model	Standard	Ground benign	Ground fixed		Ground mobile	Unit
		T <sub>C</sub> = 40 °C	<i>T</i> <sub>C</sub> = 40 °C	<i>T</i> <sub>C</sub> = 70 °C	<i>T</i> <sub>C</sub> = 50 °C	
24IMS7-05-9	MIL-HDBK-217F	634 000	321 000	188 000	271 000	h
40IMX7-12-12-8	MIL-HDBK-217F	851 000	395 000	253 000	342 000	
	Bellcore	3 019 000	1 510 000	809 000	409 000	
40IMX7-05-05-8	Bellcore	3 320 000	1 660 000	871 000	447 000	

# **Mechanical Data**

Dimensions in mm (inches). Tolerances ±0.3 mm, unless otherwise noted.

Weight: <20 g

Fig. 16
Case IMX7/IMS7 with standard pinout



50.8

0.8 × 1.2 Pin

00031a

3.81

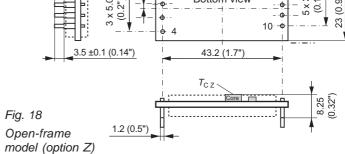
3.81

3.81

42.4

Measuring point case temper.  $T_{\rm C}$ 

Fig. 17
SMD pins (option M)



Safety and Installation Instructions

# **Pin Allocation**

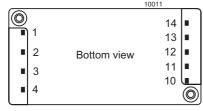


Fig. 19 Footprint

Table 12: Pin allocation

Pin	Single output	Double output
1	Vi+	Vi+
2	Vi–	Vi–
3	i	i
4	n.c.	Trim
10	Vo-	Vo1–
11	Vo+	Vo1+
12	Vo-	Vo2-
13	R	Vo2+
14	n.c.	n.c.



#### Installation Instruction

Installation of the converters must strictly follow the national safety regulations in compliance with the enclosure, mounting, creepage, clearance, casualty, markings, and segregation requirements of the end-use application.

Connection to the system shall be made via a printed circuit board; see *Mechanical Data*.

The converters should be connected to a secondary circuit.

Do not open the converter.

Ensure that a converter failure does not result in a hazardous conditions.

To prevent excessive current flowing through the input lines in case of a short-circuit, an external fuse specified in section *Input Fuse and Reverse Polarity Protection* should be installed in the non-earthed input supply line.

# **Standards and Approvals**

The converters are approved according to UL/CSA 60950-1 and IEC 60950-1 2<sup>nd</sup> Edition.

The converters have been evaluated for:

- Building-in
- Basic insulation input to output, based on their maximum input voltage
- · Pollution degree 2 environment
- Connecting the input to a secondary circuit, which is subject to a maximum transient rating of
  - 1500 V for 20IMX7, 24IMS7, 40IMX7, 48IMS7
  - 2000 V for 70IMX7
  - 2500 V for 110IMX7.

The converters are subject to manufacturing surveillance in accordance with the above mentioned standards.

# **Railway Applications**

To comply with railway standards, all components are coated

with a protective lacquer (except option Z).

# **Protection Degree**

The protection degree of the converters is IP 30, except openframe models (option Z).

# **Cleaning Liquids**

Iln order to avoid possible damage, any penetration of cleaning fluids should be prevented, since the power supplies are not hermetical sealed.

However, open-frame models (option Z) leave the factory unlacquered; they may be lacquered by the customer, for instance together with the mother board. Cleaning agents are not permitted — except washing at room temperature with isopropyl alcohol. If necessary, the mother board must be cleaned, before fitting the open-frame converter.

**Note:** Cleaning liquids may damage the adhesive joints of the ferrite cores.

#### Isolation

The electric strength test is performed in the factory as a routine test in accordance with EN 50116 and IEC/EN 60950, and should not be repeated in the field. Power-One will not honor any warranty claims resulting from electric strength field tests.

Table 12: Electric strength test voltages

Characteristic	Inp	o/o¹	Unit		
	20/40IMX 24/48IMS	70IMX	110IMX		
Factory test >1 s	1.2	1.5	2.0	0.1	kVAC
Equivalent DC volt.	1.5	2.0	2.5	0.15	kVDC
Coupling capacitance	1.2	1.2	1.2	-	nF
Insulation resist. at 500 VDC	>100	>100	>100	-	ΜΩ

<sup>&</sup>lt;sup>1</sup> Between the outputs of double-output models

# **Description of Options**

**Option M: SMD pins** 

This option allows surface mounting of the converters.

Option Z: Open Frame

For applications, where the protection by a housing is not

necessary or in the case that the motherboard should be lacquered after fitting the converter; see *Cleaning Liquids*.

## **Option G: RoHS-6**

Converters with a type designation ending by G are RoHS-compliant for all six substances.

NUCLEAR AND MEDICAL APPLICATIONS - Power-One products are not designed, intended for use in, or authorized for use as critical components in life support systems, equipment used in hazardous environments, or nuclear control systems without the express written consent of the respective divisional president of Power-One, Inc.

TECHNICAL REVISIONS - The appearance of products, including safety agency certifications pictured on labels, may change depending on the date manufactured. Specifications are subject to change without notice.