



20V N-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- Ultra-Low Package Profile, 0.4mm Maximum Package Height
- ESD Protected up to 1.5kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 standards for High Reliability

Mechanical Data

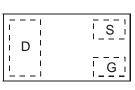
- Case: X2-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe; Solderable per MIL-STD-202, Method 208@4
- Weight: 0.001 grams (Approximate)

X2-DFN1006-3

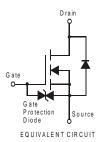




BOTTOM VIEW



TOP VIEW Package Pin Configuration



Ordering Information (Note 4)

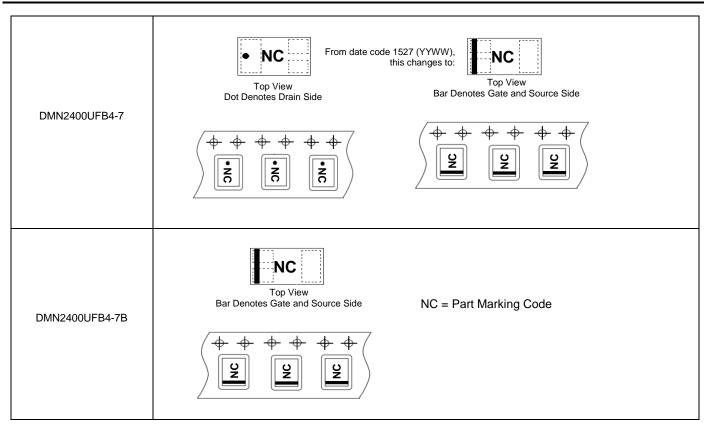
Part Number	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DMN2400UFB4-7	NC	7	8	3,000
DMN2400UFB4-7B	NC	7	8	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.



Marking Information



Maximum Ratings @T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage			V_{DSS}	20	V
Gate-Source Voltage			V_{GSS}	±12	V
Continuous Drain Current (Note 5) V _{GS} = 4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +85^{\circ}C$	I _D	0.75 0.55	А
Pulsed Drain Current (Notes 5 & 6)			I _{DM}	3	Α

Thermal Characteristics @T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P_{D}	0.47	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	258	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

Notes: 5. Device mounted on FR-4 PCB, with minimum recommended pad layout, single sided.

^{6.} Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.

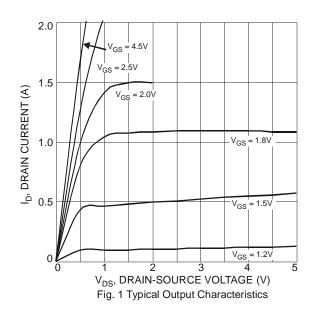


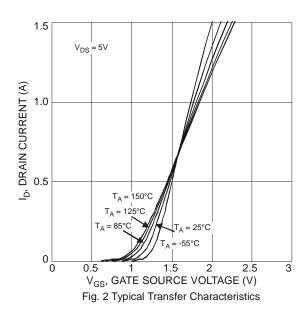
Electrical Characteristics @T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	_	100 50	nA	$V_{DS} = 20V, V_{GS} = 0V$ $V_{DS} = 5V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 3V$, $V_{DS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±1.0	μΑ	$V_{GS} = \pm 4.5V, V_{DS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±50	μΑ	$V_{GS} = \pm 10V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)						•	
Gate Threshold Voltage	$V_{GS(th)}$	0.5	_	0.9	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
			_	0.55		$V_{GS} = 4.5V, I_D = 600mA$	
Static Drain-Source On-Resistance	R _{DS} (ON)	_	_	0.75	Ω	$V_{GS} = 2.5V, I_D = 500mA$	
		_	_	0.9		$V_{GS} = 1.8V, I_D = 350mA$	
Forward Transfer Admittance	Y _{fs}	_	1.0	_	S	$V_{DS} = 10V, I_D = 400mA$	
Diode Forward Voltage (Note 7)	V _{SD}		0.7	1.2	V	$V_{GS} = 0V, I_{S} = 150mA$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	I	36.0	_	pF	$V_{DS} = 16V, V_{GS} = 0V,$ f = 1.0MHz	
Output Capacitance	Coss		5.7	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	4.2	_	pF		
Total Gate Charge	Qg	_	0.5	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$ $ID = 250mA$	
Gate-Source Charge	Q _{gs}	_	0.07	_	nC		
Gate-Drain Charge	Q _{qd}	_	0.1	_	nC		
Turn-On Delay Time	t _{D(on)}	_	4.11	_	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$ $R_{L} = 47\Omega, R_{G} = 10\Omega,$ $I_{D} = 200 \text{mA}$	
Turn-On Rise Time	t _r	_	3.82	_	ns		
Turn-Off Delay Time	t _{D(off)}		14.8	_	ns		
Turn-Off Fall Time	t _f		9.6	_	ns		

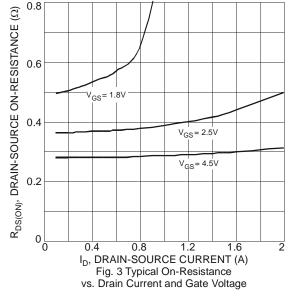
Notes:

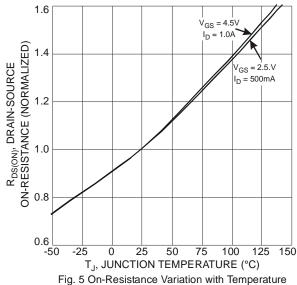
- 7. Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.











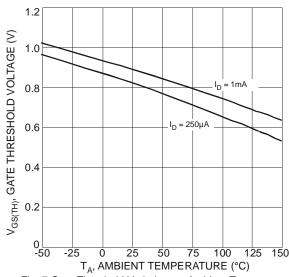


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

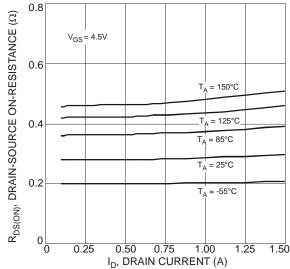


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

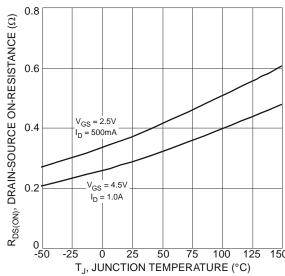


Fig. 6 On-Resistance Variation with Temperature

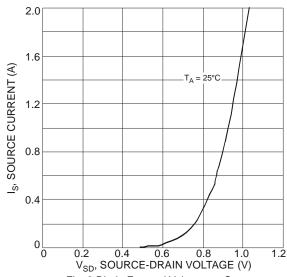
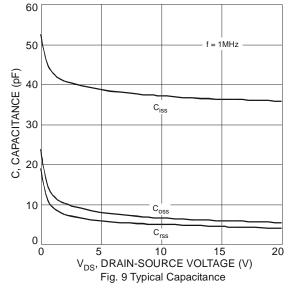


Fig. 8 Diode Forward Voltage vs. Current





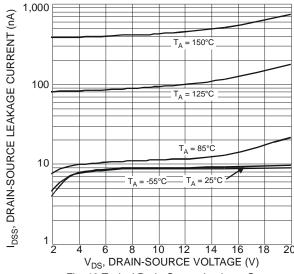
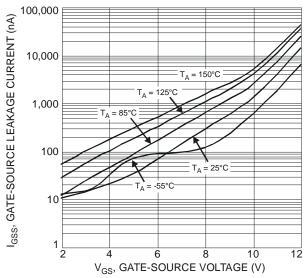


Fig. 10 Typical Drain-Source Leakage Current vs. Drain-Source Voltage



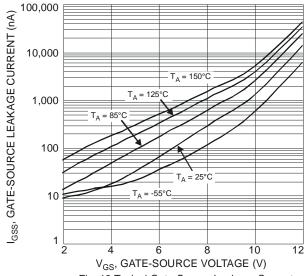


Fig. 11 Typical Gate-Source Leakage Current vs. Gate-Source Voltage

Fig. 12 Typical Gate-Source Leakage Current vs. Gate-Source Voltage

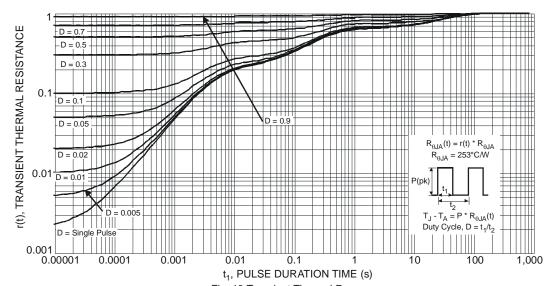
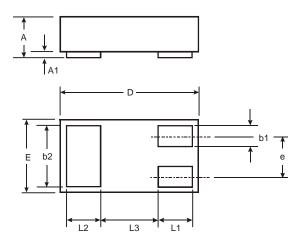


Fig. 13 Transient Thermal Response



Package Outline Dimensions

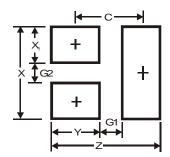
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



X2-DFN1006-3				
Dim	Min	Max	Тур	
Α		0.40	_	
A1	0	0.05	0.03	
b1	0.10	0.20	0.15	
b2	0.45	0.55	0.50	
D	0.95	1.05	1.00	
E	0.55	0.65	0.60	
е	_	_	0.35	
L1	0.20	0.30	0.25	
L2	0.20	0.30	0.25	
L3	_	_	0.40	
All Dimensions in mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	1.1
G1	0.3
G2	0.2
Х	0.7
X1	0.25
Y	0.4
C	0.7



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