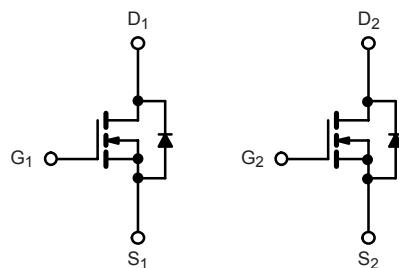
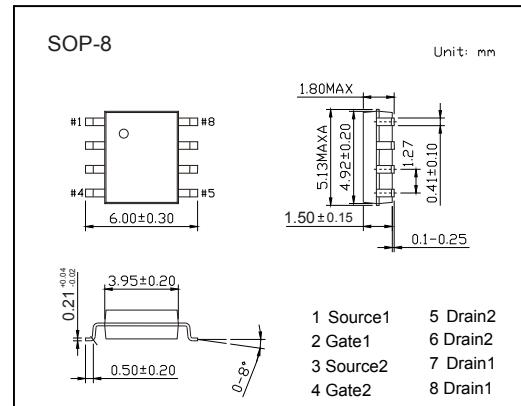


Dual N-Channel MOSFET

SI4946DY (KI4946DY)

■ Features

- $V_{DS} (V) = 60V$
- $I_D = 6.5 A (V_{GS} = 10V)$
- $R_{DS(ON)} < 41m\Omega (V_{GS} = 10V)$
- $R_{DS(ON)} < 52m\Omega (V_{GS} = 4.5V)$
- 175 °C Maximum Junction Temperature



■ Absolute Maximum Ratings Ta = 25°C

| Parameter | Symbol | Rating | Unit |
|---|-------------------|------------|------|
| Drain-Source Voltage | V _{DS} | 60 | V |
| Gate-Source Voltage | V _{GS} | ±20 | |
| Continuous Drain Current | I _D | 6.5 | A |
| | | 5.5 | |
| | | 5.3 | |
| | | 4.4 | |
| Pulsed Drain Current | I _{DM} | 30 | A |
| Avalanche Current | I _{AS} | 12 | |
| Single-Pulse Avalanche Energy | E _{AS} | 7.2 | mJ |
| Power Dissipation | P _D | 3.7 | W |
| | | 2.6 | |
| | | 2.4 | |
| | | 1.7 | |
| Thermal Resistance.Junction- to-Ambient | R _{thJA} | 62.5 | °C/W |
| Thermal Resistance.Junction- to-Case | R _{thJC} | 41 | |
| Junction Temperature | T _J | 150 | °C |
| Storage Temperature Range | T _{stg} | -55 to 150 | |

Dual N-Channel MOSFET

SI4946DY (KI4946DY)

■ Electrical Characteristics $T_a = 25^\circ\text{C}$

| Parameter | Symbol | Test Conditions | Min | Typ | Max | Unit |
|---------------------------------------|---------------------|---|-----|-----|-----------|------------------|
| Drain-Source Breakdown Voltage | V_{DSS} | $I_D=250 \mu\text{A}, V_{GS}=0\text{V}$ | 60 | | | V |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{DS}=60\text{V}, V_{GS}=0\text{V}$ | | | 1 | μA |
| | | $V_{DS}=60\text{V}, V_{GS}=0\text{V}, T_J=55^\circ\text{C}$ | | | 10 | |
| Gate-Body Leakage Current | I_{GSS} | $V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$ | | | ± 100 | nA |
| Gate Threshold Voltage | $V_{GS(\text{th})}$ | $V_{DS}=V_{GS}, I_D=250 \mu\text{A}$ | 1 | | 3 | V |
| Static Drain-Source On-Resistance | $R_{DS(\text{on})}$ | $V_{GS}=10\text{V}, I_D=5.3\text{A}$ (Note.1) | | | 41 | $\text{m}\Omega$ |
| | | $V_{GS}=4.5\text{V}, I_D=4.7\text{A}$ (Note.1) | | | 52 | |
| On State Drain Current | $I_{D(\text{ON})}$ | $V_{GS}=10\text{V}, V_{DS}=5\text{V}$ (Note.1) | 30 | | | A |
| Forward Transconductance | g_{FS} | $V_{DS}=15\text{V}, I_D=5.3\text{A}$ (Note.1) | | 24 | | S |
| Input Capacitance | C_{iss} | | | 840 | | pF |
| Output Capacitance | C_{oss} | $V_{GS}=0\text{V}, V_{DS}=30\text{V}, f=1\text{MHz}$ | | 71 | | |
| Reverse Transfer Capacitance | C_{rss} | | | 44 | | |
| Gate Resistance | R_g | $V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$ | 3.1 | | 9.5 | Ω |
| Total Gate Charge | Q_g | $V_{DS}=30\text{V}, V_{GS}=10\text{V}, I_D=5.3\text{A}$ | | 17 | 25 | nC |
| | | | | | 9.2 | 12 |
| Gate Source Charge | Q_{gs} | $V_{DS}=30\text{V}, V_{GS}=5\text{V}, I_D=5.3\text{A}$ | | | 3.3 | |
| Gate Drain Charge | Q_{gd} | | | | 3.7 | |
| Turn-On Delay Time | $t_{d(on)}$ | | | | 30 | ns |
| Turn-On Rise Time | t_r | $V_{DD}=30\text{V}, R_L=6.8\Omega$ $I_D \approx 4.4\text{A}, V_{GEN}=4.5\text{V}, R_g=1\Omega$ | | | 180 | |
| Turn-Off Delay Time | $t_{d(off)}$ | | | | 30 | |
| Turn-Off Fall Time | t_f | | | | 45 | |
| Turn-On Delay Time | $t_{d(on)}$ | | | | 15 | |
| Turn-On Rise Time | t_r | $V_{DD}=30\text{V}, R_L=6.8\Omega$ $I_D \approx 4.4\text{A}, V_{GEN}=10\text{V}, R_g=1\Omega$ | | | 20 | nC |
| Turn-Off Delay Time | $t_{d(off)}$ | | | | 40 | |
| Turn-Off Fall Time | t_f | | | | 15 | |
| Body Diode Reverse Recovery Time | t_{rr} | | | | 50 | |
| Body Diode Reverse Recovery Charge | Q_{rr} | $I_F=4.4\text{A}, dI/dt=100\text{A}/\mu\text{s}, T_J=25^\circ\text{C}$ | | | 50 | ns |
| Reverse Recovery Fall Time | t_a | | | | 18 | |
| Reverse Recovery Rise Time | t_b | | | | 7 | |
| Maximum Body-Diode Continuous Current | I_S | $T_c=25^\circ\text{C}$ | | | 3.1 | A |
| Pulse Diode Forward Current | I_{SM} | (Note.1) | | | 30 | |
| Diode Forward Voltage | V_{SD} | $I_S=2\text{A}, V_{GS}=0\text{V}$ (Note.1) | | | 1.2 | V |

Note.1: Pulse test; pulse width $\leq 300\text{ us}$, duty cycle $\leq 2\%$.

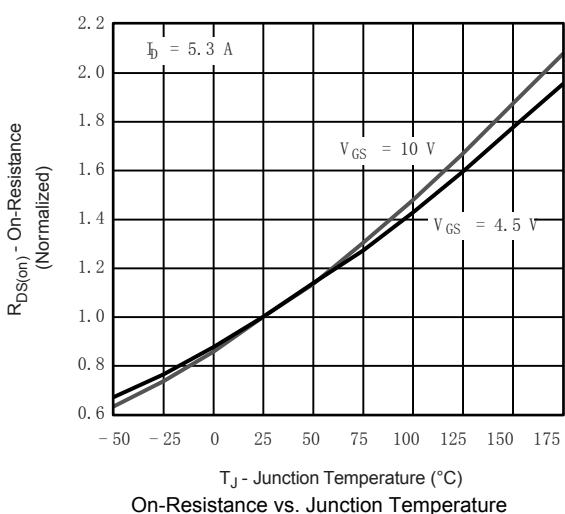
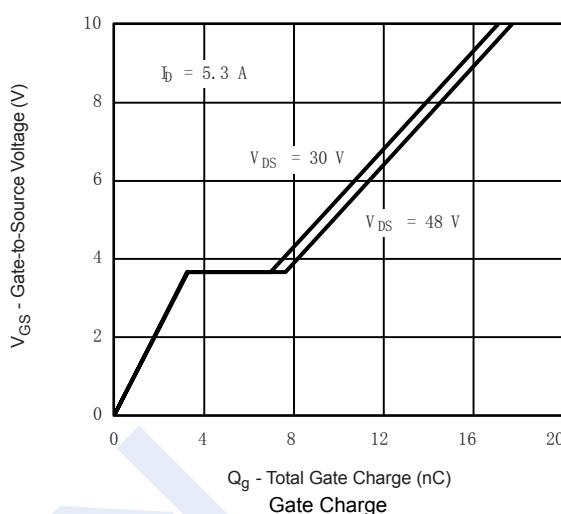
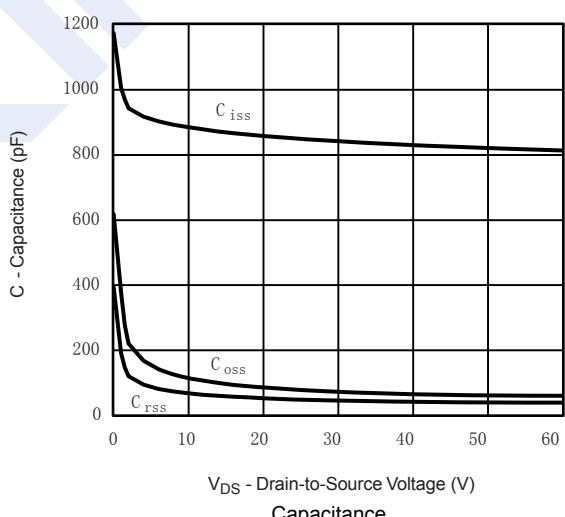
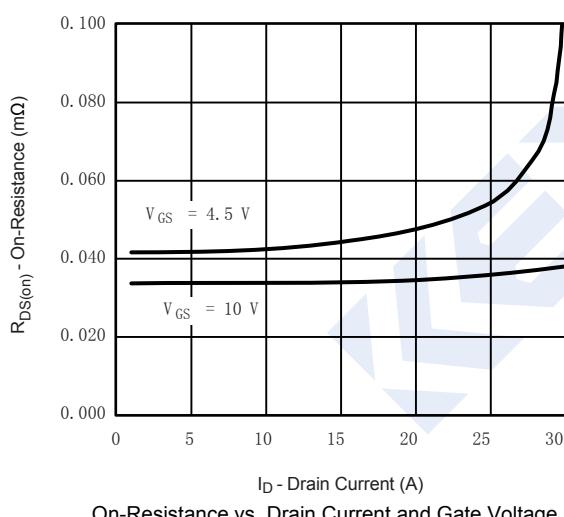
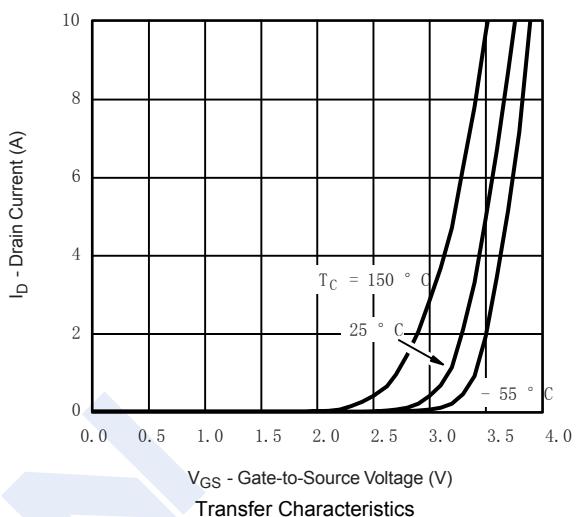
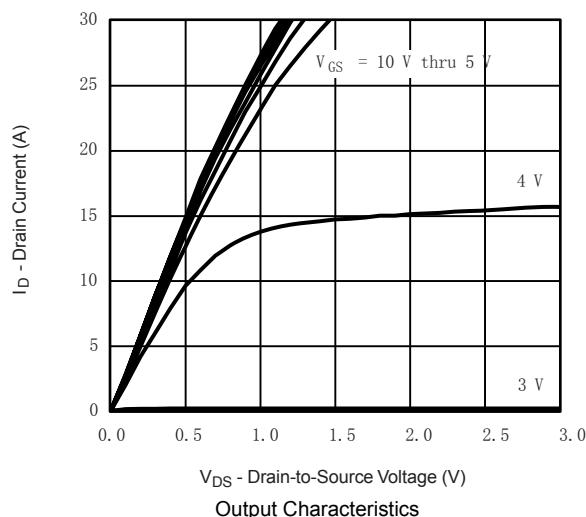
■ Marking

| | |
|---------|----------------|
| Marking | 4946 KA**** |
|---------|----------------|

Dual N-Channel MOSFET

SI4946DY (KI4946DY)

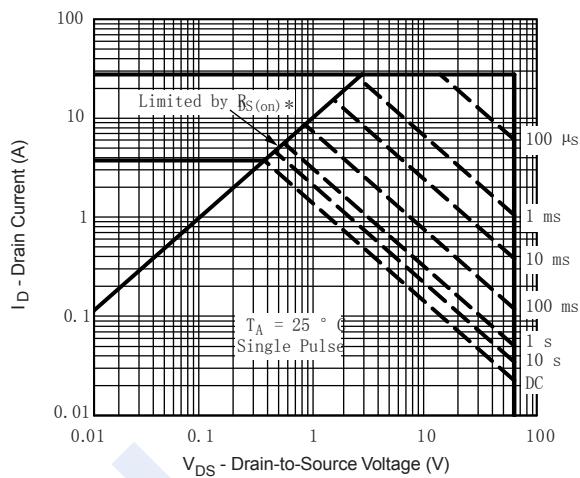
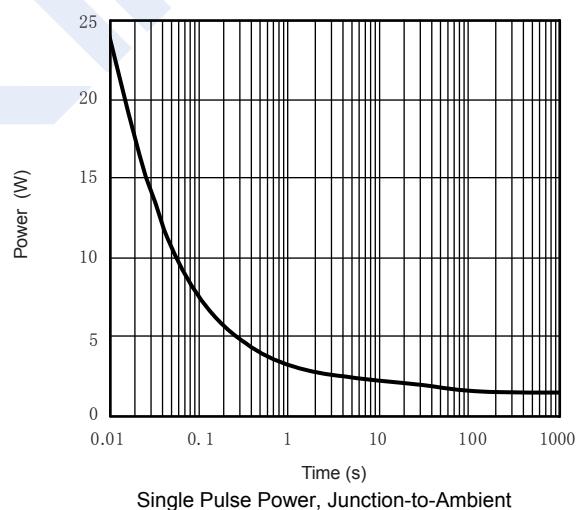
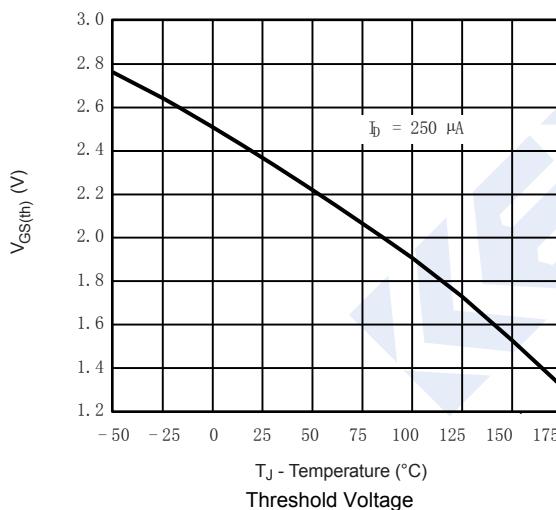
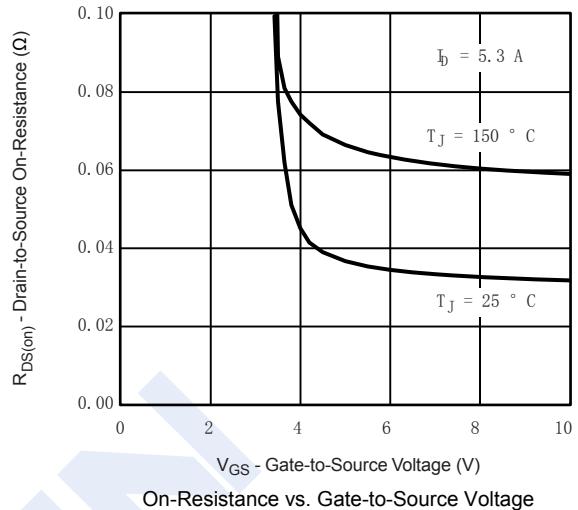
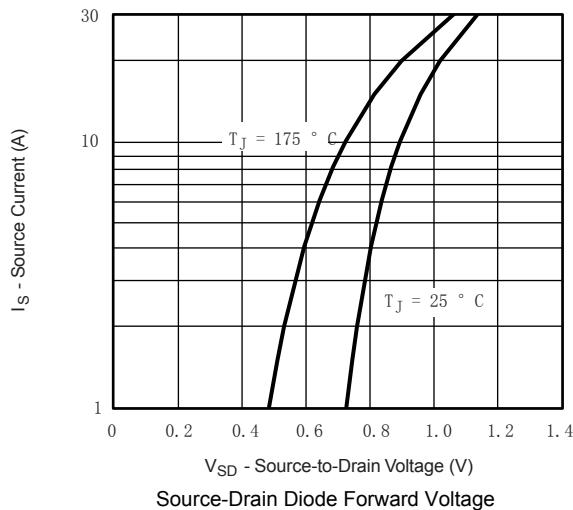
■ Typical Characteristics



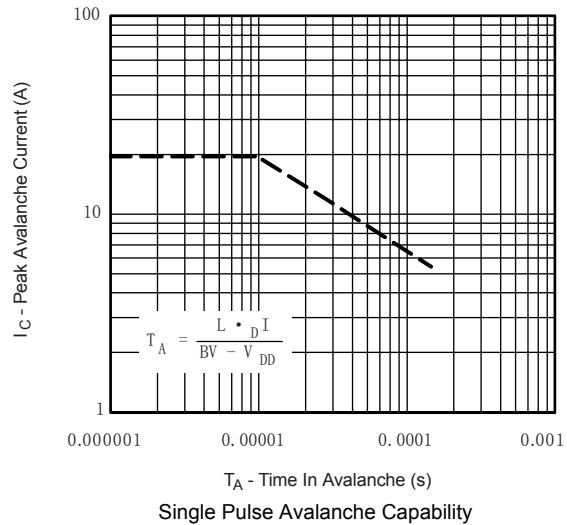
Dual N-Channel MOSFET

SI4946DY (KI4946DY)

■ Typical Characteristics



* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified
Safe Operating Area, Junction-to-Ambient



Dual N-Channel MOSFET

SI4946DY (KI4946DY)

■ Typical Characteristics

