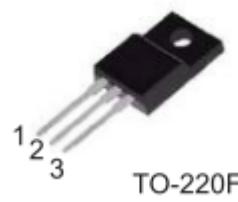
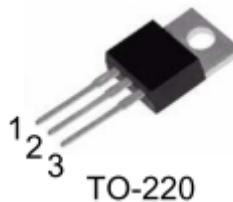
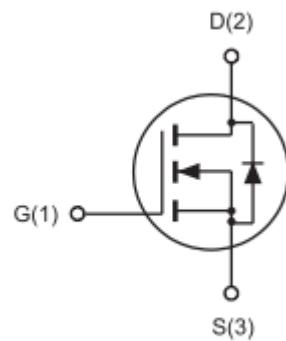


## Features

- ◆ 600V, 5A,  $R_{DS(ON)}$ (Max.) = 2.0Ω @ V<sub>GS</sub> = 10V
- ◆ Low Crss
- ◆ Fast Switching
- ◆ 100 % Avalanche Tested

## Applications

- ◆ Charger
- ◆ STB
- ◆ Open Framed Power Supply



## Absolute Maximum Ratings (T<sub>C</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Limit		Unit
		TO-220	TO-220F	
V <sub>DS</sub>	Drain-Source Voltage <sup>a</sup>	600		V
V <sub>GS</sub>	Gate-Source Voltage	± 30		V
I <sub>D</sub>	Drain Current-Continuous, T <sub>C</sub> = 25°C	5		A
	Drain Current-Continuous, T <sub>C</sub> = 100 °C	3		A
I <sub>DM</sub>	Drain Current-Pulsed <sup>b</sup>	20		A
P <sub>D</sub>	Maximum Power Dissipation @ T <sub>J</sub> = 25 °C	104	35	W
E <sub>AS</sub>	Single Pulsed Avalanche Energy <sup>e</sup>	114		mJ
dv/dt	Peak Diode Recovery dv/dt <sup>c</sup>	4.5		V/ns
T <sub>J</sub> , T <sub>TSG</sub>	Operating and Store Temperature Range	-55 to 150		°C

## Thermal Characteristics

Symbol	Parameter	Value		Unit
R <sub>θ JC</sub>	Thermal Resistance, Junction-Case Max.	1.2	3.6	°C/W
R <sub>θ JA</sub>	Thermal Resistance, Junction-Ambient Max.	63		°C/W

## Electrical Characteristics (T<sub>J</sub> = 25°C unless otherwise noted)

### ■ Off Characteristics

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	600	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V	-	-	20	μA
I <sub>GSSF</sub>	Forward Gate Body Leakage Current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 30V	-	-	100	nA
I <sub>GSSR</sub>	Reverse Gate Body Leakage Current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = -30V	-	-	-100	nA

### ■ On Characteristics

V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2.0	2.9	4.0	V
R <sub>D(on)</sub>	Static Drain-Source On-Resistance <sup>d</sup>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 3A	-	1.6	2	Ω
g <sub>FS</sub>	Forward Transconductance <sup>d</sup>	V <sub>DS</sub> = 15V, I <sub>D</sub> = 2.5A	-	2.2	10	S

### ■ Dynamic Characteristics

C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz	-	798	-	pF
C <sub>oss</sub>	Output Capacitance		-	114	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	25	-	pF

### ■ Switching Characteristics

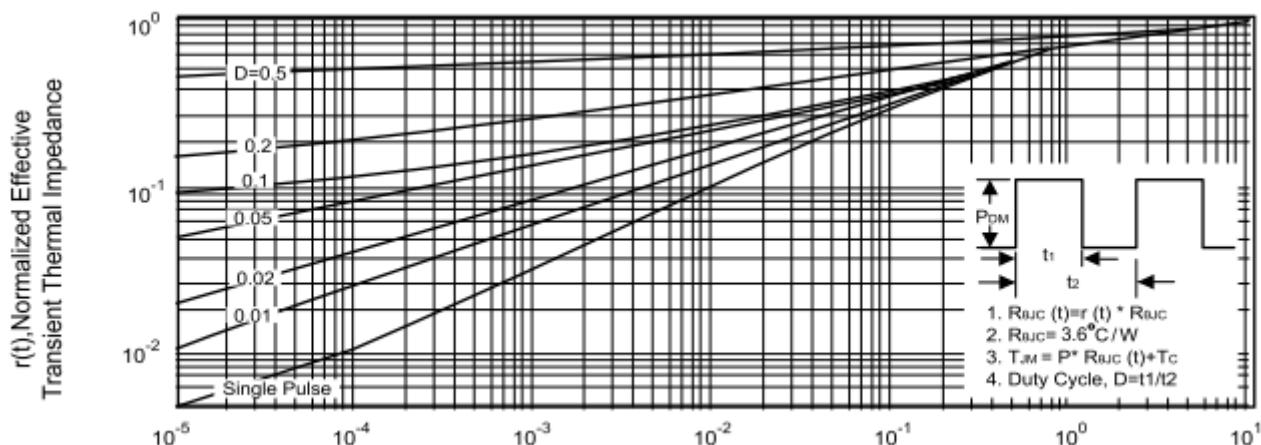
td(on)	Turn-On Delay Time	V <sub>DD</sub> = 300V, I <sub>D</sub> = 5A, RG = 10Ω, V <sub>GS</sub> = 10V	-	25	-	ns
tr	Turn-On Rise Time		-	8.1	-	ns
td(off)	Turn-Off Delay Time		-	38	-	ns
tf	Turn-Off Fall Time		-	7.8	-	ns
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> = 300V, I <sub>D</sub> = 5A, V <sub>GS</sub> = 10V	-	19		nC
Q <sub>gs</sub>	Gate-Source Charge		-	5.8		nC
Q <sub>gd</sub>	Gate-Drain Charge		-	8		nC

## ■ Drain-Source Diode Characteristics

IS	Drain-Source Diode Forward Continuous Current	VGS = 0V	-	-	5	A
ISM	Maximum Pulsed Current	VGS = 0V	-	-	20	A
VSD	Drain-Source Diode Forward Voltage	VGS = 0V, IS = 2.5A	-	0.81	1.5	V
trr	Reverse Recovery time	VGS = 0V, IS = 5A, di/dt = 100A/μs	-	310	-	ns
Qrr	Reverse Recovery Charge		-	2.1	-	μC

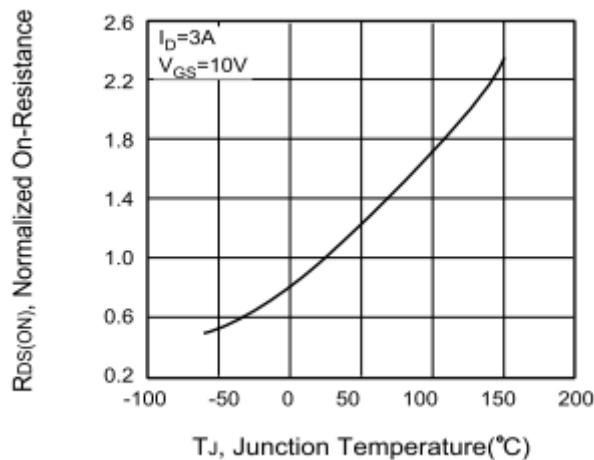
**Notes :**

- a.  $T_J = +25\text{ C}$  to  $+150\text{ C}$ .
- b. Repetitive rating; pulse width limited by maximum junction temperature.
- c.  $I_{SD} = 5.0\text{A}$   $\frac{di}{dt} \leq 100\text{ A/}\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ ,  $T_J \leq +150\text{ C}$ .
- d. Pulse width  $\leq 300\text{ }\mu\text{s}$ ; duty cycle  $\leq 2\%$ .
- e.  $L=10\text{mH}$ ,  $V_{DD} = 50\text{V}$ ,  $I_{AS} = 5\text{A}$ ,  $R_G = 25\Omega$  Starting  $T_J = 25\text{ }^{\circ}\text{C}$ .

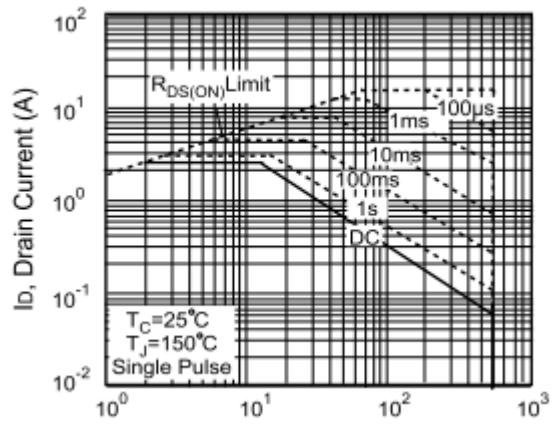


Square Pulse Duration (sec) For EC745N60AF

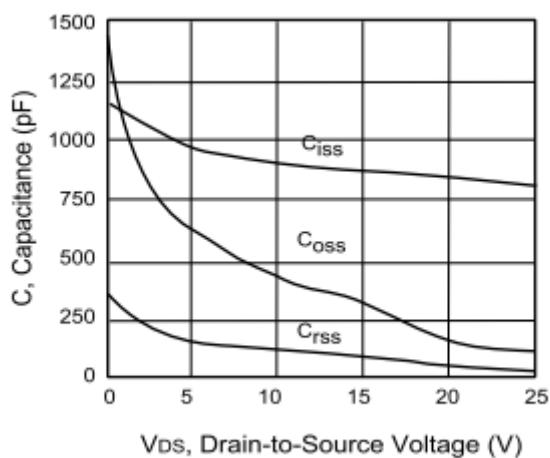
**Figure 1. Normalized Effective Transient Thermal Impedance With Pulse Duration**



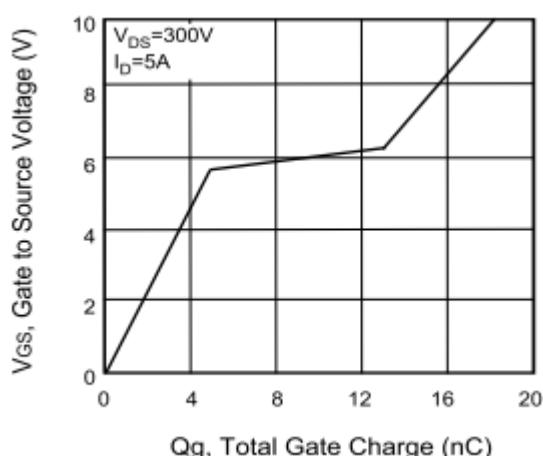
**Figure 2. Normalized On-Resistance Variation with Temperature**



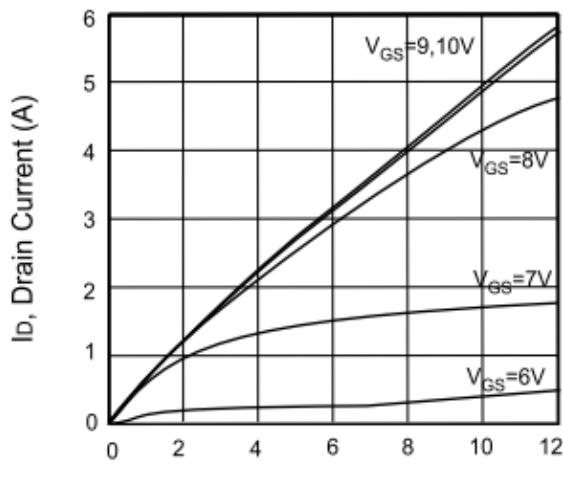
**V<sub>DS</sub>, Drain-Source Voltage (V) for EC745N60AFR**  
**Figure 3 Maximum Safe Operating Area**



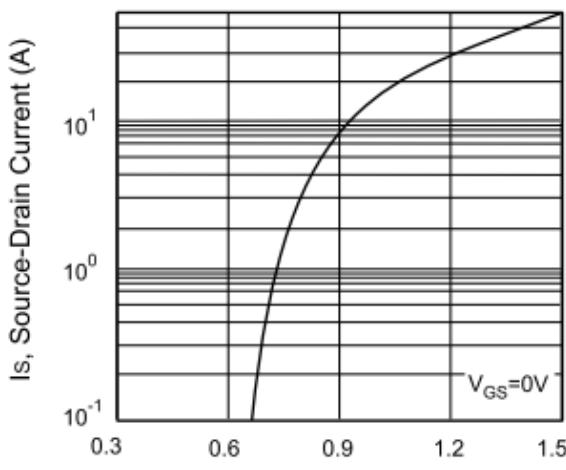
**Figure 4. Capacitance Characteristics**



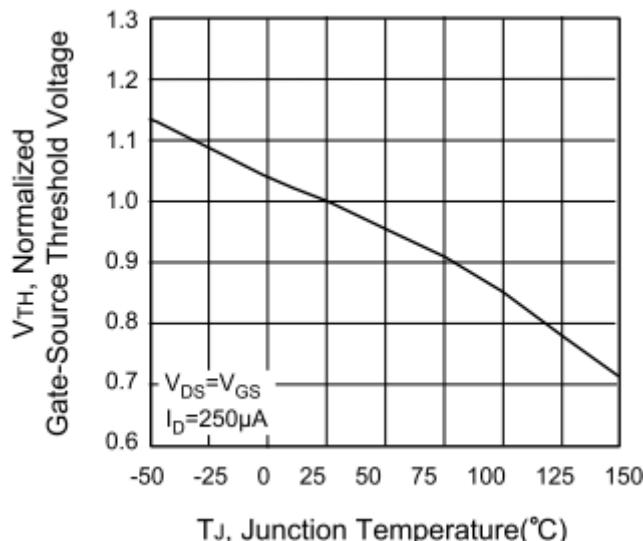
**Figure 5. Gate Charge Characteristics**



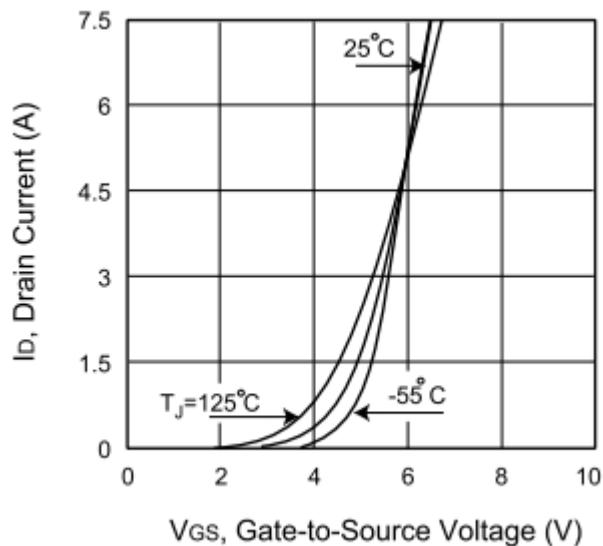
**Figure 6. On-state Characteristics**



**V<sub>SD</sub>, Body Diode Forward Voltage (V)**  
**Figure 7. Body Diode Forward Voltage Variation with Source Current**



**Figure 8.** Gate Threshold Variation With Temperature



**Figure 9.** Transfer Characteristics

## ORDERING INFORMATION

Part Number	Package	Marking	Marking Information
EC745N60AFR	TO-220F-3L	745N60 LLLLL YYWW	1. LLLL : Lot No. 2. YY : Year code 3. WW : Week code
EC745N60AR	TO-220-3L		