New Jersey Semi-Conductor Products, Inc.

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MPF102

Preferred Devices

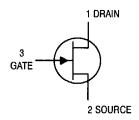
JFET VHF Amplifier

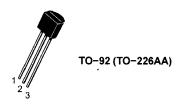
N-Channel - Depletion

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	25	Vdc
Drain - Gate Voltage	V _{DG}	25	Vdc
Gate-Source Voltage	V _{GS}	-25	Vdc
Gate Current	l _G	10	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	350 2.8	mW mW/°C
Junction Temperature Range	TJ	125	°C
Storage Temperature Range	T _{stg}	-65 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.







NJ Semi-Conductors reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However, NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Gate – Source Breakdown Voltage (I _G = -10 µAdc, V _{DS} = 0)	V _(BR) GSS	-25	_	Vdc
Gate Reverse Current $(V_{GS} = -15 \text{ Vdc}, V_{DS} = 0)$ $(V_{GS} = -15 \text{ Vdc}, V_{DS} = 0, T_A = 100^{\circ}\text{C})$	I _{GSS}		-2.0 -2.0	nAdc μAdc
Gate-Source Cutoff Voltage (V _{DS} = 15 Vdc, I _D = 2.0 nAdc)	V _{GS(off)}	_	-8.0	Vdc
Gate-Source Voltage (V _{DS} = 15 Vdc, I _D = 0.2 mAdc)	V_{GS}	-0.5	-7.5	Vdc
ON CHARACTERISTICS				
Zero - Gate - Voltage Drain Current (Note 1) (V _{DS} = 15 Vdc, V _{GS} = 0 Vdc)	I _{DSS}	2.0	20	mAdc
SMALL-SIGNAL CHARACTERISTICS				
Forward Transfer Admittance (Note 1) $(V_{DS} = 15 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ kHz})$ $(V_{DS} = 15 \text{ Vdc}, V_{GS} = 0, f = 100 \text{ MHz})$	y _{fs}	2000 1600	7500 -	μmhos
Input Admittance (V _{DS} = 15 Vdc, V _{GS} = 0, f = 100 MHz)	Re(y _{is})	_	800	μmhos
Output Conductance (V _{DS} = 15 Vdc, V _{GS} = 0, f = 100 MHz)	Re(y _{os})	_	200	μmhos
Input Capacitance (V _{DS} = 15 Vdc, V _{GS} = 0, f = 1.0 MHz)	C _{iss}	_	7.0	pF
Reverse Transfer Capacitance (V _{DS} = 15 Vdc, V _{GS} = 0, f = 1.0 MHz)	C _{rss}	_	3.0	pF

^{1.} Pulse Test; Pulse Width ≤ 630 ms, Duty Cycle ≤ 10%.